

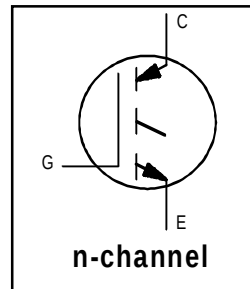
INSULATED GATE BIPOLAR TRANSISTOR

IRG4P254S

Standard Speed IGBT

Features

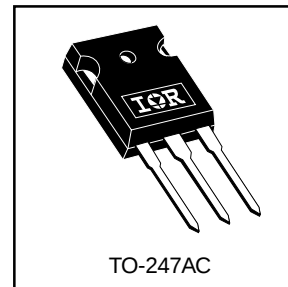
- Standard: Optimized for minimum saturation voltage and operating frequencies up to 10kHz
- Generation 4 IGBT design provides tighter parameter distribution and higher efficiency than Generation 3
- Industry standard TO-247AC package



$V_{CES} = 250V$
$V_{CE(on)} \text{ typ.} = 1.32V$
@ $V_{GE} = 15V, I_C = 55A$

Benefits

- Generation 4 IGBT's offer highest efficiency available
- IGBT's optimized for specified application conditions
- High Power density
- Lower conduction losses than similarly rated MOSFET
- Lower Gate Charge than equivalent MOSFET
- Simple Gate Drive characteristics compared to Thyristors



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Breakdown Voltage	250	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	98*	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	55	
I_{CM}	Pulsed Collector Current ①	196	
I_{LM}	Clamped Inductive Load Current ②	196	
V_{GE}	Gate-to-Emitter Voltage	± 20	V
E_{ARV}	Reverse Voltage Avalanche Energy ③	160	mJ
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	200	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	78	
T_J	Operating Junction and	-55 to + 150	$^\circ C$
T_{STG}	Storage Temperature Range		
	Soldering Temperature, for 10 seconds	300 (0.063 in. (1.6mm) from case)	
	Mounting torque, 6-32 or M3 screw.	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	0.64	$^\circ C/W$
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.24	—	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	—	40	
Wt	Weight	6.0 (0.21)	—	g (oz)

* Package limited to 70A

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Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage	250	—	—	V	$V_{GE} = 0V, I_C = 250\mu A$
$V_{(BR)ECS}$	Emitter-to-Collector Breakdown Voltage ④	18	—	—	V	$V_{GE} = 0V, I_C = 1.0A$
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	—	0.33	—	V/°C	$V_{GE} = 0V, I_C = 1.0mA$
$V_{CE(ON)}$	Collector-to-Emitter Saturation Voltage	—	1.32	1.5	V	$I_C = 55A$ $V_{GE} = 15V$
		—	1.69	—		$I_C = 98A$ See Fig.2, 5
		—	1.31	—		$I_C = 55A, T_J = 150^\circ\text{C}$
$V_{GE(th)}$	Gate Threshold Voltage	3.0	—	6.0		$V_{CE} = V_{GE}, I_C = 250\mu A$
$\Delta V_{GE(th)}/\Delta T_J$	Temperature Coeff. of Threshold Voltage	—	-12	—	mV/°C	$V_{CE} = V_{GE}, I_C = 250\mu A$
g_{fe}	Forward Transconductance ⑤	43	63	—	S	$V_{CE} = 100V, I_C = 55A$
I_{CES}	Zero Gate Voltage Collector Current	—	—	250	μA	$V_{GE} = 0V, V_{CE} = 250V$
		—	—	2.0		$V_{GE} = 0V, V_{CE} = 10V, T_J = 25^\circ\text{C}$
		—	—	1000		$V_{GE} = 0V, V_{CE} = 250V, T_J = 150^\circ\text{C}$
I_{GES}	Gate-to-Emitter Leakage Current	—	—	± 100	nA	$V_{GE} = \pm 20V$

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge (turn-on)	—	200	300	nC	$I_C = 55A$
Q_{ge}	Gate - Emitter Charge (turn-on)	—	29	44		$V_{CC} = 200V$ See Fig. 8
Q_{gc}	Gate - Collector Charge (turn-on)	—	66	99		$V_{GE} = 15V$
$t_{d(on)}$	Turn-On Delay Time	—	40	—	ns	$T_J = 25^\circ\text{C}$ $I_C = 55A, V_{CC} = 200V$ $V_{GE} = 15V, R_G = 5.0\Omega$ Energy losses include "tail" See Fig. 9, 10, 14
t_r	Rise Time	—	44	—		
$t_{d(off)}$	Turn-Off Delay Time	—	270	400		
t_f	Fall Time	—	510	760		
E_{on}	Turn-On Switching Loss	—	0.38	—	mJ	
E_{off}	Turn-Off Switching Loss	—	3.50	—		
E_{is}	Total Switching Loss	—	3.88	5.3		
$t_{d(on)}$	Turn-On Delay Time	—	38	—	ns	$T_J = 150^\circ\text{C}$, $I_C = 55A, V_{CC} = 200V$ $V_{GE} = 15V, R_G = 5.0\Omega$ Energy losses include "tail" See Fig. 11, 14
t_r	Rise Time	—	45	—		
$t_{d(off)}$	Turn-Off Delay Time	—	400	—		
t_f	Fall Time	—	940	—		
E_{is}	Total Switching Loss	—	6.52	—	mJ	
L_E	Internal Emitter Inductance	—	13	—	nH	Measured 5mm from package
C_{ies}	Input Capacitance	—	4500	—	pF	$V_{GE} = 0V$ $V_{CC} = 30V$ See Fig. 7 $f = 1.0MHz$
C_{oes}	Output Capacitance	—	510	—		
C_{res}	Reverse Transfer Capacitance	—	100	—		

Notes:

- ① Repetitive rating; $V_{GE} = 20V$, pulse width limited by max. junction temperature. (See fig. 13b)
- ② $V_{CC} = 80\%(V_{CES})$, $V_{GE} = 20V$, $L = 10\mu H$, $R_G = 5.0\Omega$, (See fig. 13a)
- ③ Repetitive rating; pulse width limited by maximum junction temperature.
- ④ Pulse width $\leq 80\mu s$; duty factor $\leq 0.1\%$.
- ⑤ Pulse width $5.0\mu s$, single shot.

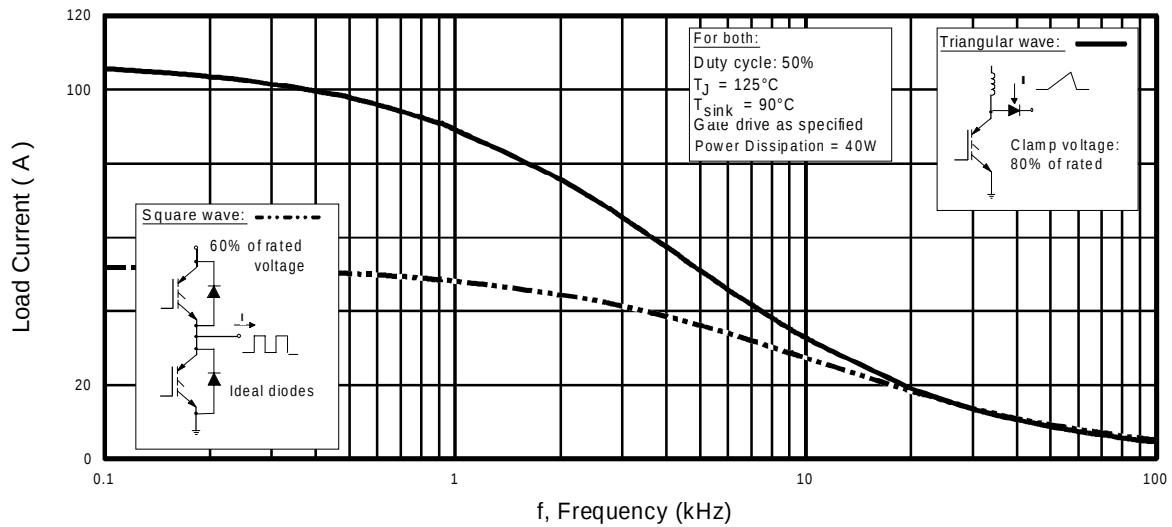


Fig. 1 - Typical Load Current vs. Frequency
(Load Current = I_{RMS} of fundamental)

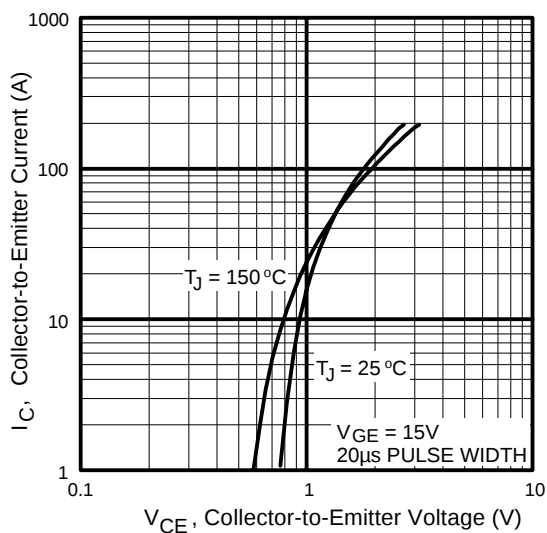


Fig. 2 - Typical Output Characteristics

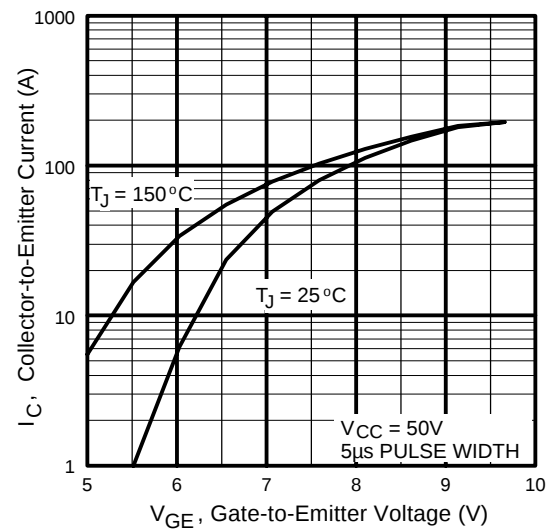


Fig. 3 - Typical Transfer Characteristics

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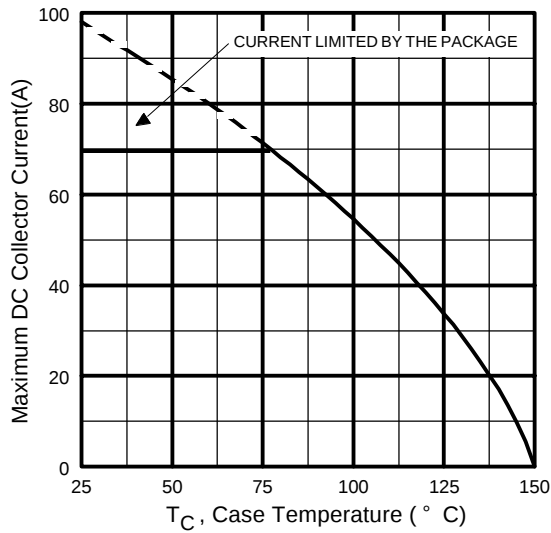


Fig. 4 - Maximum Collector Current vs. Case Temperature

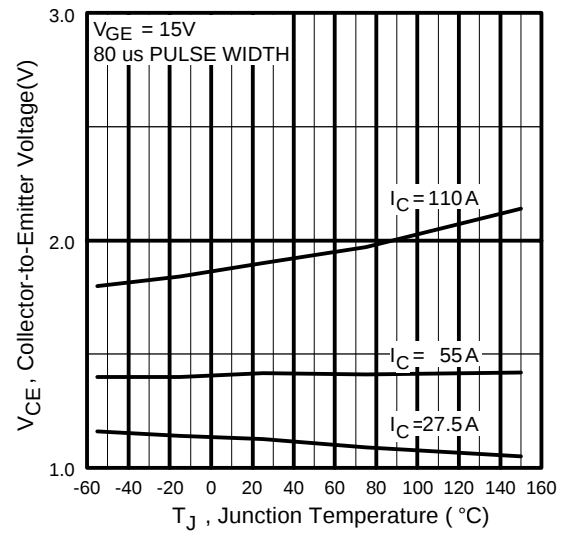


Fig. 5 - Typical Collector-to-Emitter Voltage vs. Junction Temperature

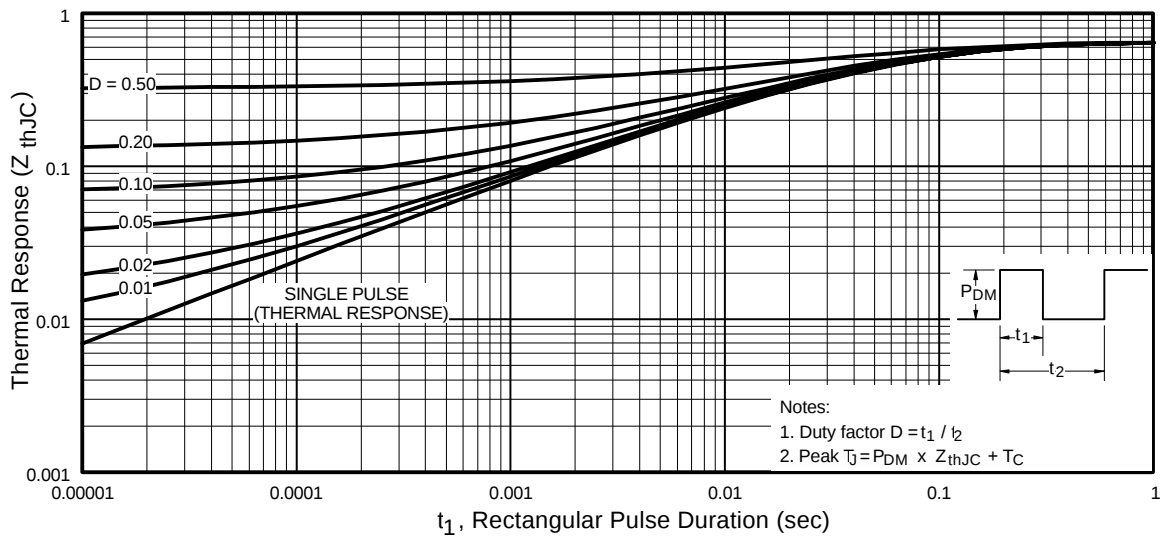


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

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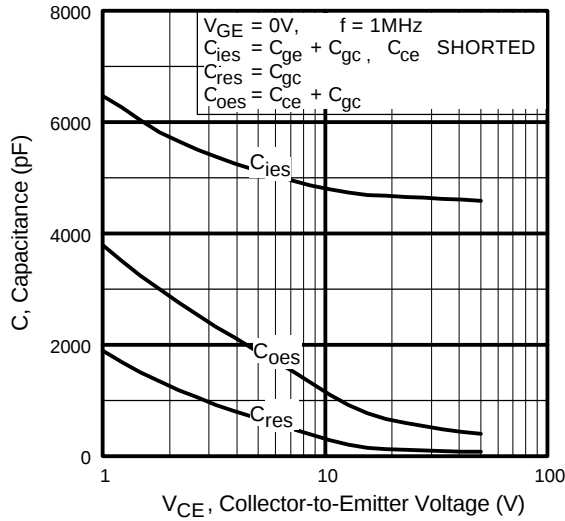


Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

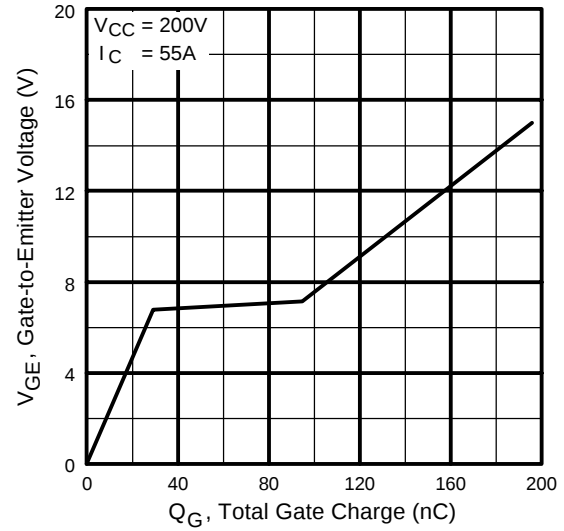


Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage

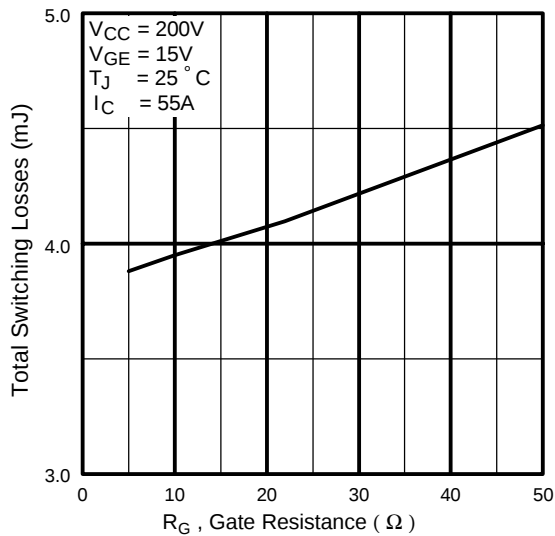


Fig. 9 - Typical Switching Losses vs. Gate Resistance

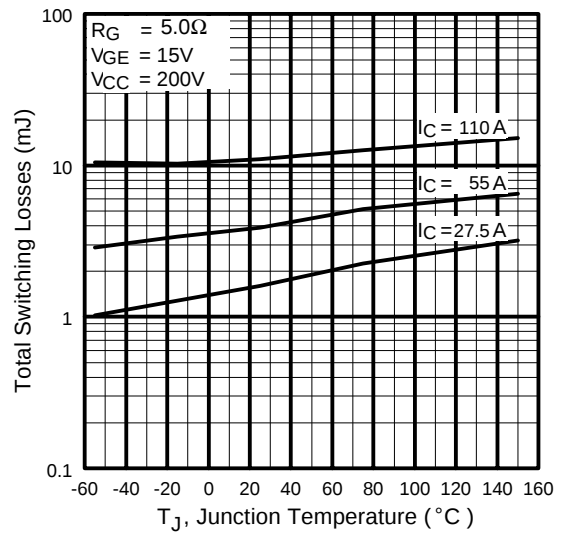


Fig. 10 - Typical Switching Losses vs. Junction Temperature

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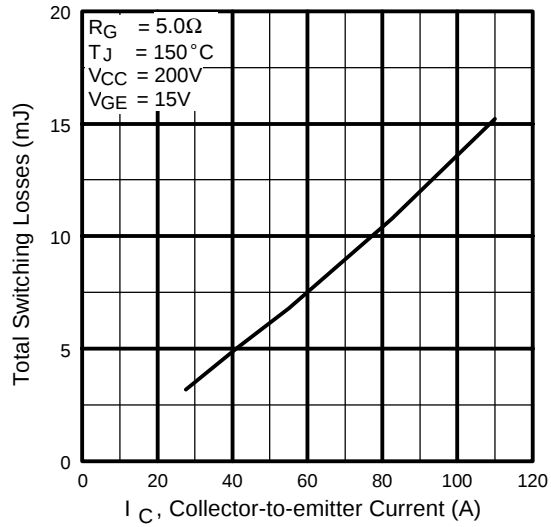


Fig. 11 - Typical Switching Losses vs. Collector-to-Emitter Current

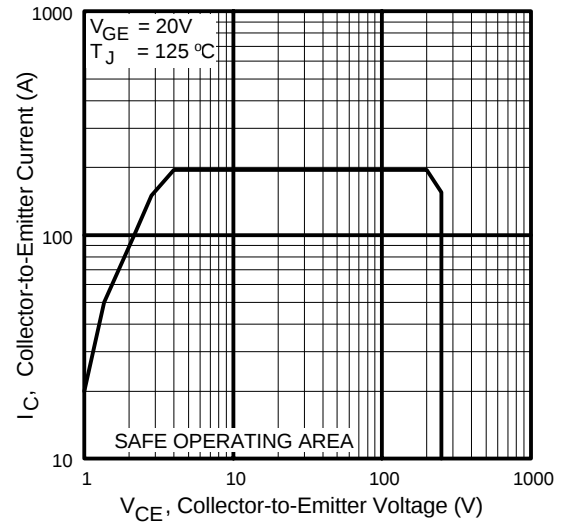


Fig. 12 - Turn-Off SOA

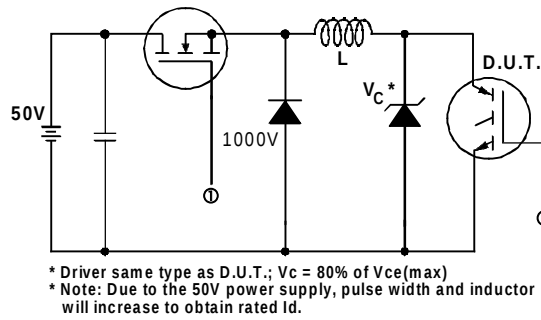


Fig. 13a - Clamped Inductive Load Test Circuit

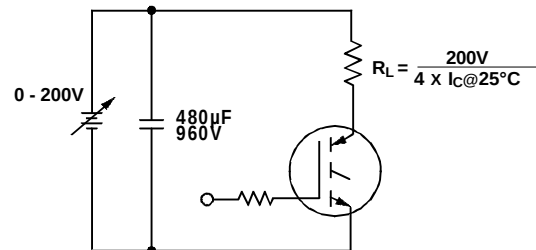


Fig. 13b - Pulsed Collector Current Test Circuit

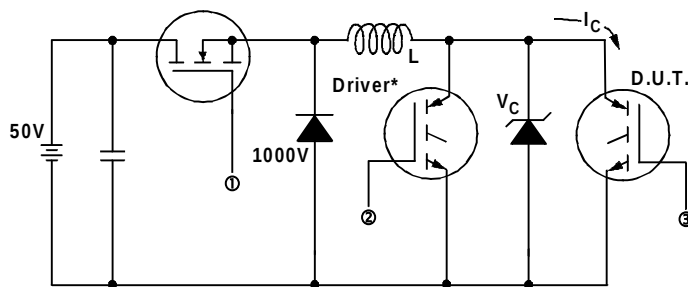


Fig. 14a - Switching Loss Test Circuit

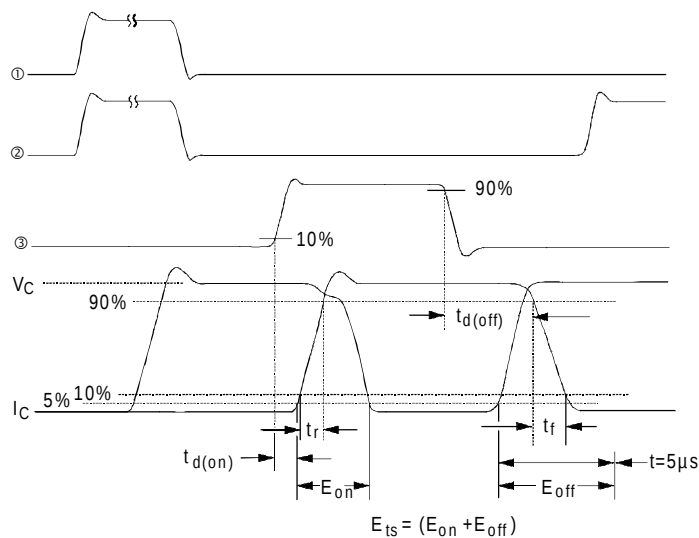


Fig. 14b - Switching Loss Waveforms

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[illegible]

1 DIMENSIONS & TOLERANCING
PER ANSI Y14.5M, 1982.
2 CONTROLLING DIMENSION : INCH.
3 DIMENSIONS ARE SHOWN
MILLIMETERS (INCHES).
4 CONFORMS TO JEDEC OUTLINE
TO-247AC.

1 - GATE
2 - COLLECTOR
3 - EMITTER
4 - COLLECTOR

* LONGER LEADED (20mm)
VERSION AVAILABLE (TO-247AD)
TO ORDER ADD "-E" SUFFIX
TO PART NUMBER

CONFORMS TO JEDEC OUTLINE TO-247AC (TO-3P)
Dimensions in Millimeters and (Inches)

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IR TAIWAN:16 Fl. Suite D, 207, Sec. 2, Tun Haw South Road, Taipei, 10673, Taiwan Tel: 886-2-2377-9936

Data and specifications subject to change without notice. 4/2000

Note: For the most current drawings please refer to the IR website at:
<http://www.irf.com/package/>

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