AC/DC Digital Power Controller for High Power Factor Analog and 0-10V Dimmable LED Drivers

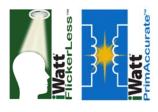


1.0 Features

- Isolated/non-isolated offline 100V_{AC}/277V_{AC} LED driver controller
- Meets IEC61000-3-2 requirements
- Total harmonic distortion < 15% with Power Factor
 > 0.95
- Analog input dimming level control
- Built-in 0-10V isolation transformer driver
- PWM digital interface
- Under 10% output ripple current
- Wide dimming range from 1% to 100%
- Flickerless[™] LED dimming technology
- Quasi-resonant control to achieve high efficiency (typical > 85%)
- Small size
 - » Two-stage topology enables small-size input and output filter capacitors
 - » 200kHz maximum switching frequency enables small transformer
- PrimAccurate[™] primary-side sensing technology eliminates opto-isolator and simplifies design
- Tight LED current regulation
- Supports wide LED output voltage range
- Multiple protection features:
 - » LED open-circuit and short-circuit protection
 - » Over-current protection
 - » Current sense resistor short-circuit protection
 - » AC line over-voltage/frequency protection

2.0 Description

The iW3630 is a two-stage, high-performance AC/DC offline power supply controller for dimmable LED luminaires with analog dimming interface. It controls LED current based on analog input voltage on the dimming pin. It has a



built-in 0-10V dimming interface that works directly with 0-10V dimming systems, eliminating the need for a driver circuit and microcontroller. It also provides a PWM digital interface to support wireless SSL applications, without adding auxiliary power supplies. The iW3630 uses iWatt's unique digital Flickerless technology to eliminate visible flicker in the entire dimming range and to minimize low frequency output ripple current.

The iW3630 operates the main power converter that delivers constant current to the LED load in quasi-resonant mode to provide high power efficiency and minimize electro-magnetic interference (EMI). It uses iWatt's patented PrimAccurate™ primary-side sensing technology to achieve excellent LED current regulation under different AC line and LED load voltages, without using a secondary-side feedback circuit and eliminating the need for an opto-coupler.

The iW3630 minimizes the external components count by simplifying the EMI filter with iWatt's EZ-EMI® technology. Additionally, the digital control loop of the iW3630 maintains stability over all operating conditions without the need for loop compensation components.

3.0 Applications

- Dimmable LED ballast with analog input for dimming control
- Dimmable LED ballast with 0-10V interface for dimming control
- Wireless SSL lighting
- Output power up to 45W



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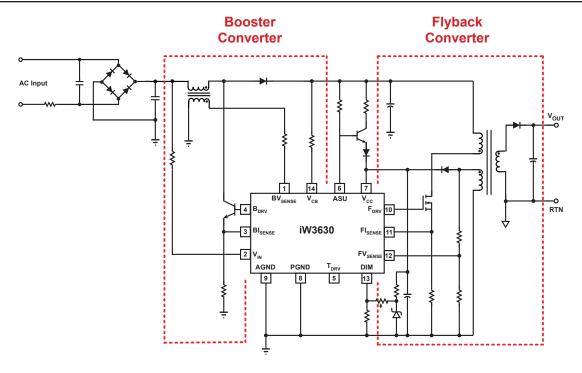


Figure 3.1: iW3630 Analog Dimming Simplified Schematic

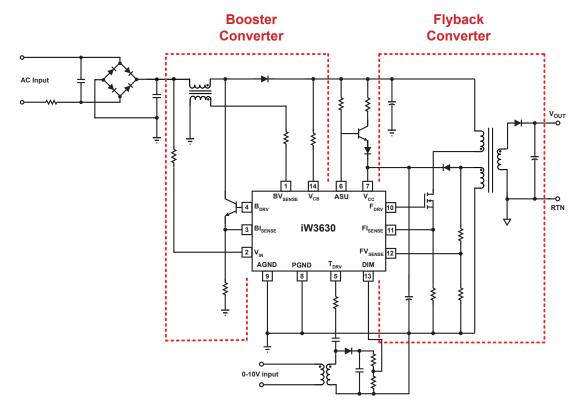


Figure 3.2: iW3630 0-10V Dimming Simplified Schematic

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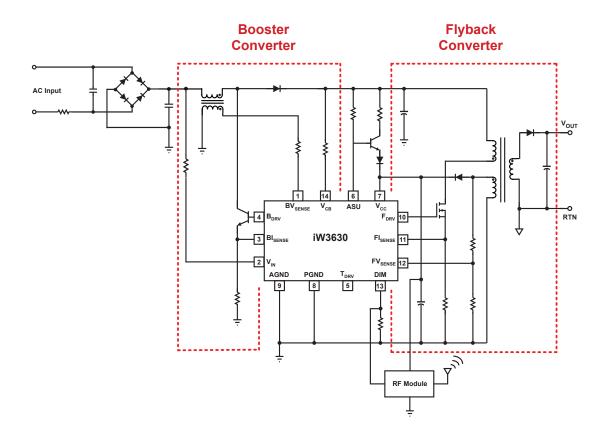


Figure 3.3: iW3630 Wireless Dimming Simplified Schematic

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4.0 Pinout Description

iW3630						
1	BVSENSE	v _{CB}	14			
2	V _{IN}	DIM	13			
3	BISENSE	FV _{SENSE}	12			
4	B _{DRV}	FISENSE	11			
5	T _{DRV}	F_{DRV}	10			
6	ASU	AGND	9			
7	v _{cc}	PGND	8			

Pin#	Name	Туре	Pin Description
1	BV _{SENSE}	Analog Input	Boost inductor voltage feedback input
2	V_{IN}	Analog Input	Rectified AC line voltage input
3	BI _{SENSE}	Analog Input	Boost current sense input
4	B _{DRV}	Output	Base drive output for boost BJT
5	T _{DRV}	Output	0-10V isolation transformer drive output
6	ASU	Output	Active start-up and bleeder control
7	V _{cc}	Power	Power supply for control logic and voltage sense for power-on reset circuit
8	PGND	Ground	Power ground
9	AGND	Ground	Signal ground. It should be connected to the power ground on PCB.
10	F _{DRV}	Output	Gate drive output for flyback MOSFET
11	FI _{SENSE}	Analog Input	Flyback current sense (used for cycle-by-cycle peak current control and limit)
12	FV _{SENSE}	Analog Input	Flyback voltage sense (used for primary-side regulation and ZVS)
13	DIM	Analog Input	Dimming level control input
14	V _{CB}	Analog Input	Boost output voltage feedback input

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5.0 Absolute Maximum Ratings

Absolute maximum ratings are the parameter values or ranges which can cause permanent damage if exceeded. For maximum safe operating conditions, refer to iW3630 Datasheet for more information.

Parameter	Symbol	Value	Units
DC supply voltage range (pin 7, I _{CC} = 20mA max)	V _{CC}	-0.3 to 18	V
DC supply current at V _{CC} pin	I _{cc}	20	mA
F _{DRV} output (pin 10)		-0.3 to 18	V
B _{DRV} output (pin 4)		-0.3 to 4.0	V
T _{DRV} output (pin 5)		-0.3 to 18	V
FV _{SENSE} input (pin 12, I _{FVSENSE} ≤ 10mA)		-0.7 to 4.0	V
BV _{SENSE} input (pin 1, I _{BVSENSE} ≤ 3mA)		-0.7 to 4.0	V
V _{IN} input (pin 2)		-0.3 to 18	V
V _{CB} input (pin 14)		-0.3 to 18	V
FI _{SENSE} input (pin 11)		-0.3 to 4.0	V
BI _{SENSE} input (pin 3)		-0.3 to 4.0	V
ASU output (pin 6)		-0.3 to 18	V
DIM input (pin 13)		-0.3 to 4.0	V
Power dissipation at T _A ≤ 25°C		900	mW
Maximum junction temperature	T _{J MAX}	150	°C
Storage temperature	T _{STG}	-65 to 150	°C
Thermal Resistance Junction-to-PCB Board Surface Temperature	Ψ _{JB} (Note 1)	45	°C/W
ESD rating per JEDEC JESD22-A114		2,000	V
Latch-up test per JEDEC 78		±100	mA

Notes:

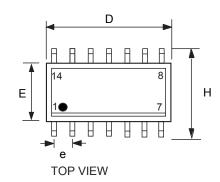
Note 1. ψ_{JB} [Psi Junction to Board] provides an estimation of the die junction temperature relative to the PCB surface temperature. This data is measured at the ground pin (pin 8 and pin 9) without using any thermal adhesives.

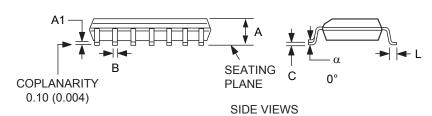
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6.0 Physical Dimensions

14-Lead SOIC Package





Symbol Inches Millimeters MIN MAX MIN MAX Α 0.053 0.069 1.35 1.75 0.004 Α1 0.010 0.10 0.25 В 0.013 0.020 0.51 0.33 С 0.007 0.010 0.25 0.19 D 0.337 0.344 8.55 8.75 Ε 0.150 0.157 3.80 4.00 0.050 BSC 1.27 BSC е 0.228 Н 0.244 5.80 6.20 Ν 0.086 0.094 2.18 2.39 Μ 0.126 3.00 0.118 3.20 L 0.016 0.050 0.40 1.27 0° 8° ٥° 8° α

Figure 6.1: Physical dimensions, 14-lead SOIC package

Compliant to JEDEC Standard MS12F

Controlling dimensions are in inches; millimeter dimensions are for reference only

This product is RoHS compliant and Halide free.

Soldering Temperature Resistance:

- [a] Package is IPC/JEDEC Std 020D Moisture Sensitivity Level 1
- [b] Package exceeds JEDEC Std No. 22-A111 for Solder Immersion Resistance; package can withstand 10 s immersion < 270°C</p>

Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15 mm per end. Dimension E does not include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.25 mm per side.

The package top may be smaller than the package bottom. Dimensions D and E are determined at the outermost extremes of the plastic bocy exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.

7.0 Ordering Information

Part Number	Options	Package	Description
iW3630-00	Universal input, 0-10V dimmable	SOIC-14	Tape & Reel ¹

Note 1: Tape & Reel packing quantity is 2,500/reel.

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