

# International Rectifier

## HEXFET® POWER MOSFET

**JANTX2N6788**
**JANTXV2N6788**
**[REF:MIL-PRF-19500/555]**
**[GENERIC:IRFF120]**
**N-CHANNEL**

### 100 Volt, 0.30Ω HEXFET

HEXFET technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry achieves very low on-state resistance combined with high transconductance. HEXFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and electrical parameter temperature stability. They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers, and high energy pulse circuits, and virtually any application where high reliability is required.

### Product Summary

| Part Number  | BV <sub>DSS</sub> | R <sub>DS(on)</sub> | I <sub>D</sub> |
|--------------|-------------------|---------------------|----------------|
| JANTX2N6788  | 100V              | 0.30Ω               | 6.0A           |
| JANTXV2N6788 |                   |                     |                |

### Features:

- Avalanche Energy Rating
- Dynamic dv/dt Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed

### Absolute Maximum Ratings

|                                                                | Parameter                   | JANTX2N6788, JANTXV2N6788                          | Units |
|----------------------------------------------------------------|-----------------------------|----------------------------------------------------|-------|
| I <sub>D</sub> @ V <sub>GS</sub> = 10V, T <sub>C</sub> = 25°C  | Continuous Drain Current    | 6.0                                                | A     |
| I <sub>D</sub> @ V <sub>GS</sub> = 10V, T <sub>C</sub> = 100°C | Continuous Drain Current    | 3.5                                                |       |
| I <sub>DM</sub>                                                | Pulsed Drain Current ①      | 24                                                 |       |
| P <sub>D</sub> @ T <sub>C</sub> = 25°C                         | Max. Power Dissipation      | 20                                                 | W     |
|                                                                | Linear Derating Factor      | 0.16                                               | W/K ⑤ |
| V <sub>GS</sub>                                                | Gate-to-Source Voltage      | ±20                                                | V     |
| dv/dt                                                          | Peak Diode Recovery dv/dt ③ | 5.5                                                | V/ns  |
| T <sub>J</sub>                                                 | Operating Junction          | -55 to 150                                         | °C    |
| T <sub>STG</sub>                                               | Storage Temperature Range   |                                                    |       |
|                                                                | Lead Temperature            | 300 (0.063 in. (1.6mm) from case for 10.5 seconds) |       |
|                                                                | Weight                      | 0.98 (typical)                                     | g     |

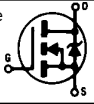


## JANTX2N6788, JANTXV2N6788 Device

### Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (Unless Otherwise Specified)

|                              | Parameter                                    | Min. | Typ. | Max.  | Units              | Test Conditions                                                                                                                                                           |
|------------------------------|----------------------------------------------|------|------|-------|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| $BV_{DSS}$                   | Drain-to-Source Breakdown Voltage            | 100  | —    | —     | V                  | $V_{GS} = 0V$ , $I_D = 1.0\text{ mA}$                                                                                                                                     |
| $\Delta BV_{DSS}/\Delta T_j$ | Temperature Coefficient of Breakdown Voltage | —    | 0.10 | —     | $V/^\circ\text{C}$ | Reference to $25^\circ\text{C}$ , $I_D = 1.0\text{ mA}$                                                                                                                   |
| $R_{DS(on)}$                 | Static Drain-to-Source On-State Resistance   | —    | —    | 0.30  | $\Omega$           | $V_{GS} = 10V$ , $I_D = 3.5A$ ④                                                                                                                                           |
|                              |                                              | —    | —    | 0.345 |                    | $V_{GS} = 10V$ , $I_D = 6.0A$                                                                                                                                             |
| $V_{GS(th)}$                 | Gate Threshold Voltage                       | 2.0  | —    | 4.0   | V                  | $V_{DS} = V_{GS}$ , $I_D = 250\mu A$                                                                                                                                      |
| $g_{fs}$                     | Forward Transconductance                     | 1.5  | —    | —     | S (r)              | $V_{DS} > 15V$ , $I_{DS} = 3.5A$ ④                                                                                                                                        |
| $I_{DSS}$                    | Zero Gate Voltage Drain Current              | —    | —    | 25    | $\mu A$            | $V_{DS} = 0.8 \times \text{Max Rating}$ , $V_{GS} = 0V$                                                                                                                   |
|                              |                                              | —    | —    | 250   |                    | $V_{DS} = 0.8 \times \text{Max Rating}$ , $V_{GS} = 0V$ , $T_j = 125^\circ\text{C}$                                                                                       |
| $I_{GSS}$                    | Gate-to-Source Leakage Forward               | —    | —    | 100   | nA                 | $V_{GS} = 20V$                                                                                                                                                            |
| $I_{GSS}$                    | Gate-to-Source Leakage Reverse               | —    | —    | -100  |                    | $V_{GS} = -20V$                                                                                                                                                           |
| $Q_g$                        | Total Gate Charge                            | 7.7  | —    | 17    | nC                 | $V_{GS} = 10V$ , $I_D = 6.0A$                                                                                                                                             |
| $Q_{gs}$                     | Gate-to-Source Charge                        | 0.7  | —    | 4.0   |                    | $V_{DS} = \text{Max. Rating} \times 0.5$                                                                                                                                  |
| $Q_{gd}$                     | Gate-to-Drain ("Miller") Charge              | 2.0  | —    | 7.7   |                    | see figures 6 and 13                                                                                                                                                      |
| $t_{d(on)}$                  | Turn-On Delay Time                           | —    | —    | 40    | ns                 | $V_{DD} = 50V$ , $I_D = 6.0A$ ,<br>$R_G = 7.5\Omega$ , $V_{GS} = 10V$                                                                                                     |
| $t_r$                        | Rise Time                                    | —    | —    | 70    |                    |                                                                                                                                                                           |
| $t_{d(off)}$                 | Turn-Off Delay Time                          | —    | —    | 40    |                    |                                                                                                                                                                           |
| $t_f$                        | Fall Time                                    | —    | —    | 70    |                    | see figure 10                                                                                                                                                             |
| $L_D$                        | Internal Drain Inductance                    | —    | 5.0  | —     | nH                 | Measured from the drain lead, 6mm (0.25 in.) from package to center of die.<br>Modified MOSFET symbol showing the internal inductances.                                   |
| $L_S$                        | Internal Source Inductance                   | —    | 15   | —     |                    | Measured from the source lead, 6mm (0.25 in.) from package to source bonding pad.<br> |
| $C_{iss}$                    | Input Capacitance                            | —    | 350  | —     | pF                 | $V_{GS} = 0V$ , $V_{DS} = 25V$                                                                                                                                            |
| $C_{oss}$                    | Output Capacitance                           | —    | 150  | —     |                    | $f = 1.0\text{ MHz}$                                                                                                                                                      |
| $C_{rss}$                    | Reverse Transfer Capacitance                 | —    | 24   | —     |                    | see figure 5                                                                                                                                                              |

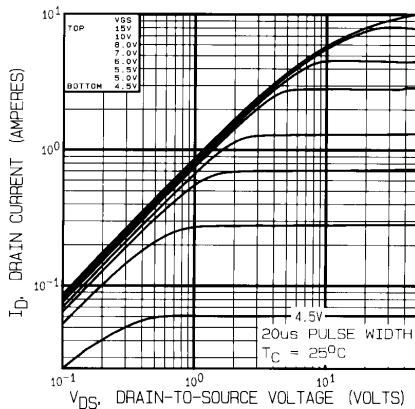
### Source-Drain Diode Ratings and Characteristics

|          | Parameter                              | Min.                                                                                             | Typ. | Max. | Units   | Test Conditions                                                                                                                                                      |
|----------|----------------------------------------|--------------------------------------------------------------------------------------------------|------|------|---------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| $I_S$    | Continuous Source Current (Body Diode) | —                                                                                                | —    | 6.0  | A       | Modified MOSFET symbol showing the integral reverse p-n junction rectifier.<br> |
| $I_{SM}$ | Pulse Source Current (Body Diode) ①    | —                                                                                                | —    | 24   |         |                                                                                                                                                                      |
| $V_{SD}$ | Diode Forward Voltage                  | —                                                                                                | —    | 1.8  | V       | $T_j = 25^\circ\text{C}$ , $I_S = 6.0A$ , $V_{GS} = 0V$ ④                                                                                                            |
| $t_{rr}$ | Reverse Recovery Time                  | —                                                                                                | —    | 240  | ns      | $T_j = 25^\circ\text{C}$ , $I_F = 6.0A$ , $di/dt \leq 100A/\mu s$                                                                                                    |
| $Q_{RR}$ | Reverse Recovery Charge                | —                                                                                                | —    | 2.0  | $\mu C$ | $V_{DD} \leq 50V$ ④                                                                                                                                                  |
| $t_{on}$ | Forward Turn-On Time                   | Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $L_S + L_D$ . |      |      |         |                                                                                                                                                                      |

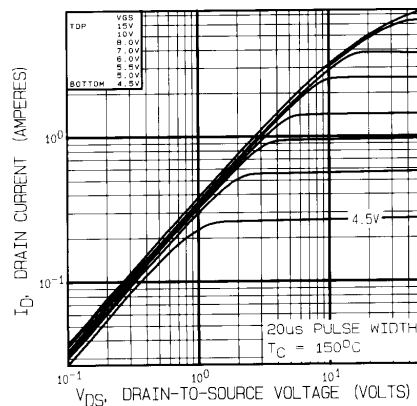
### Thermal Resistance

|            | Parameter           | Min. | Typ. | Max. | Units | Test Conditions      |
|------------|---------------------|------|------|------|-------|----------------------|
| $R_{thJC}$ | Junction-to-Case    | —    | —    | 6.25 | K/W   |                      |
| $R_{thJA}$ | Junction-to-Ambient | —    | —    | 175  |       | Typical socket mount |

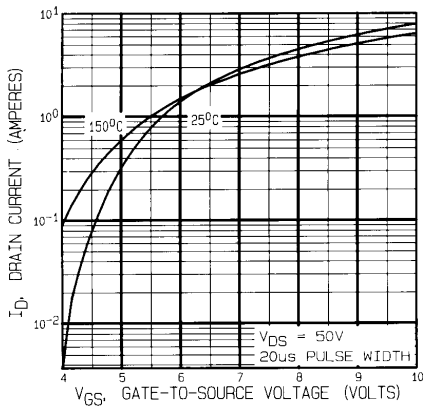
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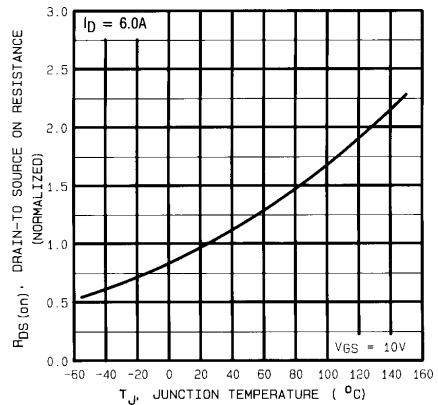
**Fig. 1 — Typical Output Characteristics**  
 $T_C = 25^\circ\text{C}$



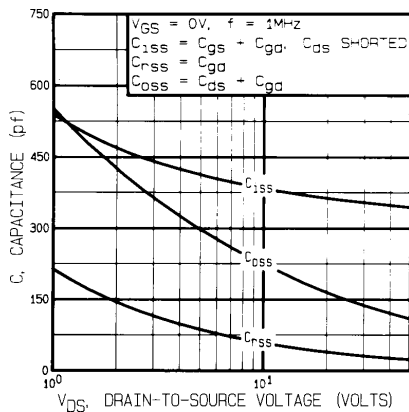
**Fig. 2 — Typical Output Characteristics**  
 $T_C = 150^\circ\text{C}$



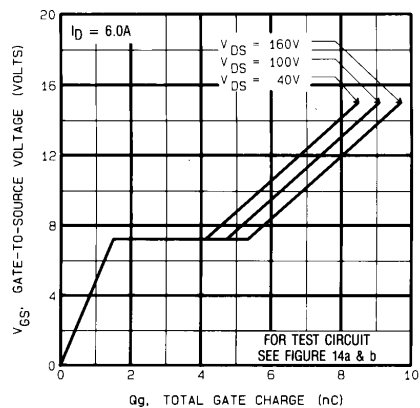
**Fig. 3 — Typical Transfer Characteristics**



**Fig. 4 — Normalized On-Resistance Vs. Temperature**



**Fig. 5 — Typical Capacitance Vs. Drain-to-Source Voltage**



**Fig. 6 — Typical Gate Charge Vs. Gate-to-Source Voltage**

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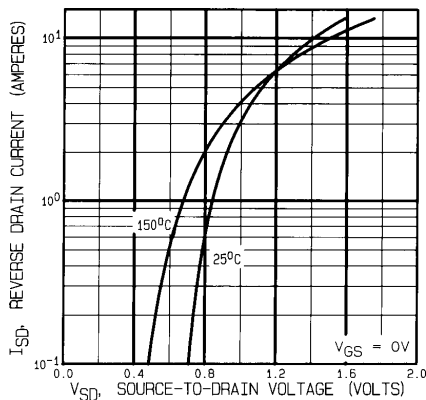


Fig. 7 — Typical Source-to-Drain Diode Forward Voltage

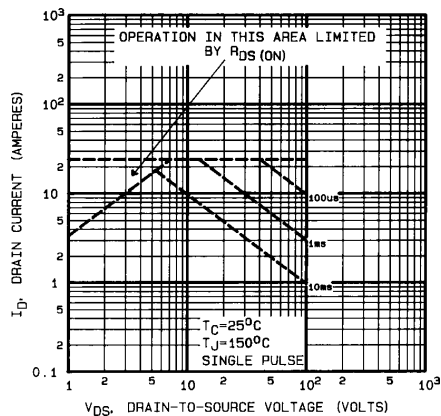


Fig. 8 — Maximum Safe Operating Area

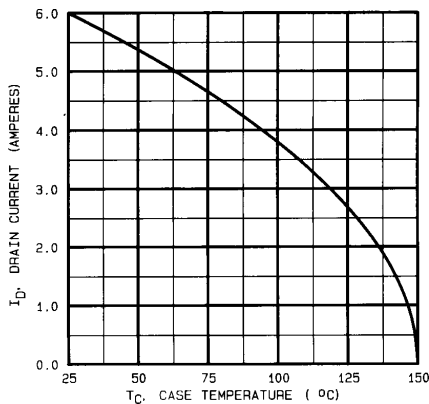


Fig. 9 — Maximum Drain Current Vs. Case Temperature

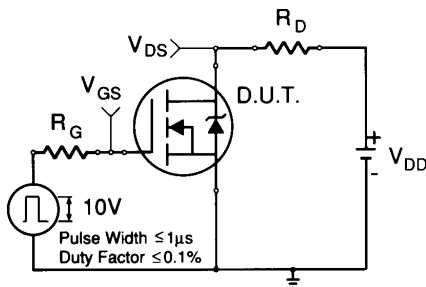


Fig. 10a — Switching Time Test Circuit

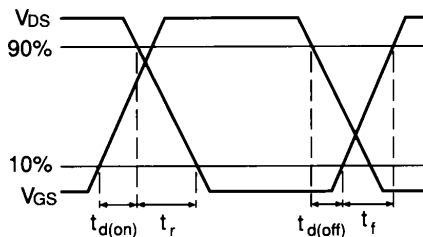


Fig. 10b — Switching Time Waveforms

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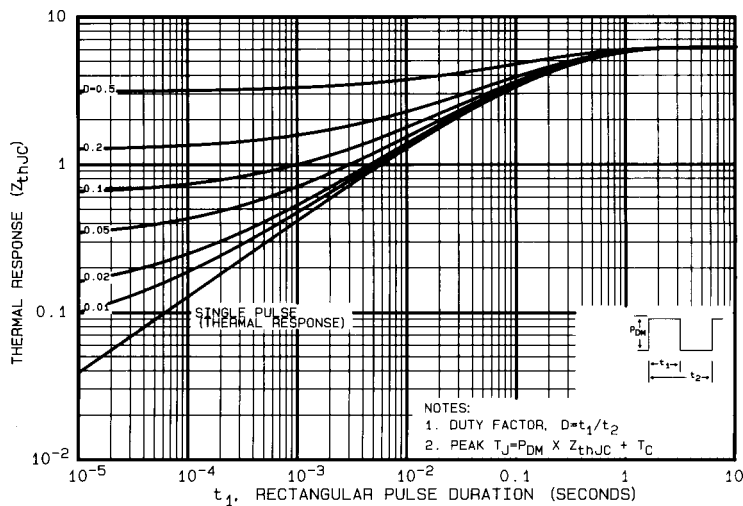


Fig. 11 — Maximum Effective Transient Thermal Impedance, Junction-to-Case Vs. Pulse Duration

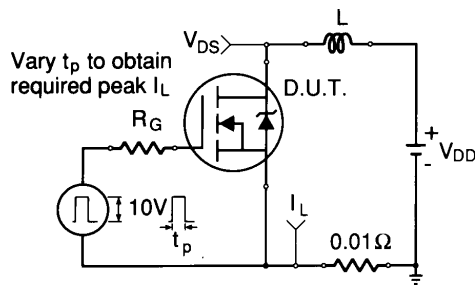


Fig. 12a — Unclamped Inductive Test Circuit

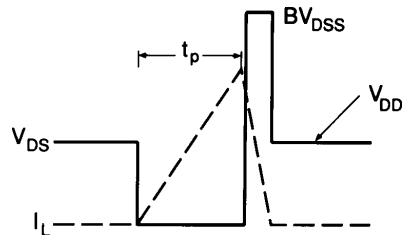


Fig. 12b — Unclamped Inductive Waveforms

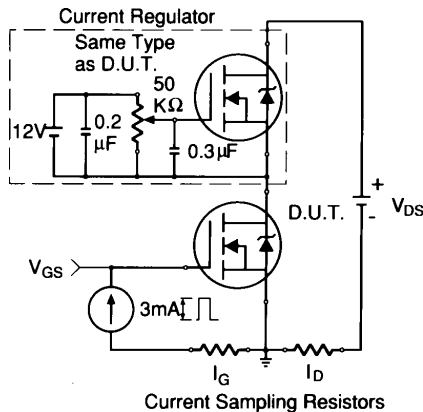


Fig. 13a — Gate Charge Test Circuit

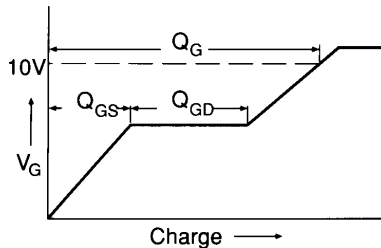
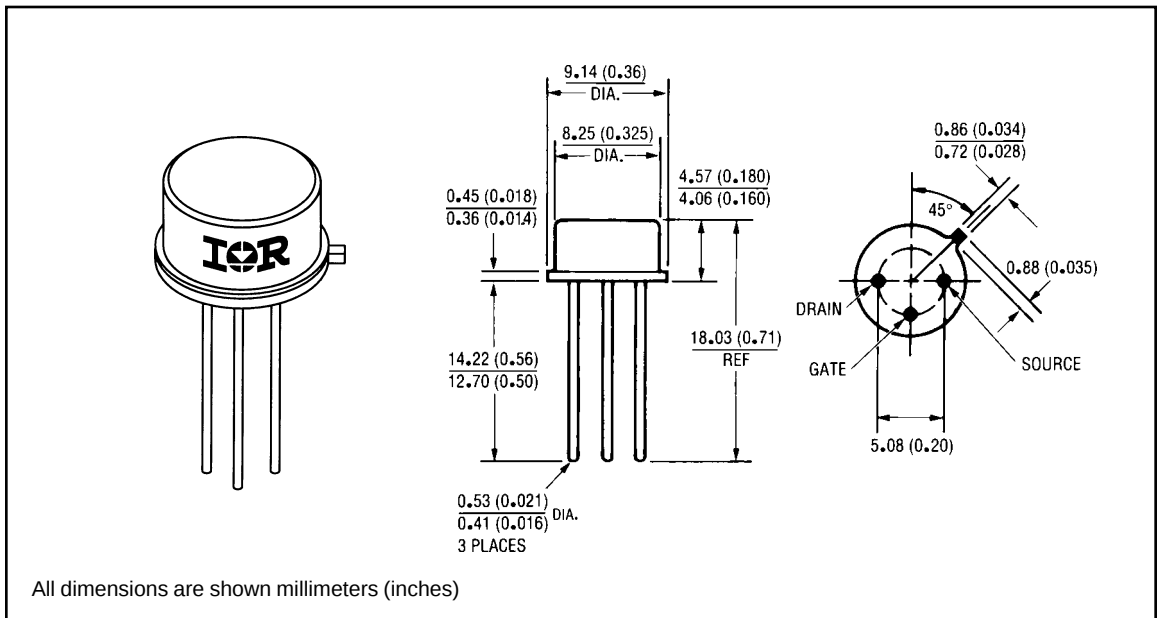


Fig. 13b — Basic Gate Charge Waveform

## JANTX2N6788, JANTXV2N6788 Device

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.  
(see figure 11)
- ② @  $V_{DD} = 25V$ , Starting  $T_J = 25^\circ C$ ,  
 $EAS = [0.5 * L * (I_L^2) * [BV_{DSS}/(BV_{DSS}-V_{DD})]$   
 Peak  $I_L = 6.0A$ ,  $V_{GS} = 10V$ ,  $25 \leq R_G \leq 200\Omega$
- ③  $I_{SD} \leq 6.0A$ ,  $di/dt \leq 110A/\mu s$ ,  
 $V_{DD} \leq BV_{DSS}$ ,  $T_J \leq 150^\circ C$
- ④ Pulse width  $\leq 300 \mu s$ ; Duty Cycle  $\leq 2\%$
- ⑤  $K/W = ^\circ C/W$   
 $W/K = W/^\circ C$

### Case Outline and Dimensions — TO-205AF (Modified TO-39)



International  
**IOR** Rectifier

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