

POWER SUPPLY

PWR_SE	Power Source
1-2	J-Link
3-4	External

The diagram illustrates the power supply circuit for the GYX-SD-TQ8065URK (RED) module. It features a power input section with a diode (D1) and a switch (PWR_SEL) that selects between a J-Link power source (pins 1-2) and an external power source (pins 3-4). The selected power source is connected to a voltage divider (R1, R2) and a 3.3V regulator (VR1). The regulator output is connected to a 3.3V output (PWR). The circuit also includes several decoupling capacitors (C1, C2, C3, C4, C5, C6) to filter the power supply noise.

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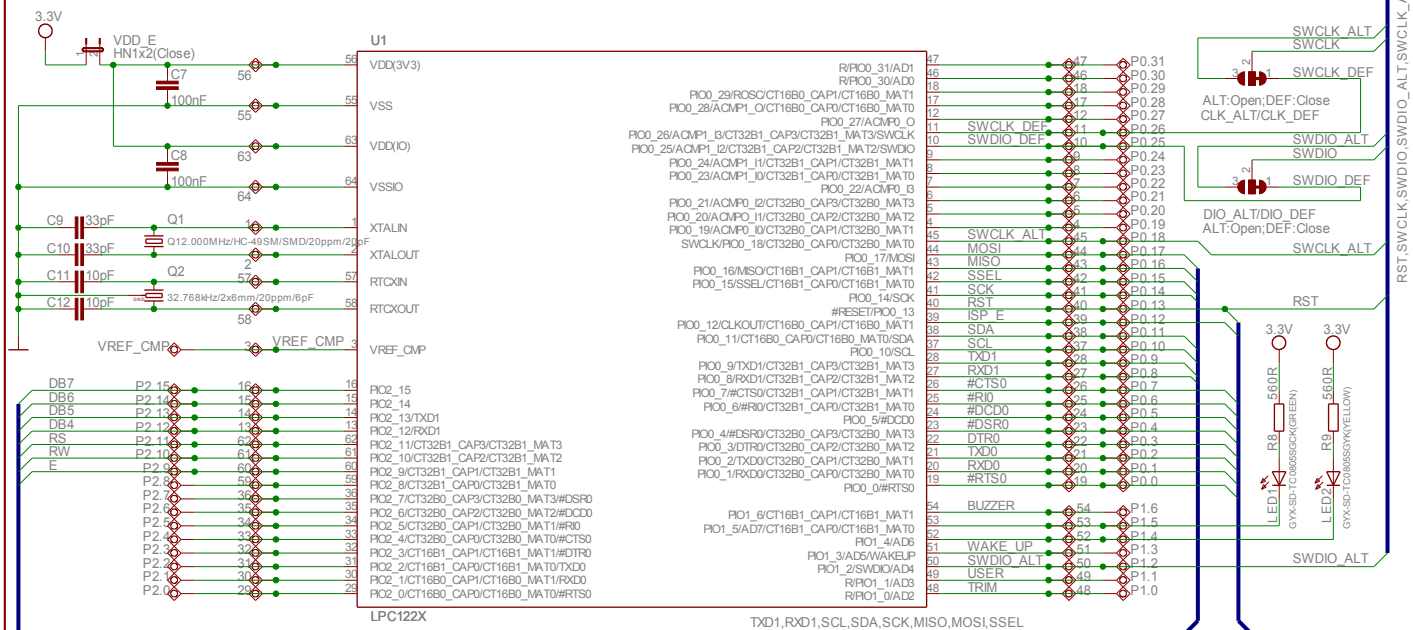
The diagram illustrates the power supply circuit for the GYX-SD-TQ8065URK (RED) module. It features a PWR_SEL switch that selects between a J-Link power source (PWR_J) and an external power source (VIN). The circuit includes a 1N5819S diode (D1) for input protection, a 1N5819S diode (D2) for output protection, and a 3.3V LM1117IMPX-ADJ voltage regulator (VR1) with a 240R/1% resistor (R1) and a 390R/1% resistor (R2). The output is filtered by a 100nF capacitor (C4) and a 470uF/6.3V/tant capacitor (C5). The output is connected to the module's 3.3V pin (PWR) and GND. The module's 3.3V pin is also connected to a 3.3V_E pin and a 3.3V pin. The module's GND pin is connected to the module's GND pin.

DEBUG INTERFACE:

The diagram illustrates the debug interface for the GHY127SMT-02x05 (PIN7-CUT) module. It features two 20-pin headers, SWD-1 and SWD-2, connected to a 3.3V supply and ground. SWD-1 pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, and 20 are connected to SWDIO, SWCLK, SWO, RST, and +5V. SWD-2 pins 1, 2, 3, 4, 5, 6, 7, 8, 9, and 10 are connected to SWDIO, SWCLK, SWO, RST, and +5V. Resistors R4, R5, R6, and R7 are connected to the 3.3V supply and ground.

Header	Pin	Signal
SWD-1	1	SWDIO
SWD-1	2	SWCLK
SWD-1	3	SWO
SWD-1	4	RST
SWD-1	5	+5V
SWD-1	6	SWDIO
SWD-1	7	SWCLK
SWD-1	8	SWO
SWD-1	9	RST
SWD-1	10	+5V
SWD-1	11	SWDIO
SWD-1	12	SWCLK
SWD-1	13	SWO
SWD-1	14	RST
SWD-1	15	+5V
SWD-1	16	SWDIO
SWD-1	17	SWCLK
SWD-1	18	SWO
SWD-1	19	RST
SWD-1	20	+5V
SWD-2	1	SWDIO
SWD-2	2	SWCLK
SWD-2	3	SWO
SWD-2	4	RST
SWD-2	5	+5V
SWD-2	6	SWDIO
SWD-2	7	SWCLK
SWD-2	8	SWO
SWD-2	9	RST
SWD-2	10	+5V

GHY127SMT-02x05(PIN7-CUT)



The schematic diagram is divided into three sections by dashed red lines:

- TRIMMER:** Contains a 3.3V supply, a 10k trimmer potentiometer (R10), a 1k resistor (R11), and a 100nF capacitor (C13) connected to ground. The trimmer is labeled "AN TRIM 10k(Piher)".
- I2C:** Contains a 3.3V supply, a 2k resistor (R12), and a 33k resistor (R14) connected to ground. It shows the I2C pins of the WF4S module: SDA (pin 3), SCL (pin 4), and SCL (pin 2).
- UXT:** Contains a 3.3V supply, a 2k resistor (R13), and a 33k resistor (R14) connected to ground. It shows the UXT module (BH10S) with pins 1 through 10. The connections are: TXD1 (pin 1) to SCL (pin 4), RXD1 (pin 2) to SDA (pin 3), SDA (pin 3) to SCL (pin 2), SCL (pin 4) to SCL (pin 1), and SSEL (pin 10) to SCL (pin 2).

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BUTTONS

The image shows two circuit diagrams for buttons, labeled 'T1157 USER' and 'T1157 WAKE_UP'. Both circuits are powered by a 3.3V supply. The 'USER' button circuit includes a 22k resistor (R27) in series with the button, a 100nF capacitor (C25) to ground, and a 1k resistor (R28) connected to the 3.3V supply. The 'WAKE_UP' button circuit includes a 22k resistor (R29) in series with the button, a 100nF capacitor (C26) to ground, and a 1k resistor (R30) connected to the 3.3V supply.

Fiducials:

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