

Exploded view diagram of an L-Series Module assembly. The diagram shows the following components and dimensions:

- Printed Circuit Board (PCB):** Dimensions include 1.380 (width), .500 TYP (length), and 1.115 (height). It features an **L-SERIES MODULE** with components labeled AC1, AC, n1b8, G2, GY, and X.
- Heatsink Plate:** Dimensions include 1.380 (width), .500 TYP (length), and .375 (thickness). It is labeled **COPPER OR ALUMINUM HEATSINK PLATE**.
- Mounting Hardware:**
 - #8 MTG. SCREW:** Indicated for mounting the PCB to the heatsink plate.
 - RECOMMEND OC340 SILICONE GREASE:** Indicated for application to the mounting surface.
 - THREADED HOLE FOR SCREW MOUNTING (2 PLACES):** Dimensions include .360 MAX (diameter) and .500 (height).
 - NUT AND LOCKWASHER MAY BE USED:** Indicated for alternative mounting.
- Other Components:**
 - .062 DIA. (6 PLACES):** Dimension for a hole in the PCB.
 - .050 TYP:** Dimension for a hole in the PCB.
 - .030:** Dimension for a hole in the heatsink plate.
 - .775:** Dimension for a hole in the heatsink plate.
 - .500:** Dimension for a hole in the heatsink plate.

120/240/280 VAC

MULTI-FINGER RECEPTACLES THAT MATE WITH "L" SERIES PINS ARE AVAILABLE FROM:

MILL-MAX MFG. CORP
P.O. BOX 300
190 PINE HOLLOW RD
OYSTER BAY, NY. 11771-0300
TEL : 516-822-6000

THIS ALLOWS "L" MODULE TO
BE PLUGGED IN AND REMOVED
FROM P.C. BOARD

PART NO. 0345-0-15-01-04-14-10-0

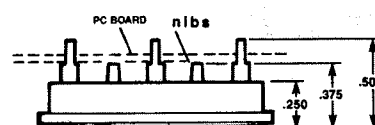


FIGURE 2

UL RECOGNIZED

ELECTRICAL SPECIFICATIONS

		SERIES			
		L3	L4	L5	L6*
I_d	maximum dc output current @ 85° ceramic full bridge (A)	13.0	20	25	42.5
$I_{T(RMS)}$	maximum output current @ 85° ceramic plate CKT 5 (A)	15.0	22	27	46
V_{TM}	maximum SCR voltage (V) @amperes peak	1.8V @13A	2.2V @20A	2.2V @25A	2.2V @40A
I_H	maximum holding current (mA)	100	100	100	200
T_J	operating junction temperature range	-25°C to +125°C			
di/dt	critical rate of rise of on-state current @ $T_J = 125^\circ\text{C}$ (A/ μS)	100	100	100	100
dv/dt	critical rate of rise of off-state voltage @ $T_J = 125^\circ\text{C}$ (V/ μS)	200	200	200	200
V_{RMS}	AC line input voltage (V)	120 240 280	120 240 280	120 240 280	120 240 280
I_{TSM}	maximum non-repetitive surge current (A)	150	250	300	600
I^2t	maximum I^2t for fusing $t = 8.3$ (A ² sec)	94	260	375	1500
I_{GT}	maximum required gate current to trigger, 25°C (mA)	100	100	100	100
I_{GM}	maximum peak gate current (A)	1.5	3.0	3.0	3.0
V_{GT}	maximum required gate voltage to trigger, 25°C (V)	2.5	2.5	2.5	3.0
V_{GD}	maximum non-triggering gate voltage at $T_J = 125^\circ\text{C}$ (V)	0.2	0.2	0.2	0.2
P_{GM}	maximum peak gate power, $t_p = 10\mu\text{Sec.}$ (W)	5.0	5.0	5.0	5.0
$P_{G(AV)}$	average gate power (W)	0.5	0.5	0.5	0.5
V_{GM}	maximum peak gate voltage (reverse) (V)	5.0	5.0	5.0	5.0
$R_{\theta CS}$	maximum thermal resistance case to sink (°C/W)	0.1	0.1	0.1	0.1
V_{FM}	maximum peak forward voltage at amperes peak	1.6 @13A	1.85 @20A	2.0 @25A	2.0 @40A
$R_{\theta JC}$	typical thermal resistance junction to ceramic base per device (°C/W)	0.6	0.5	0.5	0.5

*Not available in all circuit configurations.

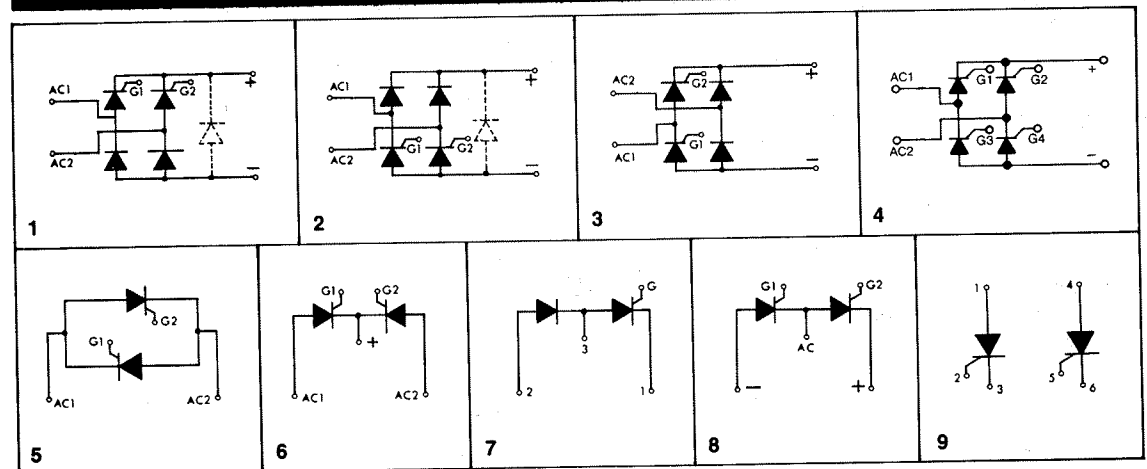
PART NUMBER IDENTIFICATION

1st Digit Series Type	2nd Digit Current	3rd Digit Circuit Type	4th Digit AC Line Voltage	Options
L	3-13 Amp. 4-20 Amp. 5-25 Amp. 6-42.5 Amp.*	1-9 (see schematic diagrams)	1-120 volts 2-240 volts 3-280 volts	F-Free Wheeling Diode Option C-Gate to cathode capacitor option. Consult factory.

*Not available in all circuit configurations.

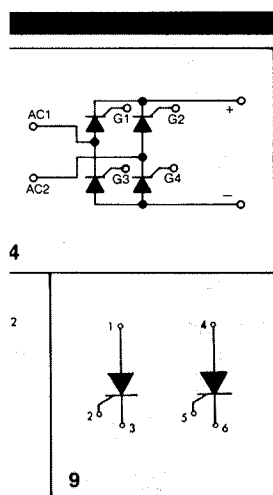
Each part number consists of 4 to 7 digits. Use the table to determine the part that fits your needs.

ELECTRICAL SCHEMATICS



SERIES				
nt to	L3	L4	L5	L6*
	100	100	100	100
A)	1.5	3.0	3.0	3.0
je to				
	2.5	2.5	2.5	3.0
voltage				
	0.2	0.2	0.2	0.2
	5.0	5.0	5.0	5.0
	0.5	0.5	0.5	0.5
	5.0	5.0	5.0	5.0
case to				
	0.1	0.1	0.1	0.1
e at	1.6	1.85	2.0	2.0
	@13A	@20A	@25A	@40A
ction to				
	0.6	0.5	0.5	0.5

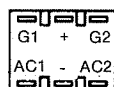
part number consists of 7 digits. Use the table to determine the part that fits your needs.



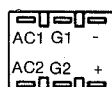
terminal orientation

"L3, 4 & 5" SERIES

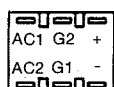
CIRCUIT 1



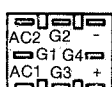
CIRCUIT 2



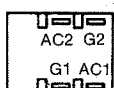
CIRCUIT 3



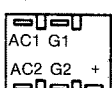
CIRCUIT 4



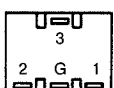
CIRCUIT 5



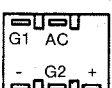
CIRCUIT 6



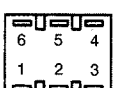
CIRCUIT 7



CIRCUIT 8



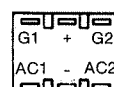
CIRCUIT 9



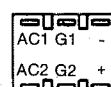
terminal orientation

"L6" SERIES

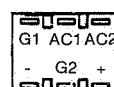
CIRCUIT 1



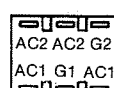
CIRCUIT 2



CIRCUIT 3



CIRCUIT 5



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