



Micro Commercial Components Corp.

Products End of Life Notification

Issue date: Jan-1st-2009

Last Buy Date :N/A

Description and Purpose:

MCC has undergone a review of its core business and products , and

determined to discontinue below products:

Discontinued Devices	Possible Replacements
SD101A	N/A
SD101B	N/A
SD101C	N/A
SD103A	N/A
SD103B	N/A
SD103C	N/A
LLSD101A	SD101AW
LLSD101B	SD101BW
LLSD101C	SD101CW
LLSD103A	SD103AW
LLSD103B	SD103BW
LLSD103C	SD103CW
1N5711	N/A
DL5711	1N5711W
1N6263	N/A
DL6263	1N6263W

**LLSD101A
THRU
LLSD101C**

Features

- Guard Ring Construction for Transient Protection
- Low Reverse Capacitance
- Low Forward Voltage Drop and Low Reverse Recovery Time
- Lead Free Finish/RoHS Compliant (Note 1) ("P" Suffix designates Compliant. See ordering information)

Mechanical Data

- Case: MiniMELF, Glass
- Terminals: Solderable per MIL-STD-202, Method 208
- Polarity: Indicated by Cathode Band
- Weight: 0.05 grams (approx.)

Maximum Ratings @ 25°C Unless Otherwise Specified

Characteristic	Symbol	LLSD101A	LLSD101B	LLSD101C
Peak Repetitive Reverse Voltage	V_{RRM}			
Working Peak Reverse Voltage	V_{RWM}	60V	50V	40V
DC Blocking Voltage	V_R			
RMS Reverse Voltage	$V_{R(RMS)}$	42V	35V	28V
Forward Continuous Current (Note 2)	I_{FM}		15mA	
Non-Repetitive Peak @ $t \leq 1.0s$	I_{FSM}		50mA	
Forward Surge Current @ $t = 10\mu s$			2.0A	
Power Dissipation (Note 2)	P_d		400mW	
Thermal Resistance (Note 2)	R		375K/W	
Operation & Storage Temp. Range	T_j, T_{STG}		-55 to 150°C	

Electrical Characteristics @ 25°C Unless Otherwise Specified

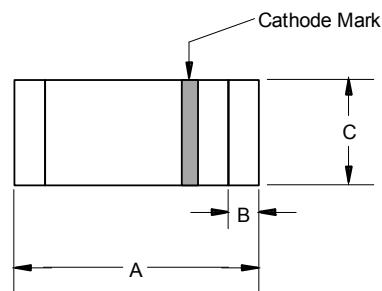
Characteristic	Symbol	Min	Max	Unit	Test Cond.
Peak Reverse Current LLSD101A	I_{RM}	-----	200	nA	$V_R=50V$
Reverse Current LLSD101B					$V_R=40V$
Reverse Current LLSD101C					$V_R=30V$
Forward Volt. Drop LLSD101A	V_{FM}	0.41		V	$I_F=1.0mA$
Forward Volt. Drop LLSD101B		0.40			$I_F=1.0mA$
Forward Volt. Drop LLSD101C		0.39			$I_F=1.0mA$
Forward Volt. Drop LLSD101A		1.00			$I_F=15mA$
Forward Volt. Drop LLSD101B		0.95			$I_F=15mA$
Forward Volt. Drop LLSD101C		0.90			$I_F=15mA$
Junction Capacitance LLSD101A	C_j	2.0		pF	$V_R=0V, f=1.0MHz$
Junction Capacitance LLSD101B		2.1			
Junction Capacitance LLSD101C		2.2			
Reverse Recovery Time	t_{rr}	-----	1.0	ns	$I_F=I_R=5mA$, recover to 0.1 I_R

Note: 1. Lead in Glass Exemption Applied, see EU Directive Annex 5.

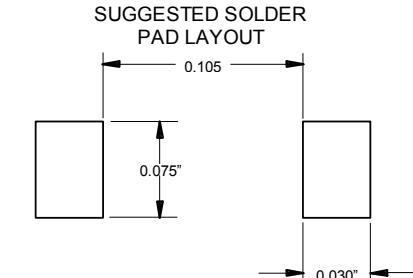
2. Valid provided that electrodes are kept at ambient temperature

**Schottky Barrier
Switching Diode**

MINIMELF



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.134	.142	3.40	3.60	
B	.008	.016	.20	.40	
C	.055	.059	1.40	1.50	



LLSD101A thru LLSD101C

•M•C•C•
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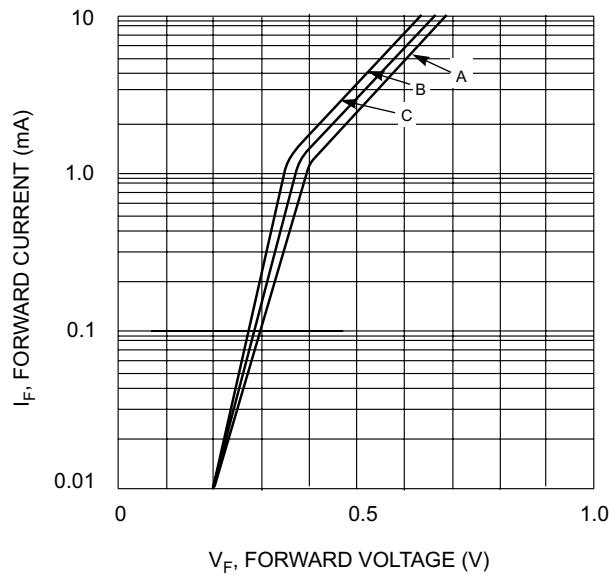


Fig. 1 Typical Forward Characteristic Variations for Primary Conduction

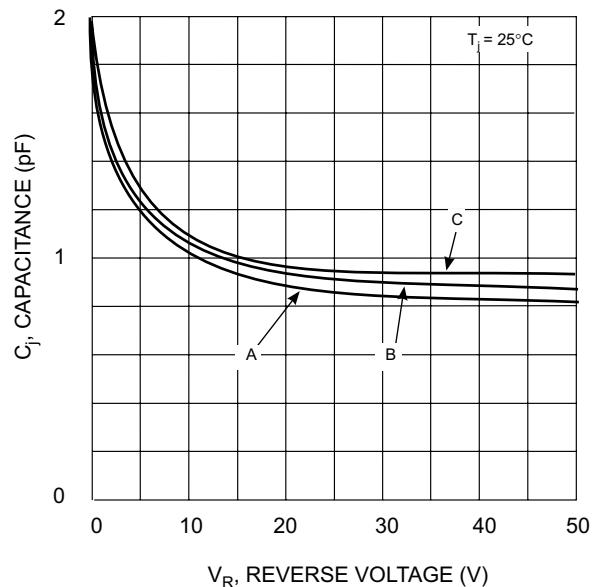


Fig. 2 Typ. Junction Capacitance vs Reverse Voltage



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Ordering Information

Device	Packing
(Part Number)-TP	Tape&Reel;2.5Kpcs/Reel

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