

April 1998

LM109/LM309 5-Volt Regulator

General Description

The LM109 series are complete 5V regulators fabricated on a single silicon chip. They are designed for local regulation on digital logic cards, eliminating the distribution problems association with single-point regulation. The devices are available in two standard transistor packages. In the solid-kovar TO-5 header, it can deliver output currents in excess of 200 mA, if adequate heat sinking is provided. With the TO-3 power package, the available output current is greater than 1A.

The regulators are essentially blowout proof. Current limiting is included to limit the peak output current to a safe value. In addition, thermal shutdown is provided to keep the IC from overheating. If internal dissipation becomes too great, the regulator will shut down to prevent excessive heating.

Considerable effort was expended to make these devices easy to use and to minimize the number of external components. It is not necessary to bypass the output, although this

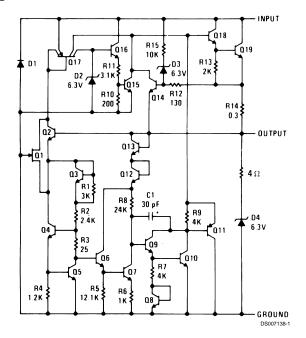
does improve transient response somewhat. Input bypassing is needed, however, if the regulator is located very far from the filter capacitor of the power supply. Stability is also achieved by methods that provide very good rejection of load or line transients as are usually seen with TTL logic.

Although designed primarily as a fixed-voltage regulator, the output of the LM109 series can be set to voltages above 5V, as shown. It is also possible to use the circuits as the control element in precision regulators, taking advantage of the good current-handling capability and the thermal overload protection.

Features

- Specified to be compatible, worst case, with TTL and DTL
- Output current in excess of 1A
- Internal thermal overload protection
- No external components required

Schematic Diagram



© 1999 National Semiconductor Corporation

DS007138

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Input Voltage 35V

Power Dissipation Internally Limited

Operating Junction Temperature Range

Storage Temperature Range -65°C to +150°C

Lead Temperature

(Soldering, 10 sec.) 300°C

Electrical Characteristics (Note 2)

Parameter	Conditions	LM109			LM309			Units
		Min	Тур	Max	Min	Тур	Max	1
Output Voltage	T _j = 25°C	4.7	5.05	5.3	4.8	5.05	5.2	V
Line Regulation	T _j = 25°C		4.0	50		4.0	50	mV
	$7.10V \le V_{IN} \le 25V$							
Load Regulation	T _j = 25°C							
TO-39 Package	$5 \text{ mA} \le I_{OUT} \le 0.5 \text{A}$		15	50		15	50	mV
TO-3 Package	5 mA ≤ I _{OUT} ≤ 1.5A		15	100		15	100	mV
Output Voltage	$7.40V \le V_{IN} \le 25V$,	4.6		5.4	4.75		5.25	V
	$5 \text{ mA} \leq I_{OUT} \leq I_{MAX}$							
	P < P _{MAX}							
Quiescent Current	7.40V ≤ V _{IN} ≤ 25V		5.2	10		5.2	10	mA
Quiescent Current Change	7.40V ≤ V _{IN} ≤ 25V			0.5			0.5	mA
	$5 \text{ mA} \leq I_{OUT} \leq I_{MAX}$			0.8			0.8	mA
Output Noise Voltage	T _A = 25°C		40			40		μV
	10 Hz ≤ f ≤ 100 kHz							
Long Term Stability			10			20		mV
Ripple Rejection	T _j = 25°C	50			50			dB
Thermal Resistance,	(Note 3)							
Junction to Case								
TO-39 Package			15			15		°C/W
TO-3 Package			2.5			2.5		°C/W

Note 1: "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

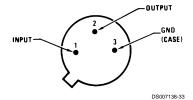
Note 2: Unless otherwise specified, these specifications apply $-55^{\circ}\text{C} \le T_j \le +150^{\circ}\text{C}$ for the LM109 and $0^{\circ}\text{C} \le T_j \le +125^{\circ}\text{C}$ for the LM309; $V_{\text{IN}} = 10V$; and $I_{\text{OUT}} = 0.1A$ for the TO-39 package or $I_{\text{OUT}} = 0.5A$ for the TO-3 package. For the TO-39 package, $I_{\text{MAX}} = 0.2A$ and $I_{\text{MAX}} = 0.2A$

Note 3: Without a heat sink, the thermal resistance of the TO-39 package is about 150°C/W, while that of the TO-3 package is approximately 35°C/W. With a heat sink, the effective thermal resistance can only approach the values specified, depending on the efficiency of the sink.

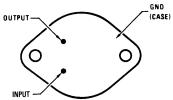
Note 4: Refer to RETS109H drawing for LM109H or RETS109K drawing for LM109K military specifications.

Connection Diagrams

Metal Can Packages



Order Number LM109H, LM109H/883 or LM309H See NS Package Number H03A



DS007138-34

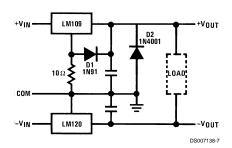
Order Number LM109K STEEL or LM309K STEEL See NS Package Number K02A Order Number LM109K/883 See NS Package Number K02C

Application Hints

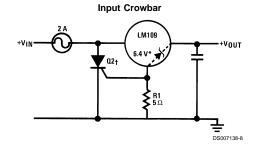
- 1. Bypass the input of the LM109 to ground with $\geq 0.2~\mu F$ ceramic or solid tantalum capacitor if main filter capacitor is more than 4 inches away.
- 2. Avoid insertion of regulator into "live" socket if input voltage is greater than 10V. The output will rise to within 2V of the unregulated input if the ground pin does not make contact, possibly damaging the load. The LM109 may also be damaged if a large output capacitor is charged up, then discharged through the internal clamp zener when the ground pin makes contact.
- 3. The output clamp zener is designed to absorb transients only. It will not clamp the output effectively if a failure occurs in the internal power transistor structure. Zener dynamic impedance is $\approx 4\Omega.$ Continuous RMS current into the zener should not exceed 0.5A.
- 4. Paralleling of LM109s for higher output current is not recommended. Current sharing will be almost nonexistent, leading to a current limit mode operation for devices with the highest initial output voltage. The current limit devices may also heat up to the thermal shutdown point (≈ 175°C). Long term reliability cannot be guaranteed under these conditions.

Preventing latchoff for loads connected to negative voltage:

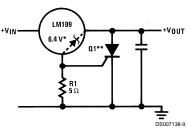
If the output of the LM109 is pulled negative by a high current supply so that the output pin is more than 0.5V negative with respect to the ground pin, the LM109 can latch off. This can be prevented by clamping the ground pin to the output pin with a germanium or Schottky diode as shown. A silicon diode (1N4001) at the output is also needed to keep the positive output from being pulled too far negative. The 10Ω resistor will raise $+V_{OUT}$ by $\approx 0.05V$.



Crowbar Overvoltage Protection



Output Crowbar

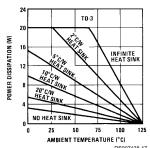


- *Zener is internal to LM109.
- $^{**}\mathrm{Q1}$ must be able to withstand 7A continuous current if fusing is not used at regulator input. LM109 bond wires will fuse at currents above 7A.
- 1Q2 is selected for surge capability. Consideration must be given to filter capacitor size, transformer impedance, and fuse blowing time.
- ††Trip point is ≈ 7.5V.

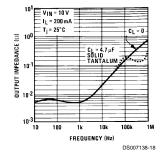
Typical Performance Characteristics

Maximum Average

Maximum Average Power Dissipation (LM309K)

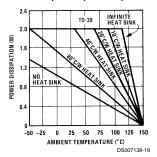


Output Impedance

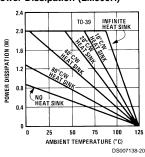


Typical Performance Characteristics (Continued)

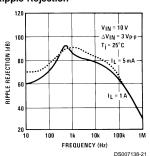
Maximum Average Power Dissipation (LM109H)



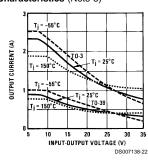
Maximum Average Power Dissipation (LM309H)



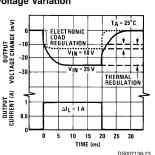
Ripple Rejection



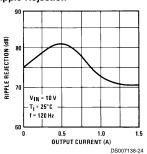
Current Limit Characteristics (Note 5)



Thermally Induced Output Voltage Variation

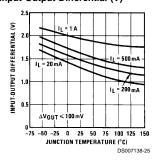


Ripple Rejection

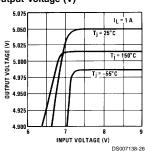


Note 5: Current limiting foldback characteristics are determined by input output differential, not by output voltage.

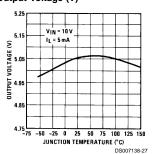
Input-Output Differential (V)



Output Voltage (V)

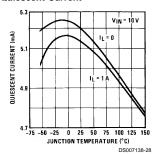


Output Voltage (V)

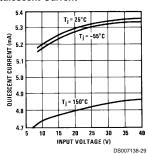


Typical Performance Characteristics (Continued)

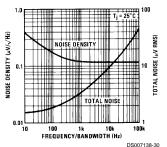
Quiescent Current



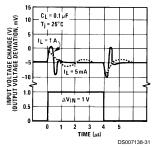
Quiescent Current



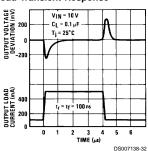
Output Voltage Noise



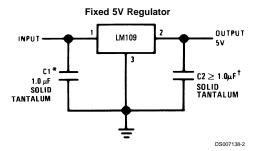
Line Transient Response



Load Transient Response



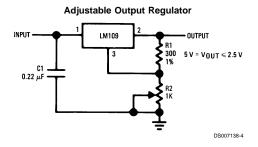
Typical Applications



*Required if regulator is located more than 4" from power supply filter capacitor.

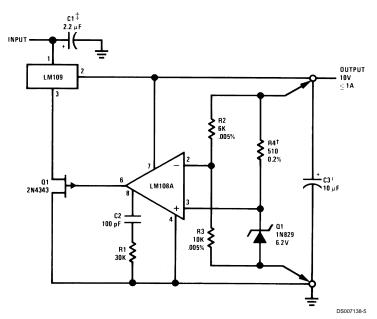
 $\uparrow\! Although$ no output capacitor is needed for stability, it does improve transient response. C2 should be used whenever long wires are used to connect to the load, or when transient response is critical.

Note: Pin 3 electrically connected to case.



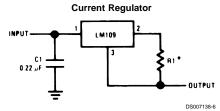
Typical Applications (Continued)

High Stability Regulator*

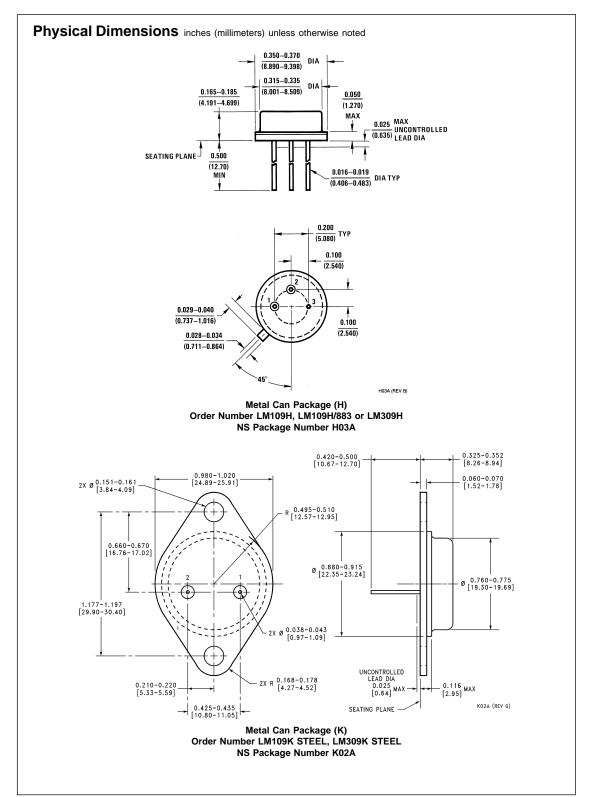


*Regulation better than 0.01%, load, line and temperature, can be obtained.

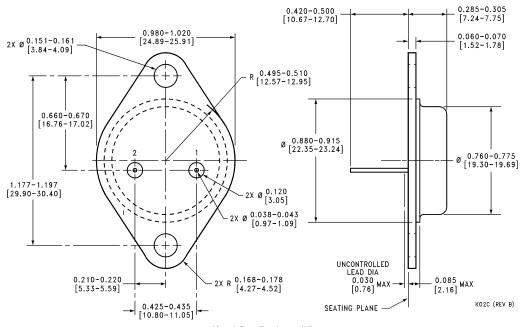
†Determines zener current. May be adjusted to minimize thermal drift. ‡Solid tantalum.



*Determines output current. If wirewound resistor is used, bypass with 0.1 $\mu\text{F}.$







Metal Can Package (K) Mil-Aero Product Order Number LM109K/883 NS Package Number K02C

LIFE SUPPORT POLICY

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF NATIONAL SEMI-CONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



National Semiconductor Corporation Americas

Tel: 1-800-272-9959 Fax: 1-800-737-7018 Email: support@nsc.com

www.national.com

National Semiconductor Europe

Fax: +49 (0) 1 80-530 85 86
Email: europe.support@nsc.com
Deutsch Tel: +49 (0) 1 80-530 85 85
English Tel: +49 (0) 1 80-532 78 32
Français Tel: +49 (0) 1 80-532 93 58
Italiano Tel: +49 (0) 1 80-534 16 80

National Semiconductor Asia Pacific Customer Response Group Tel: 65-2544466 Fax: 65-2504466 Email: sea.support@nsc.com National Semiconductor Japan Ltd. Tel: 81-3-5639-7560 Fax: 81-3-5639-7507

National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications.