

LM3406/06HV

1.5A Constant Current Buck Regulator for Driving High Power LEDs

General Description

The LM3406/06HV are monolithic switching regulators designed to deliver constant currents to high power LEDs. Ideal for automotive, industrial, and general lighting applications, they contain a high-side N-channel MOSFET switch with a current limit of 2.0A (typical) for step-down (Buck) regulators. Controlled on-time with true average current and an external current sense resistor allow the converter output voltage to adjust as needed to deliver a constant current to series and series-parallel connected LED arrays of varying number and type. LED dimming via pulse width modulation (PWM) is achieved using a dedicated logic pin or by PWM of the power input voltage. The product feature set is rounded out with low-power shutdown and thermal shutdown protection.

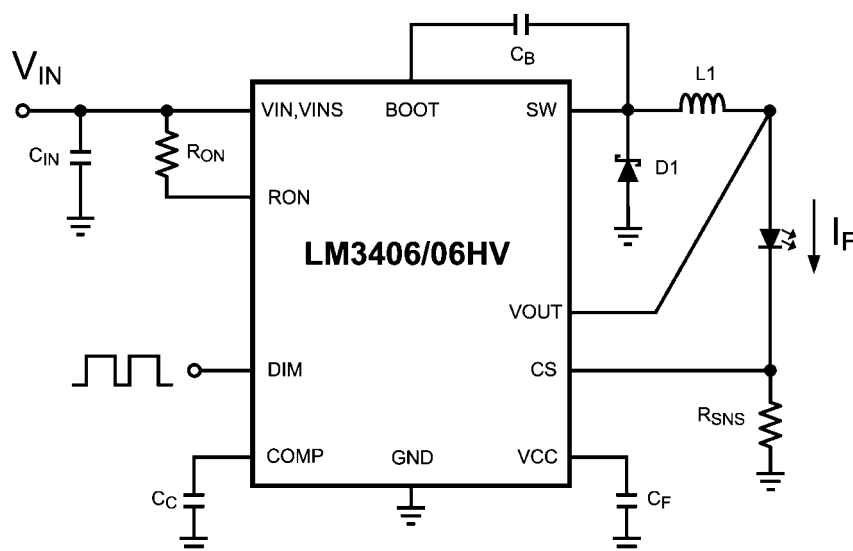
Features

- Integrated 2.0A MOSFET
- V_{IN} Range 6V to 42V (LM3406)
- V_{IN} Range 6V to 75V (LM3406HV)
- True average output current control
- 1.7A Minimum Output Current Limit Over Temperature
- Cycle-by-Cycle Current Limit
- PWM Dimming with Dedicated Logic Input
- PWM Dimming with Power Input Voltage
- Simple Control Loop Compensation
- Low Power Shutdown
- Supports All-Ceramic Output Capacitors and Capacitorless Outputs
- Thermal Shutdown Protection
- eTSSOP-14 Package

Applications

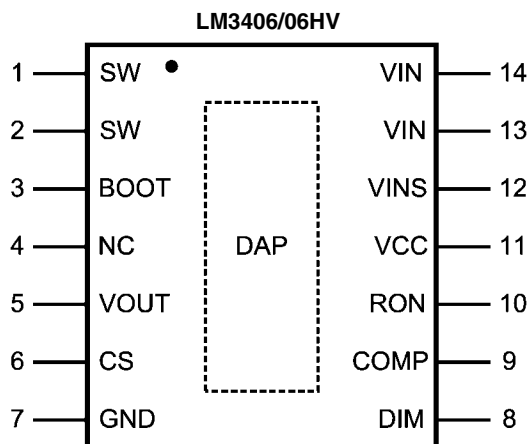
- LED Driver
- Constant Current Source
- Automotive Lighting
- General Illumination
- Industrial Lighting

Typical Application



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Connection Diagram



14-Lead Exposed Pad Plastic TSSOP Package
NS Package Number MXA14A

30020302

Ordering Information

Order Number	Package Type	NSC Package Drawing	Supplied As
LM3406MH	eTSSOP-14	MXA14A	95 units in anti-static rails
LM3406MHX			2500 units on tape and reel
LM3406HVMH			95 units in anti-static rails
LM3406HVMHX			2500 units on tape and reel

Pin Descriptions

Pin(s)	Name	Description	Application Information
1,2	SW	Switch pin	Connect these pins to the output inductor and Schottky diode.
3	BOOT	MOSFET drive bootstrap pin	Connect a 22 nF ceramic capacitor from this pin to the SW pins.
4	NC	No Connect	No internal connection. Leave this pin unconnected.
5	VOUT	Output voltage sense pin	Connect this pin to the output node where the inductor and the first LED's anode connect.
6	CS	Current sense feedback pin	Set the current through the LED array by connecting a resistor from this pin to ground.
7	GND	Ground pin	Connect this pin to system ground.
8	DIM	Input for PWM dimming	Connect a logic-level PWM signal to this pin to enable/disable the power MOSFET and reduce the average light output of the LED array. Logic high = output on, logic low - output off.
9	COMP	Error amplifier output	Connect a 0.1 μ F ceramic capacitor with X5R or X7R dielectric from this pin to ground.
10	RON	On-time control pin	A resistor connected from this pin to VIN sets the regulator controlled on-time.
11	VCC	Output of the internal 7V linear regulator	Bypass this pin to ground with a minimum 0.1 μ F ceramic capacitor with X5R or X7R dielectric.
12	VINS	Input voltage PWM dimming comparator input	Connect this pin to the anode of the input diode to allow dimming by PWM of the input voltage
13,14	VIN	Input voltage pin	Nominal operating input range for this pin is 6V to 42V (LM3406) or 6V to 75V (LM3406HV).
DAP	DAP	Thermal Pad	Connect to ground. Place 4-6 vias from DAP to bottom layer ground plane.

Absolute Maximum Ratings

LM3406/LM3406HV (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

VIN to GND	-0.3V to 45V (76V LM3406HV)
VINS to GND	-0.3V to 45V (76V LM3406HV)
VOU to GND	-0.3V to 45V (76V LM3406HV)
BOOT to GND	-0.3V to 59V (90V LM3406HV)
SW to GND	-1.5V to 45V (76V LM3406HV)
BOOT to VCC	-0.3V to 45V (76V LM3406HV)
BOOT to SW	-0.3V to 14V
VCC to GND	-0.3V to 14V
DIM to GND	-0.3V to 7V

COMP to GND	-0.3V to 7V
CS to GND	-0.3V to 7V
RON to GND	-0.3V to 7V
Junction Temperature	150°C
Storage Temp. Range	-65°C to 125°C
ESD Rating (Note 2)	2kV
Soldering Information	
Lead Temperature (Soldering, 10sec)	260°C
Infrared/Convection Reflow (15sec)	235°C

Operating Ratings

(Note 1)

V _{IN}	6V to 42V (75V LM3406HV)
Junction Temperature Range	-40°C to +125°C
Thermal Resistance θ_{JA} (eTSSOP-14 Package) (Note 4)	50°C/W

Electrical Characteristics V_{IN} = 24V unless otherwise indicated. Typicals and limits appearing in plain type apply for T_A = T_J = +25°C (Note 3). Limits appearing in **boldface type** apply over full Operating Temperature Range. Datasheet min/max specification limits are guaranteed by design, test, or statistical analysis.

LM3406/LM3406HV

Symbol	Parameter	Conditions	Min	Typ	Max	Units
REGULATION COMPARATOR AND ERROR AMPLIFIER						
V _{REF}	CS Regulation Threshold	CS Decreasing, SW turns on	187.5	200	210	mV
			191.0 (Note 5)		210.0 (Note 5)	
V _{OV}	CS Over-voltage Threshold	CS Increasing, SW turns off		300		mV
I _{CS}	CS Bias Current	CS = 0V		0.9		μA
I _{VOUT}	VOUT Bias Current	VOUT = 24V		83		μA
I _{COMP}	COMP Pin Current	CS = 0V		25		μA
G _{m-CS}	Error Amplifier Transconductance	150 mV < CS < 250 mV		145		μS
SHUTDOWN						
V _{SD-TH}	Shutdown Threshold	RON Increasing	0.3	0.7	1.05	V
V _{SD-HYS}	Shutdown Hysteresis	RON Decreasing		40		mV
ON AND OFF TIMER						
t _{OFF-MIN}	Minimum Off-time	CS = 0V		230		ns
t _{ON}	Programmed On-time	V _{IN} = 24V, V _O = 12V, R _{ON} = 200 kΩ	800	1300	1800	
t _{ON-MIN}	Minimum On-time			280		
VINS COMPARATOR						
V _{INS-TH}	VINS Pin Threshold	VINS decreasing		70		%VIN
I _{IN-2WD}	VINS Pin Input Current	VINS = 24V * 0.7		25		μA
INTERNAL REGULATOR						
V _{CC-REG}	V _{CC} Regulated Output	0 mA < I _{CC} < 5 mA	6.4	7	7.4	V
V _{IN-DO}	V _{IN} - V _{CC}	I _{CC} = 5 mA, 6.0V < V _{IN} < 8.0V, Non-switching		300		mV
V _{CC-BP-TH}	V _{CC} Bypass Threshold	V _{IN} Increasing		8.8		V
V _{CC-LIM}	V _{CC} Current Limit	V _{IN} = 24V, V _{CC} = 0V	4	20		mA

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$V_{CC-UV-TH}$	V_{CC} Under-voltage Lock-out Threshold	V_{CC} Increasing		5.3		V
$V_{CC-UV-HYS}$	V_{CC} Under-voltage Lock-out Hysteresis	V_{CC} Decreasing		150		mV
I_{IN-OP}	I_{IN} Operating Current	Non-switching, $CS = 0.5V$		1.2		mA
I_{IN-SD}	I_{IN} Shutdown Current	$R_{ON} = 0V$		240	350	μA
CURRENT LIMIT						
I_{LIM}	Current Limit Threshold		1.7	2.1	2.7	A
DIM COMPARATOR						
V_{IH}	Logic High	DIM Increasing	2.2			V
V_{IL}	Logic Low	DIM Decreasing			0.8	V
I_{DIM-PU}	DIM Pull-up Current	DIM = 1.5V		80		μA
MOSFET AND DRIVER						
R_{DS-ON}	Buck Switch On Resistance	$I_{SW} = 200\text{ mA}$, $BOOT = 6.3V$		0.37	0.75	Ω
$V_{DR-UVLO}$	BOOT Under-voltage Lock-out Threshold	BOOT–SW Increasing	1.7	2.9	4.3	V
V_{DR-HYS}	BOOT Under-voltage Lock-out Hysteresis	BOOT–SW Decreasing		370		mV
THERMAL SHUTDOWN						
T_{SD}	Thermal Shutdown Threshold			165		$^{\circ}C$
T_{SD-HYS}	Thermal Shutdown Hysteresis			25		$^{\circ}C$
THERMAL RESISTANCE						
θ_{JA}	Junction to Ambient	eTSSOP-14 Package (Note 4)		50		$^{\circ}C/W$

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur, including inoperability and degradation of device reliability and/or performance. Functional operation of the device and/or non-degradation at the Absolute Maximum Ratings or other conditions beyond those indicated in the Operating Ratings is not implied. The recommended Operating Ratings indicate conditions at which the device is functional and the device should not be operated beyond such conditions.

Note 2: The human body model is a 100 pF capacitor discharged through a 1.5 k Ω resistor into each pin.

Note 3: Typical values represent most likely parametric norms at the conditions specified and are not guaranteed.

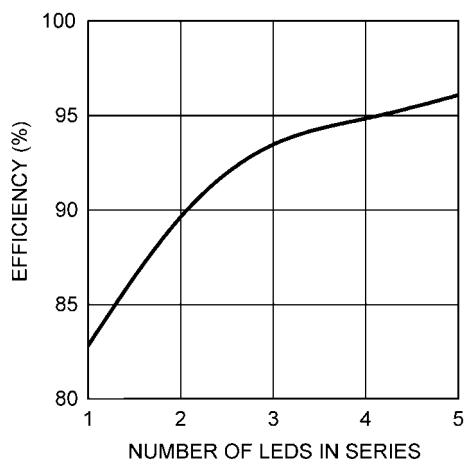
Note 4: θ_{JA} of 50 $^{\circ}C/W$ with DAP soldered to a minimum of 2 square inches of 1oz. copper on the top or bottom PCB layer.

Note 5: Specified with junction temperature from 0 $^{\circ}C$ - 125 $^{\circ}C$.

Note 6: $V_{IN} = 24V$, $I_F = 1A$, $T_A = 25^{\circ}C$, and the load consists of three InGaN LEDs in series unless otherwise noted. See the Bill of Materials table at the end of the datasheet.

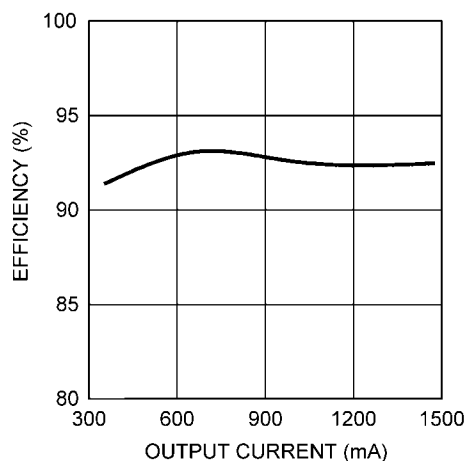
Typical Performance Characteristics

Efficiency Vs. Number of InGaN LEDs in Series
(Note 6)



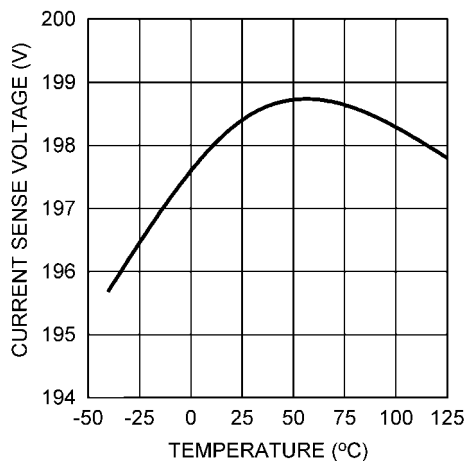
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Efficiency Vs. Output Current
(Note 6)



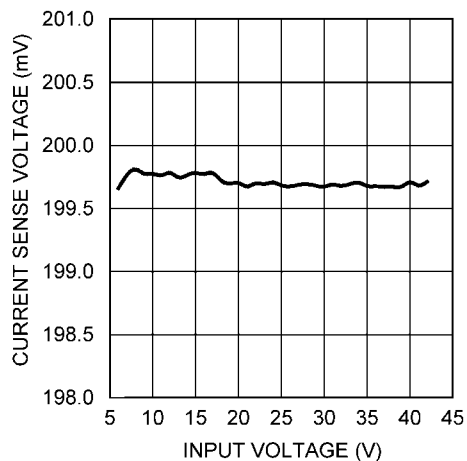
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V_{REF} vs Temperature



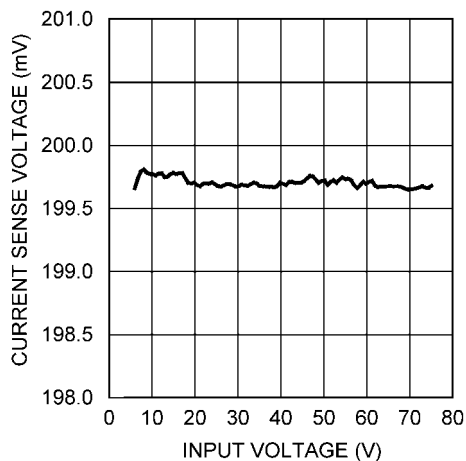
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V_{REF} vs V_{IN} , LM3406



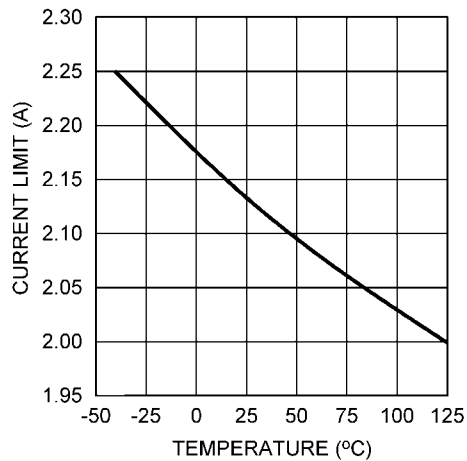
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V_{REF} vs V_{IN} , LM3406HV

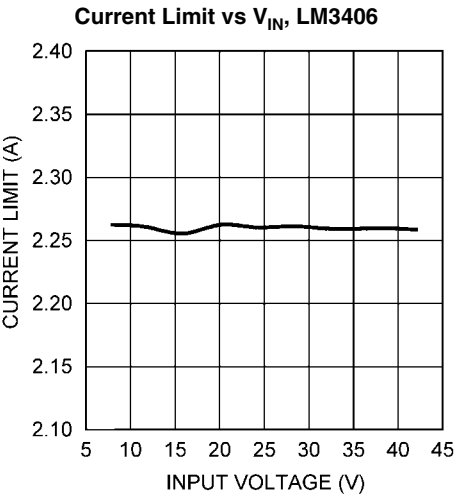


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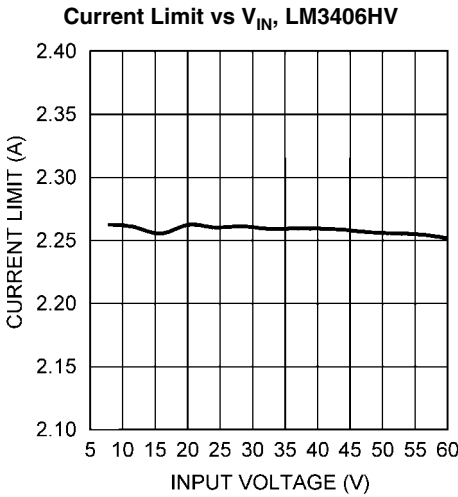
Current Limit vs Temperature



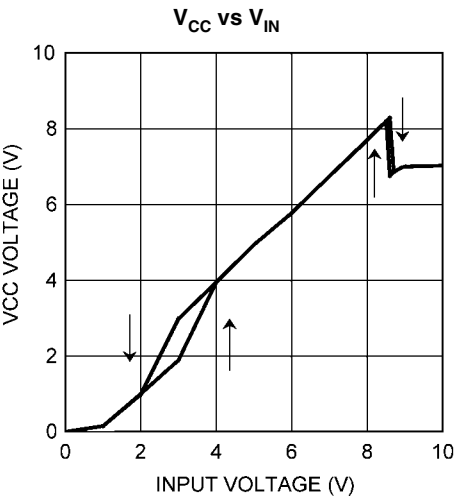
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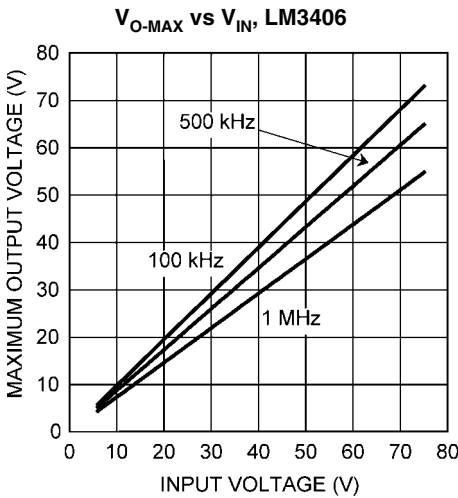
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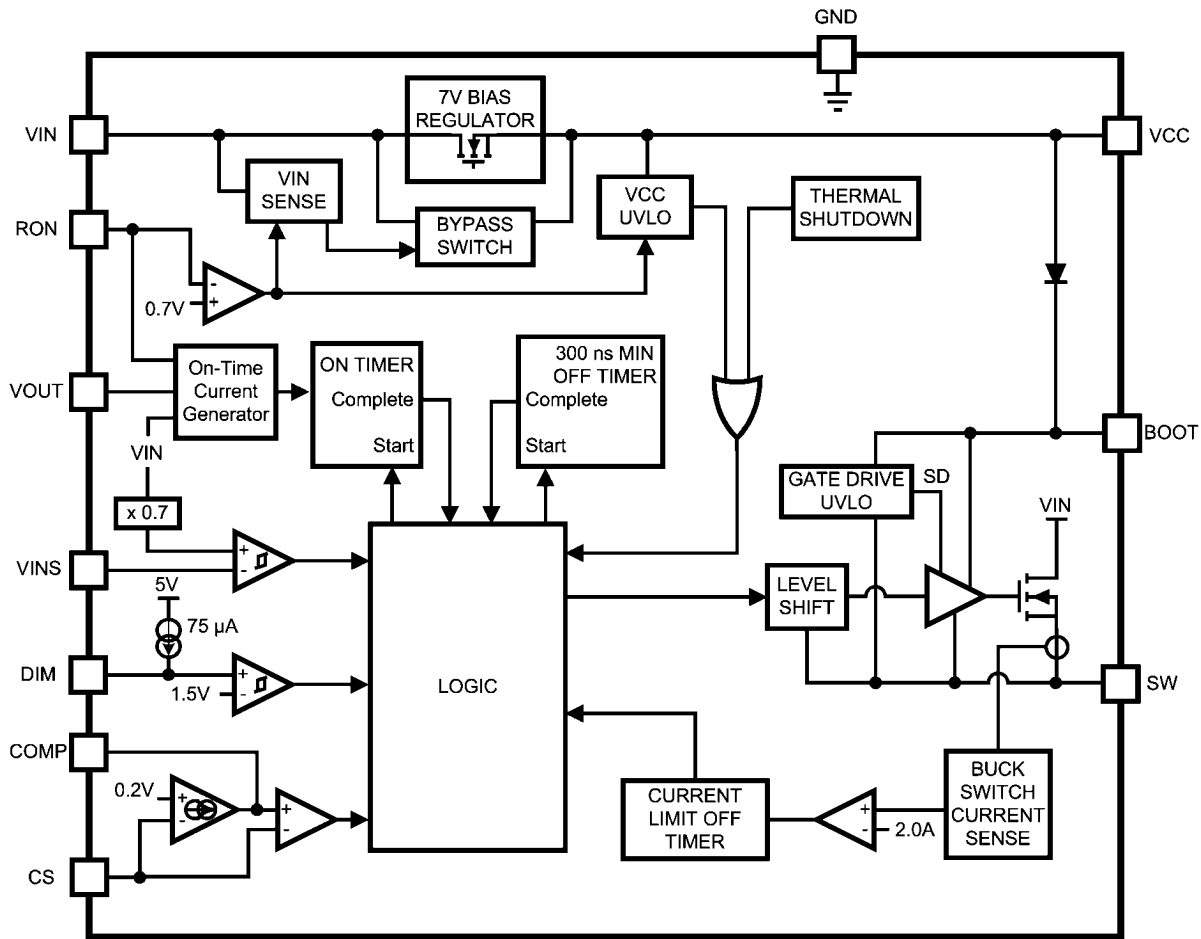


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Block Diagram



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Application Information

THEORY OF OPERATION

The LM3406 and LM3406HV are buck regulators with a wide input voltage range, low voltage reference, and a fast output enable/disable function. These features combine to make them ideal for use as a constant current source for LEDs with forward currents as high as 1.5A. The controlled on-time (COT) architecture uses a comparator and a one-shot on-timer that varies inversely with input and output voltage instead of a fixed clock. The LM3406/06HV also employs an integrator circuit that averages the output current. When the converter runs in continuous conduction mode (CCM) the controlled on-time maintains a constant switching frequency over changes in both input and output voltage. These features combine to give the LM3406/06HV an accurate output current, fast transient response, and constant switching frequency over a wide range of conditions.

CONTROLLED ON-TIME OVERVIEW

Figure 1 shows a simplified version of the feedback system used to control the current through an array of LEDs. A differential voltage signal, V_{SNS} , is created as the LED current flows through the current setting resistor, R_{SNS} . V_{SNS} is fed back by the CS pin, where it is integrated and compared against an error amplifier-generated reference. The error amplifier is a transconductance (G_m) amplifier which adjusts the voltage on COMP to maintain a 200 mV average at the CS pin. The on-comparator turns on the power MOSFET when V_{SNS} falls below the reference created by the G_m amp. The power MOSFET conducts for a controlled on-time, t_{ON} , set by an external resistor, R_{ON} , the input voltage, V_{IN} and the output voltage, V_O . On-time can be estimated by the following simplified equation (for the most accurate version of this expression see the Appendix):

$$t_{ON} = 1 \times 10^{-11} \times R_{ON} \times \frac{V_O}{V_{IN}}$$

At the conclusion of t_{ON} the power MOSFET turns off and must remain off for a minimum of 230 ns. Once this $t_{OFF-MIN}$ is complete the CS comparator compares the integrated V_{SNS} and reference again, waiting to begin the next cycle.

the V_{CC} voltage minus the diode drop to drive the MOSFET when its source voltage is equal to V_{IN} .

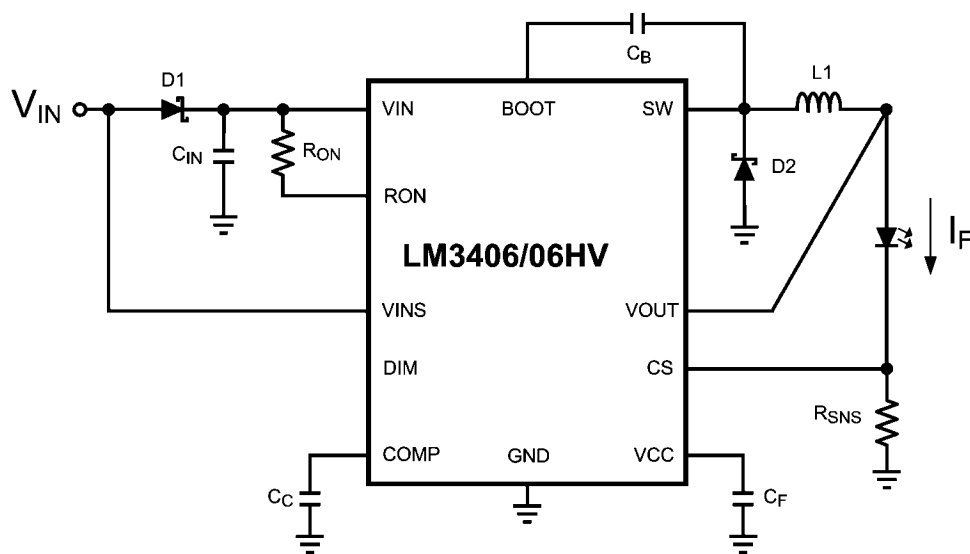
FAST LOGIC PIN FOR PWM DIMMING

The DIM pin is a TTL compatible input for PWM dimming of the LED. A logic low (below 0.8V) at DIM will disable the internal MOSFET and shut off the current flow to the LED array. While the DIM pin is in a logic low state the support circuitry (driver, bandgap, VCC) remains active in order to minimize the time needed to turn the LED array back on when the DIM pin sees a logic high (above 2.2V). A 75 μ A (typical) pull-up current ensures that the LM3406/06HV is on when DIM pin is open circuited, eliminating the need for a pull-up resistor. Dimming frequency, f_{DIM} , and duty cycle, D_{DIM} , are limited by the LED current rise time and fall time and the delay from activation of the DIM pin to the response of the internal power MOSFET. In general, f_{DIM} should be at least one order of

magnitude lower than the steady state switching frequency in order to prevent aliasing.

INPUT VOLTAGE COMPARATOR FOR PWM DIMMING

Adding an external input diode and using the internal VINS comparator allows the LM3406/06HV to sense and respond to dimming that is done by PWM of the input voltage. This method is also referred to as "Two-Wire Dimming", and a typical application circuit is shown in Figure 2. If the VINS pin voltage falls 70% below the VIN pin voltage, the LM3406/06HV disables the internal power FET and shuts off the current to the LED array. The support circuitry (driver, bandgap, VCC) remains active in order to minimize the time needed to turn the LED back on when the VINS pin voltage rises and exceeds 70% of VIN. This minimizes the response time needed to turn the LED array back on.



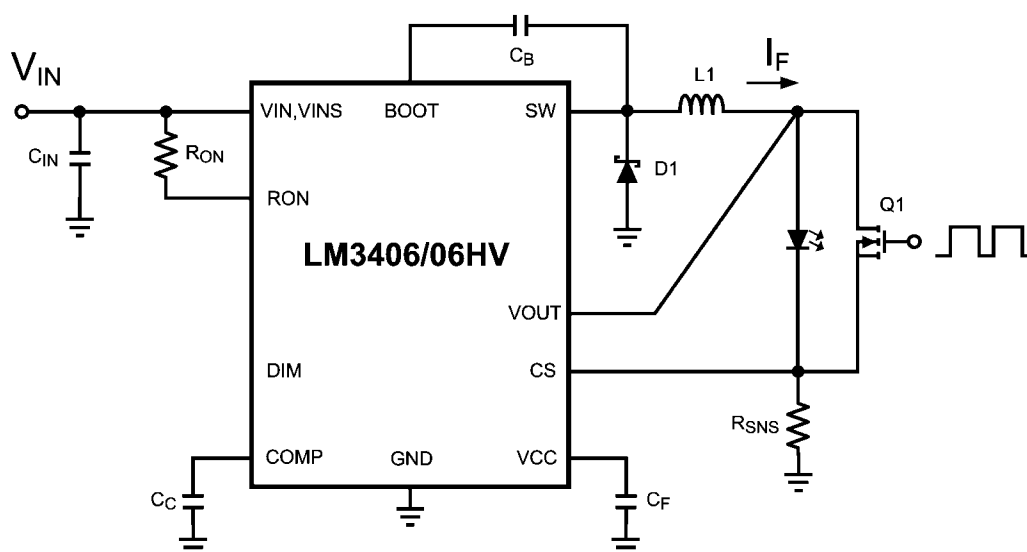
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FIGURE 2. Typical Application using Two-Wire Dimming

PARALLEL MOSFET FOR HIGH-SPEED PWM DIMMING

For applications that require dimming at high frequency or with wide dimming duty cycle range neither the VINS comparator or the DIM pin are capable of slewing the LED current from 0 to the target level fast enough. For such applications the LED current slew rate can be increased by shorting the LED current with a N-MOSFET placed in parallel to the LED or LED array, as shown in Figure 3. While the parallel FET is on the output current flows through it, effectively reducing the output voltage to equal the CS pin voltage of 0.2V. This dim-

ming method maintains a continuous current through the inductor, and therefore eliminates the biggest delay in turning the LED(s) on and off. The trade-off with parallel FET dimming is that more power is wasted while the FET is on, although in most cases the power wasted is small compared to the power dissipated in the LEDs. Parallel FET circuits should use no output capacitance or a bare minimum for noise filtering in order to minimize the slew rate of output voltage. Dimming FET Q1 can be driven from a ground-referenced source because the source stays at 0.2V along with the CS pin.



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FIGURE 3. Dimming with a Parallel FET

PEAK CURRENT LIMIT

The current limit comparator of the LM3406/06HV will engage whenever the power MOSFET current (equal to the inductor current while the MOSFET is on) exceeds 2.1A (typical). The power MOSFET is disabled for a cool-down time that of approximately 100 μ s. At the conclusion of this cool-down time the system re-starts. If the current limit condition persists the cycle of cool-down time and restarting will continue, creating a low-power hiccup mode, minimizing thermal stress on the LM3406/06HV and the external circuit components.

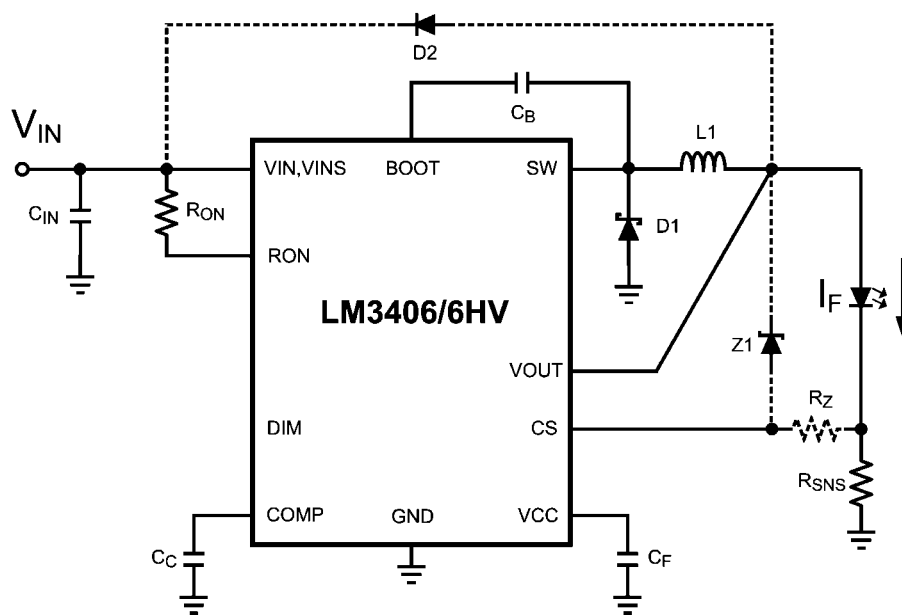
OVER-VOLTAGE/OVER-CURRENT COMPARATOR

The CS pin includes an output over-voltage/over-current comparator that will disable the power MOSFET whenever V_{SNS} exceeds 300 mV. This threshold provides a hard limit for the output current. Output current overshoot is limited to $300 \text{ mV} / R_{SNS}$ by this comparator during transients. The OVP/OCP comparator limits the maximum ripple voltage at the CS pin to 200 mV_{P-P}.

OUTPUT OPEN-CIRCUIT

The most common failure mode for power LEDs is a broken bond wire, and the result is an output open-circuit. When this happens the feedback path is disconnected, and the output voltage will attempt to rise. In buck converters the output voltage can only rise as high as the input voltage, and the minimum off-time requirement ensures that $V_{O(MAX)}$ is slightly less than V_{IN} . Figure 4 shows a method using a zener diode, Z1, and zener limiting resistor, R_Z , to limit output voltage to the reverse breakdown voltage of Z1 plus 200 mV. The zener diode reverse breakdown voltage, V_Z , must be greater than the maximum combined V_F of all LEDs in the array. The maximum recommended value for R_Z is 1 k Ω .

The output stage (SW and VOUT pins) of the LM3406/06HV is capable of withstanding $V_{O(MAX)}$ indefinitely as long as the output capacitor is rated to handle the full input voltage. When an LED fails open-circuit and there is no output capacitor present the surge in output voltage due to the collapsing magnetic field in the output inductor can exceed V_{IN} and can damage the LM3406/06HV IC. As an alternative to the zener clamp method described previously, a diode can be connected from the output to the input of the regulator circuit that will clamp the inductive surge to one V_D above V_{IN} .



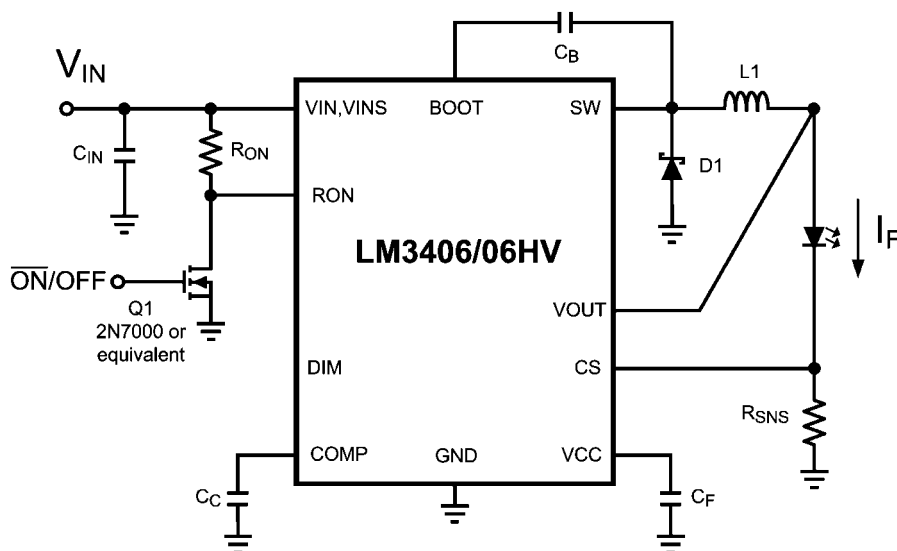
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FIGURE 4. Two Methods of Output Open Circuit Protection

LOW POWER SHUTDOWN

The LM3406/06HV can be placed into a low power state ($I_{IN,SD} = 240 \mu A$) by grounding the RON pin with a signal-level MOSFET as shown in Figure 5. Low power MOSFETs like the 2N7000, 2N3904, or equivalent are recommended devices for putting the LM3406/06HV into low power shutdown. Logic gates can also be used to shut down the LM3406/06HV

as long as the logic low voltage is below the over temperature minimum threshold of 0.3V. Noise filter circuitry on the RON pin can cause a few pulses with longer on-times than normal after RON is grounded or released. In these cases the OVP/OCP comparator will ensure that the peak inductor or LED current does not exceed $300 \text{ mV} / R_{SNS}$.



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FIGURE 5. Low Power Shutdown

THERMAL SHUTDOWN

Internal thermal shutdown circuitry is provided to protect the IC in the event that the maximum junction temperature is exceeded. The threshold for thermal shutdown is 165°C with a

25°C hysteresis (both values typical). During thermal shutdown the MOSFET and driver are disabled.

Design Considerations

SWITCHING FREQUENCY

Switching frequency is selected based on the trade-offs between efficiency (better at low frequency), solution size/cost (smaller at high frequency), and the range of output voltage that can be regulated (wider at lower frequency.) Many applications place limits on switching frequency due to EMI sensitivity. The on-time of the LM3406/06HV can be programmed for switching frequencies ranging from the 10's of kHz to over 1 MHz. This on-time varies in proportion to both V_{IN} and V_O in order to maintain first-order control over switching frequency, however in practice the switching frequency will shift in response to large swings in V_{IN} or V_O . The maximum switching frequency is limited only by the minimum on-time and minimum off-time requirements.

LED RIPPLE CURRENT

Selection of the ripple current, ΔI_F , through the LED array is similar to the selection of output ripple voltage in a standard voltage regulator. Where the output ripple in a voltage regulator is commonly $\pm 1\%$ to $\pm 5\%$ of the DC output voltage, LED manufacturers generally recommend values for ΔI_F ranging from $\pm 5\%$ to $\pm 20\%$ of I_F . Higher LED ripple current allows the use of smaller inductors, smaller output capacitors, or no output capacitors at all. Lower ripple current requires more output inductance, higher switching frequency, or additional output capacitance, and may be necessary for applications that are not intended for human eyes, such as machine vision or industrial inspection.

BUCK CONVERTERS WITHOUT OUTPUT CAPACITORS

The buck converter is unique among non-isolated topologies because of the direct connection of the inductor to the load during the entire switching cycle. By definition an inductor will control the rate of change of current that flows through it, and this control over current ripple forms the basis for component selection in both voltage regulators and current regulators. A current regulator such as the LED driver for which the LM3406/06HV was designed focuses on the control of the current through the load, not the voltage across it. A constant current regulator is free of load current transients, and has no need of output capacitance to supply the load and maintain output voltage. Referring to the Typical Application circuit on the front page of this datasheet, the inductor and LED can form a single series chain, sharing the same current. When no output capacitor is used, the same equations that govern inductor ripple current, ΔI_L , also apply to the LED ripple current, ΔI_F . For a controlled on-time converter such as LM3406/06HV the ripple current is described by the following expression:

$$\Delta I_L = \Delta I_F = \frac{V_{IN} - V_O}{L} \times t_{ON}$$

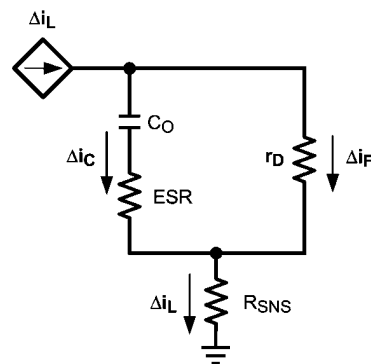
The triangle-wave inductor current ripple flows through R_{SNS} and produces a triangle-wave voltage at the CS pin. To provide good signal to noise ratio (SNR) the amplitude of CS pin ripple voltage, ΔV_{CS} , should be at least 25 mV_{P-P}. ΔV_{CS} is described by the following:

$$\Delta V_{CS} = \Delta I_F \times R_{SNS}$$

BUCK CONVERTERS WITH OUTPUT CAPACITORS

A capacitor placed in parallel with the LED(s) can be used to reduce the LED current ripple while keeping the same average current through both the inductor and the LED array. With an output capacitor the output inductance can be lowered, making the magnetics smaller and less expensive. Alternatively, the circuit could be run at lower frequency but keep the same inductor value, improving the power efficiency. Both the peak current limit and the OVP/OCP comparator still monitor peak inductor current, placing a limit on how large ΔI_L can be even if ΔI_F is made very small. Adding a capacitor that reduces ΔI_F to well below the target provides headroom for changes in inductance or V_{IN} that might otherwise push the peak LED ripple current too high.

Figure 6 shows the equivalent impedances presented to the inductor current ripple when an output capacitor, C_O , and its equivalent series resistance (ESR) are placed in parallel with the LED array. Note that ceramic capacitors have so little ESR that it can be ignored. The entire inductor ripple current still flows through R_{SNS} to provide the required 25 mV of ripple voltage for proper operation of the CS comparator.



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FIGURE 6. LED and C_O Ripple Current

To calculate the respective ripple currents the LED array is represented as a dynamic resistance, r_D . LED dynamic resistance is not always specified on the manufacturer's datasheet, but it can be calculated as the inverse slope of the LED's V_F vs. I_F curve. Note that dividing V_F by I_F will give an incorrect value that is 5x to 10x too high. Total dynamic resistance for a string of n LEDs connected in series can be calculated as the r_D of one device multiplied by n . Inductor ripple current is still calculated with the expression from Buck Regulators without Output Capacitors. The following equations can then be used to estimate ΔI_F when using a parallel capacitor:

$$\Delta I_F = \frac{\Delta I_L}{1 + \frac{r_D}{Z_C}}$$

$$Z_C = ESR + \frac{1}{2\pi \times f_{SW} \times C_O}$$

The calculation for Z_C assumes that the shape of the inductor ripple current is approximately sinusoidal.

Small values of C_O that do not significantly reduce ΔI_F can also be used to control EMI generated by the switching action of the LM3406/06HV. EMI reduction becomes more important

as the length of the connections between the LED and the rest of the circuit increase.

INPUT CAPACITORS

Input capacitors at the VIN pin of the LM3406/06HV are selected using requirements for minimum capacitance and rms ripple current. The input capacitors supply pulses of current approximately equal to I_F while the power MOSFET is on, and are charged up by the input voltage while the power MOSFET is off. All switching regulators have a negative input impedance due to the decrease in input current as input voltage increases. This inverse proportionality of input current to input voltage can cause oscillations (sometimes called 'power supply interaction') if the magnitude of the negative input impedance is greater than the input filter impedance. Minimum capacitance can be selected by comparing the input impedance to the converter's negative resistance; however this requires accurate calculation of the input voltage source inductance and resistance, quantities which can be difficult to determine. An alternative method to select the minimum input capacitance, $C_{IN(MIN)}$, is to select the maximum input voltage ripple which can be tolerated. This value, $\Delta V_{IN(MAX)}$, is equal to the change in voltage across C_{IN} during the converter on-time, when C_{IN} supplies the load current. $C_{IN(MIN)}$ can be selected with the following:

$$C_{IN(MIN)} = \frac{I_F \times t_{ON}}{\Delta V_{IN(MAX)}}$$

A good starting point for selection of C_{IN} is to use an input voltage ripple of 5% to 10% of V_{IN} . A minimum input capacitance of 2x the $C_{IN(MIN)}$ value is recommended for all LM3406/06HV circuits. To determine the rms current rating, the following formula can be used:

$$I_{IN(rms)} = I_F \times \sqrt{D(1 - D)}$$

Ceramic capacitors are the best choice for the input to the LM3406/06HV due to their high ripple current rating, low ESR, low cost, and small size compared to other types. When selecting a ceramic capacitor, special attention must be paid to the operating conditions of the application. Ceramic capacitors can lose one-half or more of their capacitance at their rated DC voltage bias and also lose capacitance with extremes in temperature. A DC voltage rating equal to twice the expected maximum input voltage is recommended. In addition,

the minimum quality dielectric which is suitable for switching power supply inputs is X5R, while X7R or better is preferred.

RECIRCULATING DIODE

The LM3406/06HV is a non-synchronous buck regulator that requires a recirculating diode D1 (see the Typical Application circuit) to carrying the inductor current during the MOSFET off-time. The most efficient choice for D1 is a Schottky diode due to low forward drop and near-zero reverse recovery time. D1 must be rated to handle the maximum input voltage plus any switching node ringing when the MOSFET is on. In practice all switching converters have some ringing at the switching node due to the diode parasitic capacitance and the lead inductance. D1 must also be rated to handle the average current, I_D , calculated as:

$$I_D = (1 - D) \times I_F$$

This calculation should be done at the maximum expected input voltage. The overall converter efficiency becomes more dependent on the selection of D1 at low duty cycles, where the recirculating diode carries the load current for an increasing percentage of the time. This power dissipation can be calculated by checking the typical diode forward voltage, V_D , from the I-V curve on the product datasheet and then multiplying it by I_D . Diode datasheets will also provide a typical junction-to-ambient thermal resistance, θ_{JA} , which can be used to estimate the operating die temperature of the device. Multiplying the power dissipation ($P_D = I_D \times V_D$) by θ_{JA} gives the temperature rise. The diode case size can then be selected to maintain the Schottky diode temperature below the operational maximum.

Design Example 1:

The first example circuit uses the LM3406 to create a flexible LED driver capable of driving anywhere from one to five white series-connected LEDs at a current of $1.5A \pm 5\%$ from a regulated DC voltage input of $24V \pm 10\%$. In addition to the $\pm 5\%$ tolerance specified for the average output current, the LED ripple current must be controlled to $10\%_{P-P}$ of the DC value, or $150 mA_{P-P}$. The typical forward voltage of each individual LED at 1.5A is 3.9V, hence the output voltage ranges from 4.1V to 19.7V, adding in the 0.2V drop for current sensing. A complete bill of materials can be found in *Table 1* at the end of this datasheet.

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$$\Delta i_{L(5 \text{ LEDs})} = [(24 - 19.7) \times 1.51 \times 10^{-6}] / 22 \times 10^{-6} \\ = 295 \text{ mA}_{P.P}$$

The peak LED/inductor current is then estimated. This calculation uses the worst-case ripple current which occurs with three LEDs.

$$I_{L(PEAK)} = I_L + 0.5 \times \Delta I_{L(MAX)}$$

$$I_{L(PEAK)} = 1.5 + 0.5 \times 0.56 = 1.78A$$

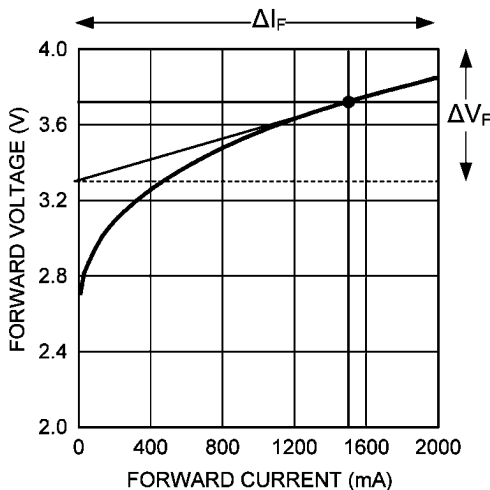
In order to prevent inductor saturation the inductor's peak current rating must be above 1.8A. A 22 μH off-the shelf inductor rated to 2.1A (peak) and 1.9A (average) with a DCR of 59 m Ω will be used.

USING AN OUTPUT CAPACITOR

This application does not require high frequency PWM dimming, allowing the use of an output capacitor to reduce the size and cost of the output inductor while still meeting the 10% P_{P-P} target for LED ripple current. To select the proper output capacitor the equation from Buck Regulators with Output Capacitors is re-arranged to yield the following:

$$Z_C = \frac{\Delta I_F}{\Delta I_L - \Delta I_F} \times r_D$$

The dynamic resistance, r_D , of one LED can be calculated by taking the tangent line to the V_F vs. I_F curve in the LED datasheet. Figure 8 shows an example r_D calculation.



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FIGURE 8. Calculating r_D from the V_F vs. I_F Curve

Extending the tangent line to the ends of the plot yields values for ΔV_F and ΔI_F of 0.7V and 2000 mA, respectively. Dynamic resistance is then:

$$r_D = \Delta V_F / \Delta I_F = 0.7V / 2A = 0.35\Omega$$

The most filtering (and therefore the highest output capacitance) is needed when r_D is lowest, which is when there is only one LED. Inductor ripple current with one LED is 478 mA $_{P-P}$. The required impedance of C_O is calculated:

$$Z_C = [0.15 / (0.478 - 0.15)] \times 0.35 = 0.16\Omega$$

A ceramic capacitor will be used and the required capacitance is selected based on the impedance at 362 kHz:

$$C_O = 1 / (2 \times \pi \times 0.16 \times 3.62 \times 10^5) = 2.75 \mu F$$

This calculation assumes that C_O will be a ceramic capacitor, and therefore impedance due to the equivalent series resistance (ESR) and equivalent series inductance (ESL) of the device is negligible. The closest 10% tolerance capacitor value is 3.3 μF . The capacitor used should be rated to 25V or more and have an X7R dielectric. Several manufacturers produce ceramic capacitors with these specifications in the 1206 case size. A typical value for ESR of 3 m Ω can be read from the curve of impedance vs. frequency in the product datasheet.

R_{SNS}

Using the expression for R_{SNS} :

$$R_{SNS} = 0.2 / I_F$$

$$R_{SNS} = 0.2 / 1.5 = 0.133\Omega$$

Sub-1 Ω resistors are available in both 1% and 5% tolerance. A 1%, 0.13 Ω device is the closest value, and a 0.33W, 1210 size device will handle the power dissipation of 290 mW. With the resistance selected, the average value of LED current is re-calculated to ensure that current is within the $\pm 5\%$ tolerance requirement. From the expression for average LED current:

$$I_F = 0.2 / 0.13 = 1.54A, 3\% \text{ above the target current}$$

INPUT CAPACITOR

Following the calculations from the Input Capacitor section, $\Delta v_{IN(MAX)}$ will be $24V \times 2\%_{P-P} = 480$ mV. The minimum required capacitance is calculated for the largest t_{ON} , corresponding to five LEDs:

$$C_{IN(MIN)} = (1.5 \times 1.5 \times 10^{-6}) / 0.48 = 4.7 \mu F$$

As with the output capacitor, this required value is low enough to use a ceramic capacitor, and again the effective capacitance will be lower than the rated value with 24V across C_{IN} . Reviewing plots of %C vs. DC Bias for several capacitors reveals that a 4.7 μF , 1812-size capacitor in X7R rated to 50V loses about 40% of its rated capacitance at 24V, hence two such caps are needed.

Input rms current is high in buck regulators, and the worst-case is when the duty cycle is 50%. Duty cycle in a buck regulator can be estimated as $D = V_O / V_{IN}$, and when this converter drives three LEDs the duty cycle will be nearly 50%.

$$I_{IN-RMS} = 1.5 \times \text{Sqrt}(0.5 \times 0.5) = 750 \text{ mA}$$

Ripple current ratings for 1812 size ceramic capacitors are typically higher than 2A, so two of them in parallel can tolerate more than enough for this design.

RECIRCULATING DIODE

The input voltage of 24V $\pm 5\%$ requires Schottky diodes with a reverse voltage rating greater than 30V. The next highest standard voltage rating is 40V. Selecting a 40V rated diode provides a large safety margin for the ringing of the switch node and also makes cross-referencing of diodes from different vendors easier.

The next parameters to be determined are the forward current rating and case size. The lower the duty cycle the more thermal stress is placed on the recirculating diode. When driving one LED the duty cycle can be estimated as:

$$D = 4.1 / 24 = 0.17$$

The estimated average diode current is then:

$$I_D = (1 - 0.17) \times 1.54 = 1.28A$$

A 2A-rated diode will be used. To determine the proper case size, the dissipation and temperature rise in D1 can be calculated as shown in the Design Considerations section. V_D for a case size such as SMB in a 40V, 2A Schottky diode at 1.5A is approximately 0.4V and the θ_{JA} is 75°C/W. Power dissipation and temperature rise can be calculated as:

$$P_D = 1.28 \times 0.4 = 512 \text{ mW}$$

$$T_{RISE} = 0.51 \times 75 = 38^\circ\text{C}$$

C_B, C_C AND C_F

The bootstrap capacitor C_B should always be a 22 nF ceramic capacitors with X7R dielectric. A 25V rating is appropriate for all application circuits. The COMP pin capacitor C_C and the linear regulator filter capacitor C_F should always be 100 nF ceramic capacitors, also with X7R dielectric and a 25V ratings.

EFFICIENCY

To estimate the electrical efficiency of this example the power dissipation in each current carrying element can be calculated and summed. Electrical efficiency, η , should not be confused with the optical efficacy of the circuit, which depends upon the LEDs themselves. One calculation will be detailed for three LEDs in series, where $V_O = 11.8V$, and these calculations can be repeated for other numbers of LEDs.

Total output power, P_O , is calculated as:

$$P_O = I_F \times V_O = 1.54 \times 11.8 = 18.2W$$

Conduction loss, P_C , in the internal MOSFET:

$$P_C = (I_F^2 \times R_{DS(ON)}) \times D = (1.54^2 \times 0.75) \times 0.5 = 890 \text{ mW}$$

Gate charging and VCC loss, P_G , in the gate drive and linear regulator:

$$P_G = (I_{IN-OP} + f_{SW} \times Q_G) \times V_{IN}$$

$$P_G = (600 \times 10^{-6} + 5 \times 10^5 \times 9 \times 10^{-9}) \times 24 = 122 \text{ mW}$$

Switching loss, P_S , in the internal MOSFET:

$$P_S = 0.5 \times V_{IN} \times I_F \times (t_R + t_F) \times f_{SW}$$

$$P_S = 0.5 \times 24 \times 1.54 \times 40 \times 10^{-9} \times 5 \times 10^5 = 370 \text{ mW}$$

AC rms current loss, P_{CIN} , in the input capacitor:

$$P_{CIN} = I_{IN(rms)}^2 \times ESR = 0.75^2 \times 0.003 = 2 \text{ mW (negligible)}$$

DCR loss, P_L , in the inductor

$$P_L = I_F^2 \times DCR = 1.54^2 \times 0.06 = 142 \text{ mW}$$

Recirculating diode loss, $P_D = (1 - 0.5) \times 1.54 \times 0.4 = 300 \text{ mW}$

Current Sense Resistor Loss, $P_{SNS} = 293 \text{ mW}$

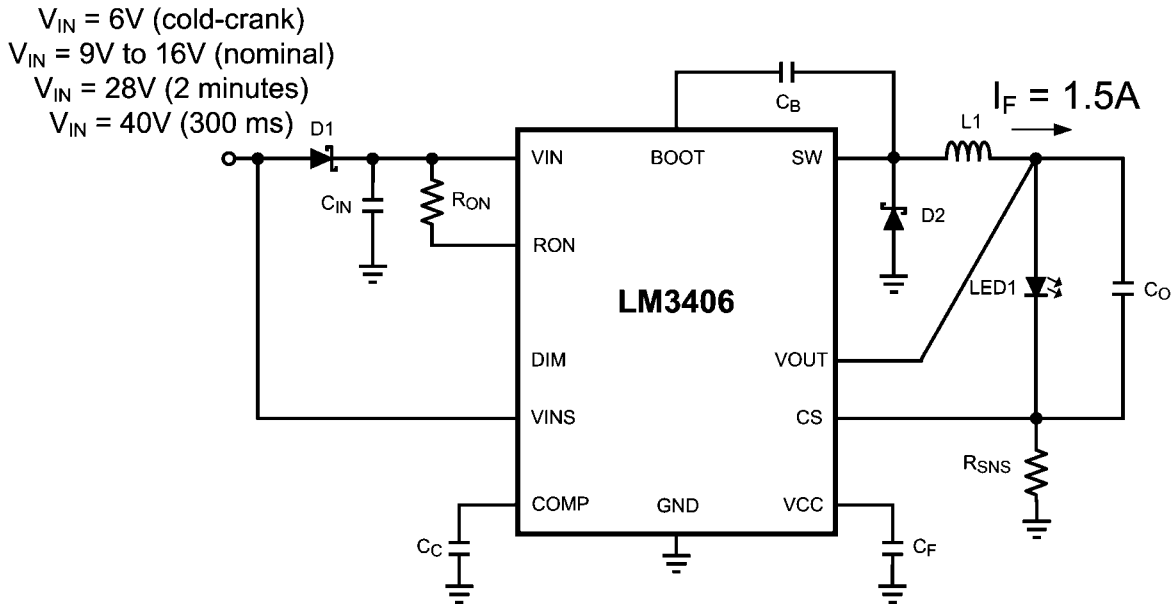
Electrical efficiency, $\eta = P_O / (P_O + \text{Sum of all loss terms}) = 18.2 / (18.2 + 2.1) = 89\%$

Temperature Rise in the LM3406 IC is calculated as:

$$T_{LM3406} = (P_C + P_G + P_S) \times \theta_{JA} = (0.89 + 0.122 + 0.37) \times 50 = 69^\circ\text{C}$$

Design Example 2:

The second example circuit uses the LM3406 to drive a single white LED at 1.5A $\pm 10\%$ with a ripple current of 20%_{p-p} in a typical 12V automotive electrical system. The two-wire dimming function will be employed in order to take advantage of the legacy 'theater dimming' method which dims and brightens the interior lights of automobiles by chopping the input voltage with a 200Hz PWM signal. As with the previous example, the typical V_F of a white LED is 3.9V, and with the current sense voltage of 0.2V the total output voltage will be 4.1V. The LED driver must operate to specifications over an input range of 9V to 16V as well as operating without suffering damage at 28V for two minutes (the 'double battery jump-start' test) and for 300 ms at 40V (the 'load-dump' test). The LED driver must also be able to operate without suffering damage at inputs as low as 6V to satisfy the 'cold crank' tests. A complete bill of materials can be found in *Table 2* at the end of this datasheet.



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FIGURE 9. Schematic for Design Example 2

 R_{ON} and t_{ON}

A switching frequency of 450 kHz helps balance the requirements of inductor size and overall power efficiency, but more importantly keeps the switching frequency below 530 kHz, where the AM radio band begins. This design will concentrate on meeting the switching frequency and LED current requirements over the nominal input range of 9V to 16V, and will then check to ensure that the transient conditions do not cause the LM3406 to overheat. The LM3406 will allow a small shift in switching frequency when V_{IN} changes, so the calculation for R_{ON} is done at the typical expected condition where $V_{IN} = 13.8V$ and $V_O = 4.1V$. The actual R_{ON} calculation uses the high accuracy equation listed in the Appendix.

$$R_{ON} = \frac{1}{f_{SW} \times 1 \times 10^{-11}}$$

$$R_{ON} = 124 \text{ k}\Omega$$

The closest 1% tolerance resistor is 124 k Ω . The switching frequency and on-time of the circuit should be checked at V_{IN-MIN} and V_{IN-MAX} which are 9V and 16V, respectively. The actual f_{SW} and t_{ON} values have been calculated with the high accuracy equations in the Appendix.

$$f_{SW} = \frac{1}{1 \times 10^{-11} \times R_{ON}}$$

$$f_{SW(VMIN)} = 463 \text{ kHz}$$

$$f_{SW(VMAX)} = 440 \text{ kHz}$$

$$t_{ON} = 1 \times 10^{-11} \times R_{ON} \times \frac{V_O}{V_{IN}}$$

$$t_{ON(VMIN)} = 1090 \text{ ns}$$

$$t_{ON(VMAX)} = 650 \text{ ns}$$

OUTPUT INDUCTOR

Since an output capacitor will be used to filter some of the LED ripple current, the inductor ripple current can be set higher than the LED ripple current. A value of 40% $_{P-P}$ is typical in many buck converters:

$$\Delta i_L = 0.4 \times 1.5 = 0.6 A_{P-P}$$

The minimum inductance required to ensure a ripple current of 600 mA $_{P-P}$ or less is calculated at V_{IN-MAX} :

$$L = \frac{V_{IN} - V_O}{\Delta i_L} \times t_{ON}$$

$$L_{MIN} = [(16 - 4.1) \times 6.5 \times 10^{-7}] / (0.6) = 12.9 \mu H$$

The closest standard inductor value is 15 μH . The average current rating should be greater than 1.5A to prevent overheating in the inductor. Inductor current ripple should be calculated for V_{IN-MIN} and V_{IN-MAX} :

$$\Delta i_{L(VMIN)} = [(9 - 4.1) \times 6.5 \times 10^{-7}] / 15 \times 10^{-6} = 357 \text{ mA}_{P-P}$$

$$\Delta i_{L(VMAX)} = [(16 - 4.1) \times 1.09 \times 10^{-6}] / 15 \times 10^{-6} = 516 \text{ mA}_{P-P}$$

The peak LED/inductor current is then estimated. This calculation uses the worst-case ripple current which occurs at $V_{IN,MAX}$.

$$I_{L(PEAK)} = I_L + 0.5 \times \Delta i_{L(MAX)}$$

$$I_{L(PEAK)} = 1.5 + 0.5 \times 0.516 = 1.76A$$

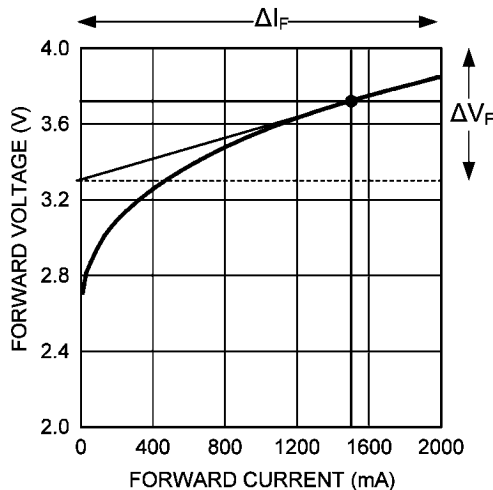
In order to prevent inductor saturation the inductor's peak current rating must be above 1.8A. A 15 μH off-the shelf inductor rated to 2.4A (peak) and 2.2A (average) with a DCR of 47 m Ω will be used.

USING AN OUTPUT CAPACITOR

This application does not require high frequency PWM dimming, allowing the use of an output capacitor to reduce the size and cost of the output inductor while still meeting the 20% P_{P-P} (300 mA) target for LED ripple current. To select the proper output capacitor the equation from Buck Regulators with Output Capacitors is re-arranged to yield the following:

$$Z_C = \frac{\Delta i_F}{\Delta i_L - \Delta i_F} \times r_D$$

The dynamic resistance, r_D , of one LED can be calculated by taking the tangent line to the V_F vs. I_F curve in the LED datasheet. Figure 8 shows an example r_D calculation.



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FIGURE 10. Calculating r_D from the V_F vs. I_F Curve

Extending the tangent line to the ends of the plot yields values for ΔV_F and ΔI_F of 0.7V and 2000 mA, respectively. Dynamic resistance is then:

$$r_D = \Delta V_F / \Delta I_F = 0.7V / 2A = 0.35\Omega$$

The most filtering (and therefore the highest output capacitance) is needed when ΔI_L is highest, which occurs at $V_{IN,MAX}$.

MAX : Inductor ripple current with one LED is 516 mA $_{P-P}$. The required impedance of C_O is calculated:

$$Z_C = [0.3 / (0.516 - 0.3)] \times 0.35 = 0.49\Omega$$

A ceramic capacitor will be used and the required capacitance is selected based on the impedance at 440 kHz:

$$C_O = 1 / (2 \times \pi \times 0.49 \times 4.4 \times 10^5) = 0.74 \mu F$$

This calculation assumes that C_O will be a ceramic capacitor, and therefore impedance due to the equivalent series resistance (ESR) and equivalent series inductance (ESL) of the device is negligible. The closest 10% tolerance capacitor value is 1 μF . The capacitor used should have an X7R dielectric and should be rated to 50V. The high voltage rating ensures that C_O will not be damaged if the LED fails open circuit and a load dump occurs. Several manufacturers produce ceramic capacitors with these specifications in the 1206 case size. With only 4V of DC bias a 50V rated ceramic capacitor will have better than 90% of its rated capacitance, which is more than enough for this design.

R_{SNS}

Using the expression for R_{SNS} :

$$R_{SNS} = 0.2 / I_F$$

$$R_{SNS} = 0.2 / 1.5 = 0.133\Omega$$

Sub-1 Ω resistors are available in both 1% and 5% tolerance. A 1%, 0.13 Ω device is the closest value, and a 0.33W, 1210 size device will handle the power dissipation of 290 mW. With the resistance selected, the average value of LED current is re-calculated to ensure that current is within the $\pm 5\%$ tolerance requirement. From the expression for average LED current:

$$I_F = 0.2 / 0.13 = 1.54A, 3\% \text{ above the target current}$$

INPUT CAPACITOR

Controlling input ripple current and voltage is critical in automotive applications where stringent conducted electromagnetic interference tests are required. $\Delta v_{IN(MAX)}$ will be limited to 300 mV $_{P-P}$ or less. The minimum required capacitance is calculated for the largest t_{ON} , 1090 ns, which occurs at the minimum input voltage. Using the equations from the Input Capacitors section:

$$C_{IN(MIN)} = (1.5 \times 1.09 \times 10^{-6}) / 0.3 = 5.5 \mu F$$

As with the output capacitor, this required value is low enough to use a ceramic capacitor, and again the effective capacitance will be lower than the rated value with 16V across C_{IN} . Reviewing plots of %C vs. DC Bias for several capacitors reveals that a 3.3 μF , 1210-size capacitor in X7R rated to 50V loses about 22% of its rated capacitance at 16V, hence two such caps are needed.

Input rms current is high in buck regulators, and the worst-case is when the duty cycle is 50%. Duty cycle in a buck regulator can be estimated as $D = V_O / V_{IN}$, and when V_{IN} drops to 9V the duty cycle will be nearly 50%.

$$I_{IN-RMS} = 1.5 \times \text{Sqrt}(0.5 \times 0.5) = 750 \text{ mA}$$

Ripple current ratings for 1210 size ceramic capacitors are typically higher than 2A, so two of them in parallel can tolerate more than enough for this design.

RECIRCULATING DIODE

To survive an input voltage transient of 40V the Schottky diode must be rated to a higher voltage. The next highest standard voltage rating is 60V. Selecting a 60V rated diode provides a large safety margin for the ringing of the switch node and also makes cross-referencing of diodes from different vendors easier.

The next parameters to be determined are the forward current rating and case size. The lower the duty cycle the more thermal stress is placed on the recirculating diode. When driving one LED the duty cycle can be estimated as:

$$D = 4.1 / 13.8 = 0.3$$

The estimated average diode current is then:

$$I_D = (1 - 0.3) \times 1.54 = 1.1 \text{ A}$$

A 2A-rated diode will be used. To determine the proper case size, the dissipation and temperature rise in D1 can be calculated as shown in the Design Considerations section. V_D for a case size such as SMB in a 60V, 2A Schottky diode at 1.5A is approximately 0.4V and the θ_{JA} is 75°C/W. Power dissipation and temperature rise can be calculated as:

$$P_D = 1.1 \times 0.4 = 440 \text{ mW}$$

$$T_{RISE} = 0.44 \times 75 = 33^\circ\text{C}$$

C_B, C_C AND C_F

The bootstrap capacitor C_B should always be a 22 nF ceramic capacitors with X7R dielectric. A 25V rating is appropriate for all application circuits. The COMP pin capacitor C_C and the linear regulator filter capacitor C_F should always be 100 nF ceramic capacitors, also with X7R dielectric and a 25V ratings.

EFFICIENCY

To estimate the electrical efficiency of this example the power dissipation in each current carrying element can be calculated and summed. One calculation will be detailed for the nominal input voltage of 13.8V, and these calculations can be repeated for other numbers of LEDs.

Total output power, P_O , is calculated as:

$$P_O = I_F \times V_O = 1.54 \times 4.1 = 6.3 \text{ W}$$

Conduction loss, P_C , in the internal MOSFET:

$$P_C = (I_F^2 \times R_{DS(ON)}) \times D = (1.54^2 \times 0.75) \times 0.3 = 530 \text{ mW}$$

Gate charging and VCC loss, P_G , in the gate drive and linear regulator:

$$P_G = (I_{IN-OP} + f_{SW} \times Q_G) \times V_{IN}$$

$$P_G = (600 \times 10^{-6} + 4.5 \times 10^5 \times 9 \times 10^{-9}) \times 13.8 = 64 \text{ mW}$$

Switching loss, P_S , in the internal MOSFET:

$$P_S = 0.5 \times V_{IN} \times I_F \times (t_R + t_F) \times f_{SW}$$

$$P_S = 0.5 \times 13.8 \times 1.54 \times 40 \times 10^{-9} \times 4.5 \times 10^5 = 190 \text{ mW}$$

AC rms current loss, P_{CIN} , in the input capacitor:

$$P_{CIN} = I_{IN(rms)}^2 \times ESR = 0.75^2 \times 0.003 = 2 \text{ mW (negligible)}$$

DCR loss, P_L , in the inductor

$$P_L = I_F^2 \times DCR = 1.54^2 \times 0.05 = 120 \text{ mW}$$

Recirculating diode loss, $P_D = (1 - 0.3) \times 1.54 \times 0.4 = 430 \text{ mW}$

Current Sense Resistor Loss, $P_{SNS} = 293 \text{ mW}$

Electrical efficiency, $\eta = P_O / (P_O + \text{Sum of all loss terms}) = 6.3 / (6.3 + 1.6) = 80\%$

Temperature Rise in the LM3406 IC is calculated as:

$$T_{LM3406} = (P_C + P_G + P_S) \times \theta_{JA} = (0.53 + 0.06 + 0.19) \times 50 = 39^\circ\text{C}$$

Thermal Considerations During Input Transients

The error amplifier of the LM3406 ensures that average LED current is controlled even at the transient load-dump voltage of 40V, leaving thermal considerations as a primary design consideration during high voltage transients. A review of the operating conditions at an input of 40V is still useful to make sure that the LM3406 die temperature is not exceeded. Switching frequency drops to 325 kHz, the on-time drops to 350 ns, and the duty cycle drops to 0.12. Repeating the calculations for conduction, gate charging and switching loss leads to a total internal loss of 731 mW, and hence a die temperature rise of 37°C. The LM3406 should operate properly even if the ambient temperature is as high as 85°C.

Layout Considerations

The performance of any switching converter depends as much upon the layout of the PCB as the component selection. The following guidelines will help the user design a circuit with maximum rejection of outside EMI and minimum generation of unwanted EMI.

COMPACT LAYOUT

Parasitic inductance can be reduced by keeping the power path components close together and keeping the area of the loops that high currents travel small. Short, thick traces or copper pours (shapes) are best. In particular, the switch node (where L1, D1, and the SW pin connect) should be just large enough to connect all three components without excessive heating from the current it carries. The LM3406/06HV operates in two distinct cycles whose high current paths are shown in *Figure 11*:

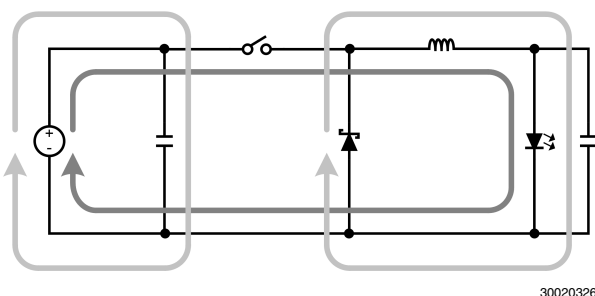


FIGURE 11. Buck Converter Current Loops

The dark grey, inner loop represents the high current path during the MOSFET on-time. The light grey, outer loop represents the high current path during the off-time.

GROUND PLANE AND SHAPE ROUTING

The diagram of *Figure 11* is also useful for analyzing the flow of continuous current vs. the flow of pulsating currents. The circuit paths with current flow during both the on-time and off-time are considered to be continuous current, while those that carry current during the on-time or off-time only are pulsating currents. Preference in routing should be given to the pulsating current paths, as these are the portions of the circuit most likely to emit EMI. The ground plane of a PCB is a conductor and return path, and it is susceptible to noise injection just as any other circuit path. The continuous current paths on the ground net can be routed on the system ground plane with less risk of injecting noise into other circuits. The path between the input source and the input capacitor and the path between the recirculating diode and the LEDs/current sense resistor are examples of continuous current paths. In contrast, the path between the recirculating diode and the input capacitor carries a large pulsating current. This path should be routed with a short, thick shape, preferably on the component side of the PCB. Do not place any vias near the anode of Schottky diode. Instead, multiple vias in parallel should be used right at the pad of the input capacitor to connect the component side shapes to the ground plane. A second pulsating current loop that is often ignored is the gate drive loop formed by the SW and BOOT pins and capacitor C_B . To minimize this loop and the EMI it generates, keep C_B close to the SW and BOOT pins.

CURRENT SENSING

The CS pin is a high-impedance input, and the loop created by R_{SNS} , R_Z (if used), the CS pin and ground should be made

as small as possible to maximize noise rejection. R_{SNS} should therefore be placed as close as possible to the CS and GND pins of the IC.

REMOTE LED ARRAYS

In some applications the LED or LED array can be far away (several inches or more) from the LM3406/06HV, or on a separate PCB connected by a wiring harness. When an output capacitor is used and the LED array is large or separated from the rest of the converter, the output capacitor should be placed close to the LEDs to reduce the effects of parasitic inductance on the AC impedance of the capacitor. The current sense resistor should remain on the same PCB, close to the LM3406/06HV.

Remote LED arrays and high speed dimming with a parallel FET must be treated with special care. The parallel dimming FET should be placed on the same board and/or heatsink as the LEDs to minimize the loop area between them, as the switching of output current by the parallel FET produces a pulsating current just like the switching action of the LM3406's internal power FET and the Schottky diode. *Figure 12* shows the path that the inductor current takes through the LED or through the dimming FET. To minimize the EMI from parallel FET dimming the parasitic inductance of the loop formed by the LED and the dimming FET (where only the dark grey arrows appear) should be reduced as much as possible. Parasitic inductance of a loop is mostly controlled by the loop area, hence making this loop as physically small (short) as possible will reduce the inductance.

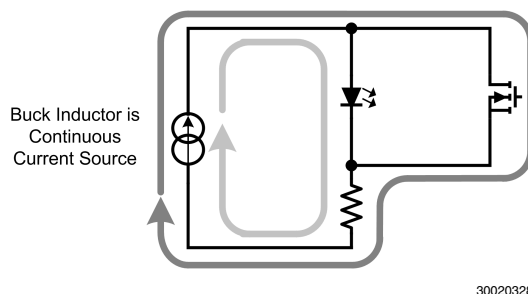


FIGURE 12. Parallel FET Dimming Current Loops

TABLE 1. BOM for Design Example 1

ID	Part Number	Type	Size	Parameters	Qty	Vendor
U1	LM3406	LED Driver	eTSSOP-14	42V, 2A	1	NSC
L1	SLF10145T-220M1R-PF	Inductor	10 x 10 x 4.5mm	22 μ H, 1.9A, 59 m Ω	1	TDK
D1	CMSH2-40	Schottky Diode	SMB	40V, 2A	1	Central Semi
Cc, Cf	VJ0603Y104KXXAT	Capacitor	0603	100 nF 10%	2	Vishay
Cb	VJ0603Y223KXXAT	Capacitor	0603	22 nF 10%	1	Vishay
Cin1 Cin2	C4532X7R1H475M	Capacitor	1812	4.7 μ F, 50V	2	TDK
Co	C2012X7R1E105M	Capacitor	0805	1.0 μ F, 25V	1	TDK
Rsns	ERJ14RQFR13V	Resistor	1210	0.13 Ω 1%	1	Panasonic
Ron	CRCW08051433F	Resistor	0805	143 k Ω 1%	1	Vishay

TABLE 2. BOM for Design Example 2

ID	Part Number	Type	Size	Parameters	Qty	Vendor
U1	LM3406	LED Driver	eTSSOP-14	42V, 2A	1	NSC
L1	SLF10145T-150M2R2-P	Inductor	10 x 10 x 4.5mm	15 μ H, 2.2A, 47 m Ω	1	TDK
D1	CMSH2-60	Schottky Diode	SMB	60V, 2A	1	Central Semi
Cc, Cf	VJ0603Y104KXXAT	Capacitor	0603	100 nF 10%	2	Vishay
Cb	VJ0603Y223KXXAT	Capacitor	0603	22 nF 10%	1	Vishay
Cin1 Cin2	C3225X7R1H335M	Capacitor	1210	3.3 μ F, 50V	2	TDK
Co	C3216X7R1H105M	Capacitor	1206	0.15 μ F, 50V	1	TDK
Rsns	ERJ14RQFR13V	Resistor	1210	0.13 Ω 1%	1	Panasonic
Ron	CRCW08051243F	Resistor	0805	124 k Ω 1%	1	Vishay
Rpd	CRCW08051002F	Resistor	0805	10 k Ω 1%	1	Vishay

TABLE 3. Bill of Materials for Efficiency Curves

ID	Part Number	Type	Size	Parameters	Qty	Vendor
U1	LM3406	Buck LED Driver	eTSSOP-14	42V, 1.5A	1	NSC
Q1	Si3458DV	N-MOSFET	SOT23-6	60V, 2.8A	1	Vishay
D1	CMSH2-60M	Schottky Diode	SMA	60V, 2A	1	Central Semi
L1	VLF10045T-330M2R3	Inductor	10 x 10 x 4.5mm	33 μ H, 2.3A, 70 m Ω	1	TDK
Cin1 Cin2	C4532X7R1H685M	Capacitor	1812	6.8 μ F, 50V	2	TDK
Co	C3216X7R1H474M	Capacitor	1206	470 nF, 50V	1	TDK
Cf ,Cc	VJ0603Y104KXXAT	Capacitor	0603	100 nF 10%	2	Vishay
Cb	VJ0603Y223KXXAT	Capacitor	0603	22 nF 10%	1	Vishay
R3.5	ERJ6RQFR56V	Resistor	0805	0.56 Ω 1%	1	Panasonic
R.7	ERJ6RQFR62V	Resistor	0805	0.62 Ω 1%	1	Panasonic
R1	ERJ6RQFR30V	Resistor	0805	0.3 Ω 1%	1	Panasonic
R1.5	ERJ6RQFR16V	Resistor	0805	0.16 Ω 1%	1	Panasonic
Ron	CRCW08051433F	Resistor	0805	143k Ω 1%	1	Vishay
Rpd Rout	CRCW06031002F	Resistor	0603	10 k Ω 1%	2	Vishay
OFF* DIM1 DIM2	160-1512	Terminal	0.062"		3	Cambion
VIN GND CS/LED- Vo/LED+	160-1026	Terminal	0.094"		2	Cambion

Appendix

The following expressions provide the best accuracy for users who wish to create computer-based simulations or circuit calculators:

$$t_{ON} = \frac{9.92 \times 10^{-12} \times (V_O + 0.65) \times R_{ON}}{V_{IN} - 1.5} + 1.75 \times 10^{-7}$$

$$R_{ON} = \frac{(D - f_{SW} \times 1.75 \times 10^{-7}) \times (V_{IN} - 1.5)}{9.92 \times 10^{-12} \times f_{SW} \times (V_O + 0.65)}$$

$$f_{SW} = \frac{D \times (V_{IN} - 1.5)}{9.92 \times 10^{-12} \times (V_O + 0.65) \times R_{ON} + 1.75 \times 10^{-7} \times (V_{IN} - 1.5)}$$

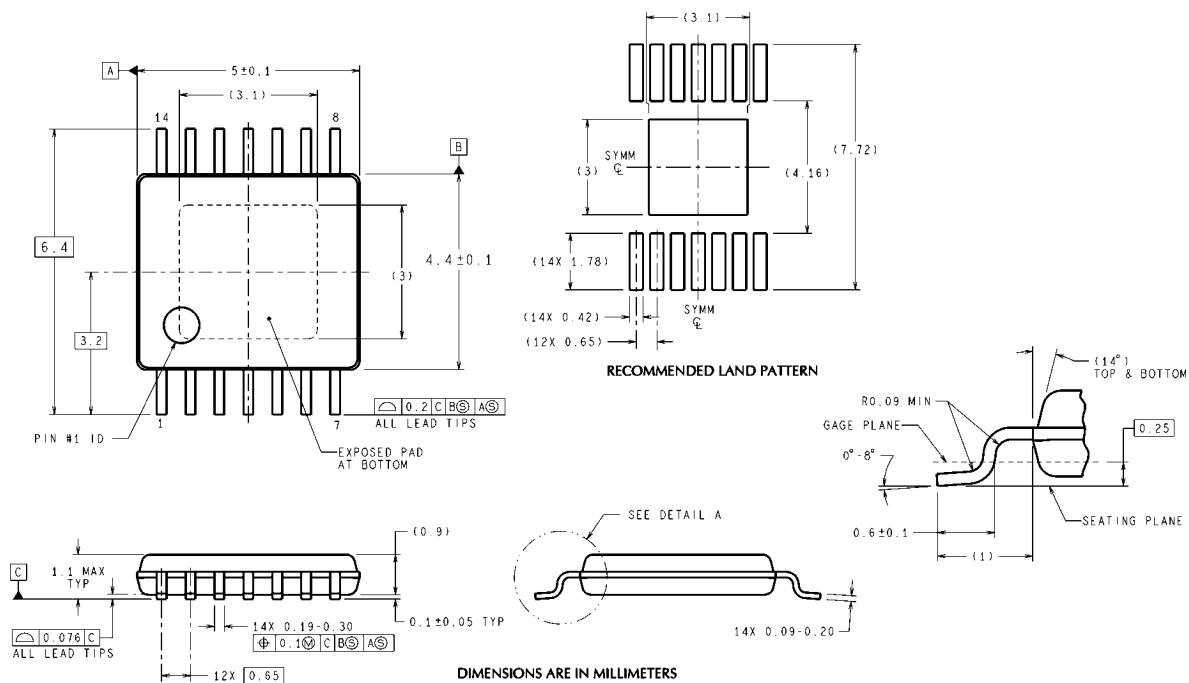
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14-Lead Exposed Pad Plastic TSSOP Package
NS Package Number MXA14A

MXA14A (Rev A)

Notes

Notes

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