LM431

LM431 Adjustable Precision Zener Shunt Regulator



Literature Number: SNVS020F

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LM431

Adjustable Precision Zener Shunt Regulator

General Description

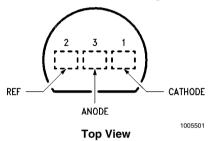
The LM431 is a 3-terminal adjustable shunt regulator with guaranteed temperature stability over the entire temperature range of operation. The output voltage may be set at any level greater than 2.5V (V_{REF}) up to 36V merely by selecting two external resistors that act as a voltage divided network. Due to the sharp turn-on characteristics this device is an excellent replacement for many zener diode applications.

Features

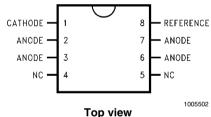
- Average temperature coefficient 50 ppm/°C
- Temperature compensated for operation over the full temperature range
- Programmable output voltage
- Fast turn-on response
- Low output noise

Connection Diagrams

TO-92: Plastic Package

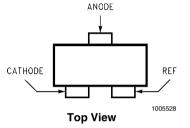


SO-8: 8-Pin Surface Mount



Note: NC = Not internally connected.

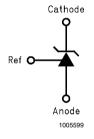
SOT-23: 3-Lead Small Outline

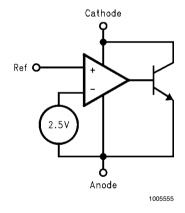


Ordering Information

Package	Typical Accuracy Order Number/Package Marking			Temperature	Transport	NSC
	0.5%	1%	2%	Range	Media	Drawing
TO-92	LM431CCZ/	LM431BCZ/	LM431ACZ/	0°C to +70°C		Z03A
	LM431CCZ	LM431BCZ	LM431ACZ	0 0 10 +70 0	Rails	
	LM431CIZ/	LM431BIZ/	LM431AIZ/	-40°C to +85°C	rians	
	LM431CIZ	LM431BIZ	LM431AIZ	-40 C to +65 C		
SO-8	LM431CCM/	LM431BCM/	LM431ACM/		Rails	- M08A
	431CCM	431BCM	LM431ACM	0°C to +70°C		
	LM431CCMX/	LM431BCMX/	LM431ACMX/	0 0 10 +70 0	Tape & Reel	
	431CCM	431BCM	LM431ACM			
	LM431CIM/	LM431BIM/	LM431AIM/		Rails	
	431CIM	431BIM	LM431AIM	-40°C to +85°C	nalis	
	LM431CIMX/	LM431BIMX/	LM431AIMX/	-40 C to +65 C	Tape &Reel	
	431CIM	431BIM	LM431AIM		Tape arteer	
SOT-23	LM431CCM3/	LM431BCM3/	LM431ACM3/		Rails	- MF03A
	N1B	N1D	N1F	0°C to +70°C	nalis	
	LM431CCM3X/	LM431BCM3X/	LM431ACM3X/	0 0 10 +70 0	Tape & Reel	
	N1B	N1D	N1F		rape & neer	
	LM431CIM3	LM431BIM3	LM431AIM3		Rails	
	N1A	N1C	N1E	-40°C to +85°C	rialis	
	LM431CIM3X	LM431BIM3X	LM431AIM3X	-40 C 10 +65 C	Tape &Reel	
	N1A	N1C	N1E		i ape di leel	

Symbol and Functional Diagrams





DC Test Circuits

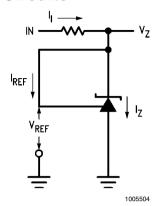
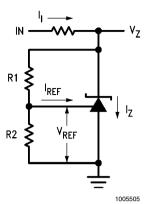


FIGURE 1. Test Circuit for $V_Z = V_{REF}$



Note: $V_Z = V_{REF} (1 + R1/R2) + I_{REF} R1$

FIGURE 2. Test Circuit for $V_Z > V_{REF}$

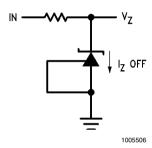


FIGURE 3. Test Circuit for Off-State Current

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Storage Temperature Range -65°C to +150°C

Operating Temperature Range

Industrial (LM431xI) -40° C to $+85^{\circ}$ C Commercial (LM431xC) 0° C to $+70^{\circ}$ C

Soldering Information

Infrared or Convection (20 sec.)

Wave Soldering (10 sec.)

Cathode Voltage

Continuous Cathode Current

Reference Voltage

Reference Input Current

260°C (lead temp.)

-10 mA to +150 mA

-0.5V

Reference Input Current

10 mA

Internal Power Dissipation (Note 2,

Note 3)

 TO-92 Package
 0.78W

 SO-8 Package
 0.81W

 SOT-23 Package
 0.28W

Operating Conditions

LM431 Electrical Characteristics

T_A = 25°C unless otherwise specified

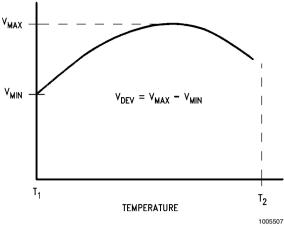
Symbol	Parameter		Conditions	Min	Тур	Max	Units
V _{REF}	Reference Voltage	$V_Z = V_{REF}$, $I_I = 10 \text{ mA}$		2.440	2.495	2.550	V
		LM431A (Figure 1)					
		$V_Z = V_{REF}$, $I_I = 10 \text{ mA}$		2.470	2.495	2.520	V
		LM431B (Figure 1) $V_{Z} = V_{REF}, I_{I} = 10 \text{ mA}$					
				2.485	2.500	2.510	V
	LM431C (Figure 1)		ure 1)				
V _{DEV}	Deviation of Reference Input Voltage Over	ge Over $V_Z = V_{REF}$, $I_I = 10$ mA, $T_A = Full Range (Figure 1)$			8.0	17	mV
	Temperature (Note 4)						
ΔV_{REF}	Ratio of the Change in Reference Voltage	I _Z = 10 mA	V _Z from V _{REF} to 10V		-1.4	-2.7	mV/V
ΔV_Z	to the Change in Cathode Voltage	(Figure 2)	V _Z from 10V to 36V		-1.0	-2.0	
I _{REF}	Reference Input Current	$R_1 = 10 \text{ k}\Omega, F$	$R_2 = \infty$,		2.0	4.0	μΑ
		I _I = 10 mA <i>(Figure 2)</i>					
I _{REF}	Deviation of Reference Input Current over	$R_1 = 10 \text{ k}\Omega, R_2 = \infty,$					
	Temperature	I _I = 10 mA,			0.4	1.2	μΑ
		T _A = Full Range <i>(Figure 2)</i>					
I _{Z(MIN)}	Minimum Cathode Current for Regulation	V _Z = V _{REF} (Figure 1)			0.4	1.0	mA
I _{Z(OFF)}	Off-State Current	$V_Z = 36V$, $V_{REF} = 0V$ (<i>Figure 3</i>)			0.3	1.0	μΑ
r _Z	Dynamic Output Impedance (Note 5)	$V_Z = V_{REF}$, LM431A,				0.75	Ω
		Frequency = 0 Hz (Figure 1)					
		$V_Z = V_{REF}$, LM431B, LM431C				0.50	Ω
		Frequency =	0 Hz <i>(Figure 1)</i>				

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Electrical specifications do not apply when operating the device beyond its rated operating conditions.

Note 2: $T_{J \text{ Max}} = 150^{\circ}\text{C}$.

Note 3: Ratings apply to ambient temperature at 25°C. Above this temperature, derate the TO-92 at 6.2 mW/°C, the SO-8 at 6.5 mW/°C, the SOT-23 at 2.2 mW/°C.

Note 4: Deviation of reference input voltage, V_{DEV}, is defined as the maximum variation of the reference input voltage over the full temperature range.



The average temperature coefficient of the reference input voltage, V_{REF} , is defined as:

$${_{\infty}V_{REF}}\frac{ppm}{{^{\circ}C}} = \frac{\pm \left[\frac{V_{Max} - V_{Min}}{V_{REF} \left(at \ 25^{\circ}C\right)}\right] 10^{6}}{T_{2} - T_{1}} = \frac{\pm \left[\frac{V_{DEV}}{V_{REF} \left(at \ 25^{\circ}C\right)}\right] 10^{6}}{T_{2} - T_{1}}$$

Where:

 $T_2 - T_1 = \text{full temperature change (0-70°C)}.$

 $\ensuremath{V_{\text{REF}}}$ can be positive or negative depending on whether the slope is positive or negative.

Example: $V_{DEV} = 8.0$ mV, $V_{REF} = 2495$ mV, $T_2 - T_1 = 70$ °C, slope is positive.

$$\propto V_{REF} = \frac{\left[\frac{8.0 \text{ mV}}{2495 \text{ mV}}\right] 10^6}{70^{\circ}\text{C}} = +46 \text{ ppm/}^{\circ}\text{C}$$

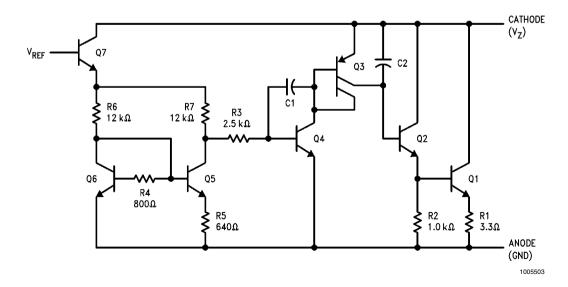
Note 5: The dynamic output impedance, r_Z , is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

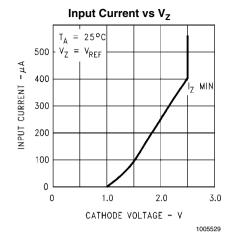
When the device is programmed with two external resistors, R1 and R2, (see $Figure\ 2$), the dynamic output impedance of the overall circuit, r_Z , is defined as:

$$r_Z = \frac{\Delta V_Z}{\Delta I_Z} \cong \left[r_Z \left(1 + \frac{R1}{R2} \right) \right]$$

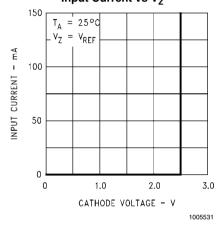
Equivalent Circuit



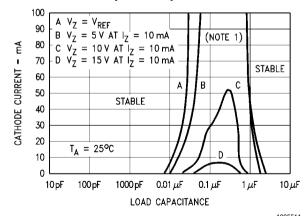
Typical Performance Characteristics



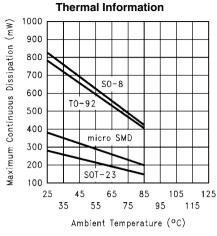




Stability Boundary Conditions

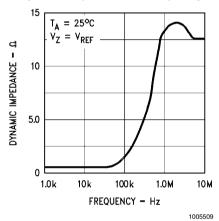


Note: The areas under the curves represent conditions that may cause the device to oscillate. For curves B, C, and D, R2 and V+ were adjusted to establish the initial V_Z and I_Z conditions with C_L = 0. V+ and C_L were then adjusted to determine the ranges of stability.



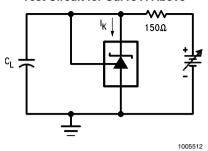
1005530

Dynamic Impedance vs Frequency

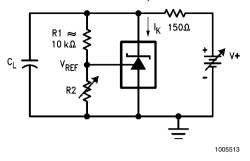


1.0 kΩ
50Ω
1005510

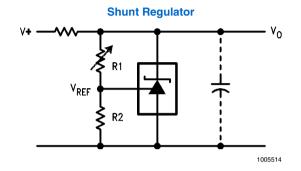
Test Circuit for Curve A Above



Test Circuit for Curves B, C and D Above

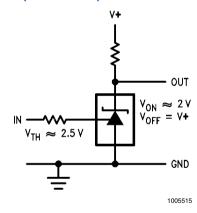


Typical Applications

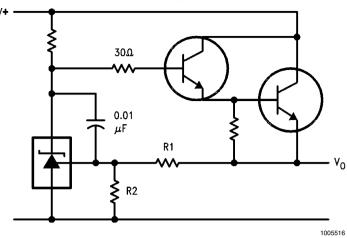


$$V_{O} \approx \left(1 + \frac{R1}{R2}\right) V_{REF}$$

Single Supply Comparator with Temperature Compensated Threshold

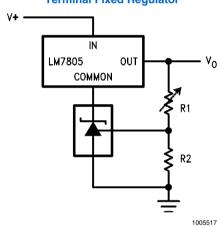


Series Regulator



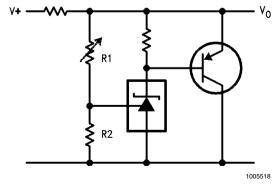
 $V_O \approx \left(1 + \frac{R1}{R2}\right) V_{REF}$

Output Control of a Three Terminal Fixed Regulator

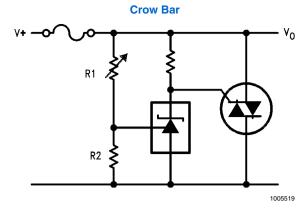


$$\begin{aligned} V_O &= \left(1 + \frac{R1}{R2}\right) V_{REF} \\ V_{O\ MIN} &= V_{REF} \,+\, 5V \end{aligned}$$

Higher Current Shunt Regulator

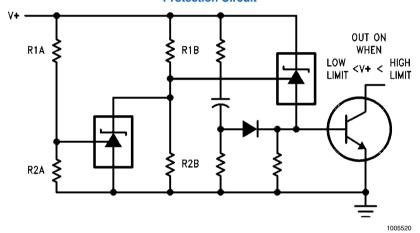


$$V_O \approx \left(1 + \frac{R1}{R2}\right) V_{REF}$$

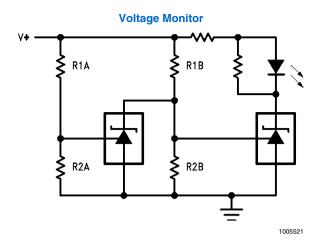


$$V_{LIMIT} \approx \bigg(\ 1\ + \frac{R1}{R2}\bigg) V_{REF}$$

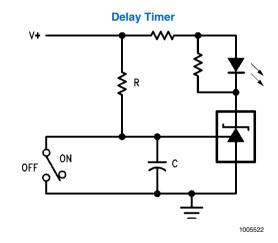
Over Voltage/Under Voltage Protection Circuit



$$\begin{split} & \text{LOW LIMIT} \approx \text{V}_{\text{REF}} \left(1 + \frac{\text{R1B}}{\text{R2B}} \right) + \text{V}_{\text{BE}} \\ & \text{HIGH LIMIT} \approx \text{V}_{\text{REF}} \left(1 + \frac{\text{R1A}}{\text{R2A}} \right) \end{split}$$

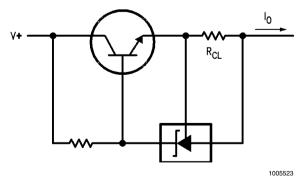


$$\begin{split} \text{LOW LIMIT} &\approx \text{V}_{\text{REF}} \left(1 + \frac{\text{R1B}}{\text{R2B}} \right) \quad \begin{array}{l} \text{LED ON WHEN} \\ \text{LOW LIMIT} &< \text{V}^+ < \text{HIGH LIMIT} \\ \\ \text{HIGH LIMIT} &\approx \text{V}_{\text{REF}} \left(1 + \frac{\text{R1A}}{\text{R2A}} \right) \end{split}$$



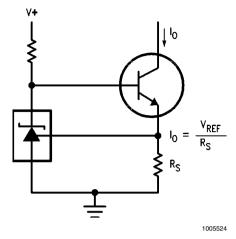
$$\mathsf{DELAY} = \mathsf{R} \bullet \mathsf{C} \bullet \, \ell n \, \frac{\mathsf{V} +}{(\mathsf{V}^+) - \mathsf{V}_{\mathsf{REF}}}$$

Current Limiter or Current Source

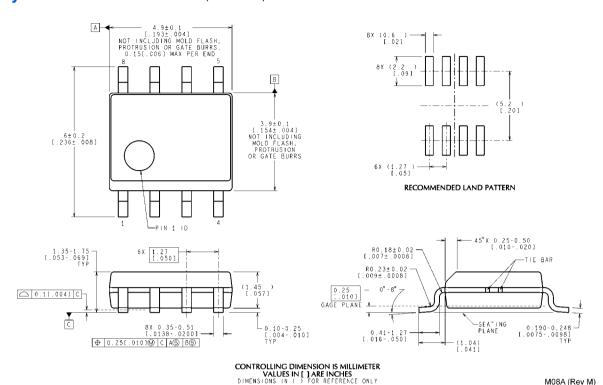


$$I_{O} = \frac{V_{REF}}{R_{CI}}$$

Constant Current Sink

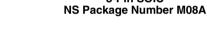


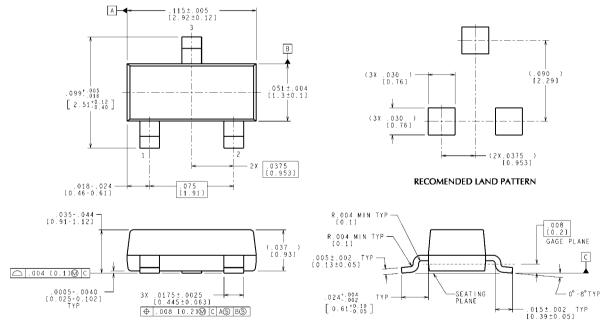
Physical Dimensions inches (millimeters) unless otherwise noted



8-Pin SOIC

M08A (Rev M)

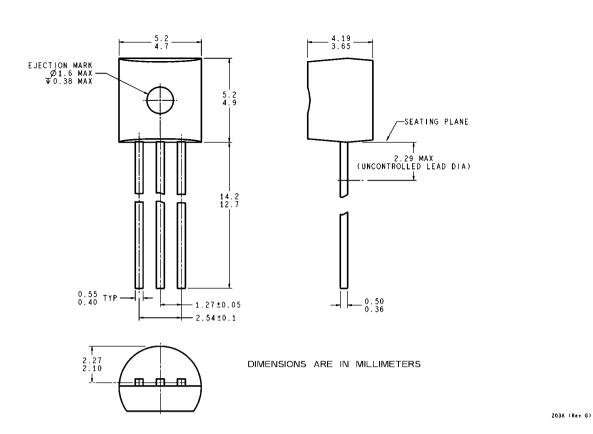




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MF03A (Rev B)

SOT-23 Molded Small Outline Transistor Package (M3) **NS Package Number MF03A**



NS Package Number Z03A

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