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LED	DISPI	$\mathbf{A}\mathbf{Y}$

LTS-4817SW-P DATA SHEET

<u>ITEM</u>	Description	By	DATE
1	New Spec	Reo Lin	2011/08/24

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FEATURES

- *0.39 inch (10.0 mm) DIGIT HEIGHT
- *CONTINUOUS UNIFORM SEGMENTS
- ***LOW POWER REQUIREMENT**
- *EXCELLENT CHARACTERS APPEARANCE
- *HIGH BRIGHTNESS & HIGH CONTRAST
- *WIDE VIEWING ANGLE
- *** SOLID STATE RELIABILITY**
- *CATEGORIZED FOR LUMINOUS INTENSITY
- *SMD DISPLAY
- *LEAD FREE PACKAGE (ACCORDING TO ROHS)

DESCRIPTION

The LTS-4817SW-P is a 0.39 inch (10.0 mm) digit height single digit SMD display. This device uses LTW-C193 series InGaN white Chip LED. The display has gray face and white segments, and suitable for reverse mount assembly.

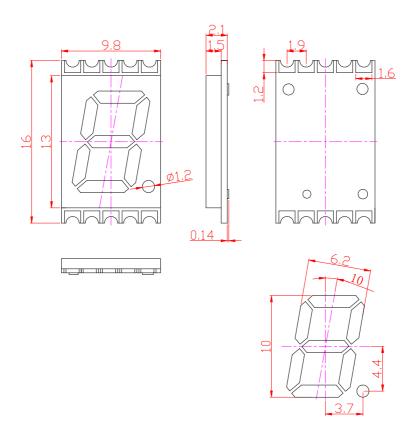
DEVICE

PART NO.	DESCRIPTION
InGaN White	C
LTS-4817SW-P	Common Anode

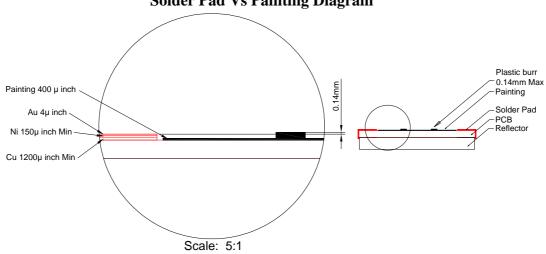
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PACKAGE DIMENSIONS



Solder Pad Vs Painting Diagram

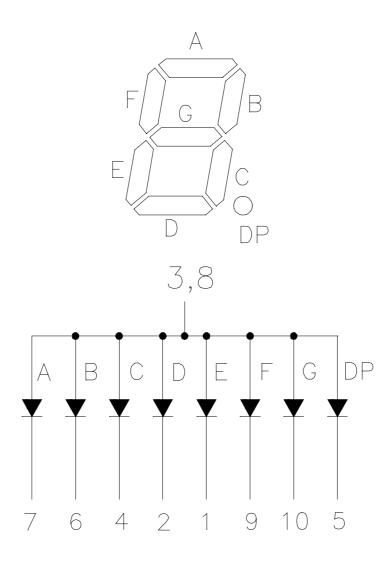


NOTES:

- 1. Plastic pins' burr max. 0.14 mm,
- 2. All dimensions are in millimeters. Tolerances are \pm 0.25mm (0.01") unless otherwise noted.
- 3. Solder pad materials and thickness: Cu: 1200μ inch Ni: Min 150μ inch Au: 4μ inch.

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INTERNAL CIRCUIT DIAGRAM



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PIN CONNECTION

No.	CONNECTION
1	CATHODE E
2	CATHODE D
3	COMMON ANODE
4	CATHODE C
5	CATHODE D.P.
6	CATHODE B
7	CATHODE A
8	COMMON ANODE
9	CATHODE F
10	CATHODE G

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ABSOLUTE MAXIMUM RATING AT Ta = 25°C

PARAMETER	MAXIMUM RATING	UNIT		
Power Dissipation Per Segment	70	mW		
Peak Forward Current Per Segment	100			
(Frequency 1Khz,10% duty cycle)	100	mA		
Continuous Forward Current Per Segment	20	mA		
Forward Current Derating from 25°C	0.25	mA/°C		
Operating Temperature Range	-35° C to $+80^{\circ}$ C			
Storage Temperature Range	-55°C to +105°C			
Iron Soldering Conditions: 1/16 inch Below Seating Plane for 3 Seconds at 260°C				

ELECTRICAL / OPTICAL CHARACTERISTICS AT Ta = 25°C

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
Luminous Intonsity	IV	28.0	44.0		mcd	IF = 5mA
Luminous Intensity	1 V					Note 1, 2, 5
Chromoticity Coordinates	X		0.294			IF = 5mA
Chromaticity Coordinates	у		0.286			Note 3, 5
Forward Voltage Per Segment	$V_{\rm F}$	2.70		3.2	V	IF = 5mA
Davaga Cymant Dan Cagmant	т			100	,, A	V _R =5V
Reverse Current Per Segment	\mathbf{I}_{R}				$\mu \mathbf{A}$	Note 7

Note:

- 1. Luminous intensity is measured with a light sensor and filter combination that approximates the CIE (Commission Internationale De L'Eclairage) eye-response curve.
- 2. Iv classification code is marked on each packing bag.
- 3. The chromaticity coordinates (x, y) is derived from the 1931 CIE chromaticity diagram.
- 4. Caution in ESD:

Static Electricity and surge damages the LED. It is recommend using a wrist band or anti-electrostatic glove when handling the LED. All devices, equipment and machinery must be properly grounded.

- 5. Tester: CAS140B is for the chromaticity coordinates (x, y) and IV.
- 6. The chromaticity coordinates (x, y) guarantee should be added ± 0.01 tolerance.
- 7. Reverse voltage is only for IR test. It can not continue to operate at this situation.

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Chip LED Bin Code List

V _F Spec. Table					
V _F Bin	Forward Voltage (V) at I _F =5mA				
V _F DIII	Min.	Max			
3	2.7	2.8			
4	2.8	2.9			
5	2.9	3.0			
6	3.0	3.1			
7	3.1	3.2			

Tolerance on each Forward Voltage bin is +/-0.1 volt

Iv Spec. Table				
Iv Bin	Iv (mcd) a	at I _F =5mA		
IV DIII	Min.	Max.		
Q11	71.0	81.0		
Q12	81.0	90.0		
Q21	90.0	101.0		
Q22	101.0	112.0		
R11	112.0	129.0		
R12	129.0	146.0		
R21	146.0	165.0		

Tolerance on each Luminous Intensity bin is $\pm -10\%$

Color Ranks Table						
Ranks		Colo	or bin limits	at $I_F = 5mA$		
Kaliks		CIE 193	1 Chromati	city coordina	ates	
S1-2	X	0.284	0.284	0.294	0.294	
31-2	у	0.240	0.272	0.286	0.254	
S2-2	X	0.284	0.284	0.294	0.294	
	у	0.272	0.305	0.319	0.286	
S3-1	X	0.294	0.294	0.304	0.304	
	у	0.254	0.286	0.300	0.268	

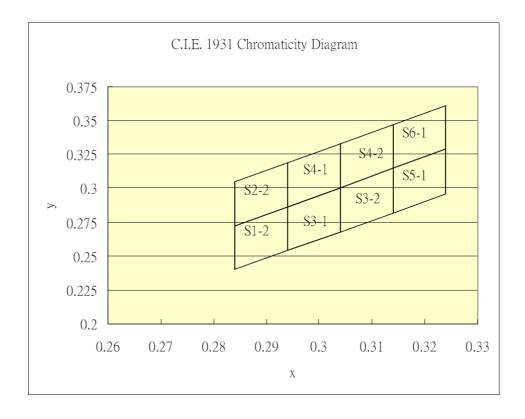
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S3-2	X	0.304	0.304	0.314	0.314
33-2	у	0.268	0.300	0.315	0.282
S4-1	X	0.294	0.294	0.304	0.304
54-1	у	0.286	0.319	0.333	0.300
S4-2	X	0.304	0.304	0.314	0.314
34-2	у	0.300	0.333	0.347	0.315
S5-1	X	0.314	0.314	0.324	0.324
33-1	у	0.282	0.315	0.329	0.296
S6-1	X	0.314	0.314	0.324	0.324
	y	0.315	0.347	0.361	0.329

Tolerance on each Hue (x,y) bin is ± -0.01



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TYPICAL ELECTRICAL / OPTICAL CHARACTERISTIC CURVES

(25°C Ambient Temperature Unless Otherwise Noted)

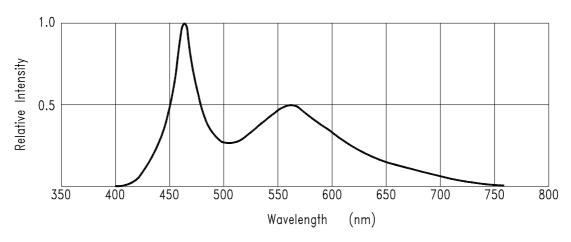
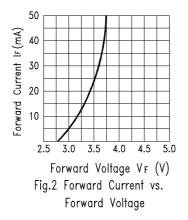
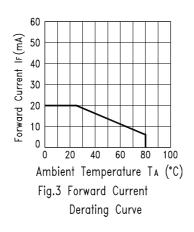


Fig.1 RELATIVE INTENSITY VS. WAVELENGTH





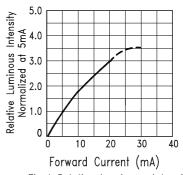


Fig.4 Relative Luminous Intensity
vs. Forward Current

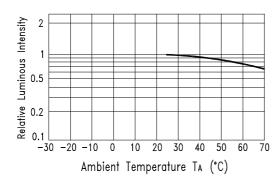


Fig.5 Luminous Intensity vs.

Ambient Temperature

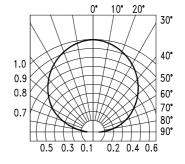


Fig.6 Spatial Distribution

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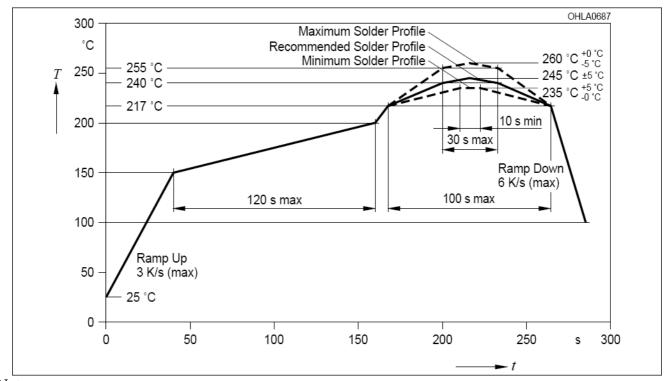
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SMT SOLDERING INSTRUCTION



Note:

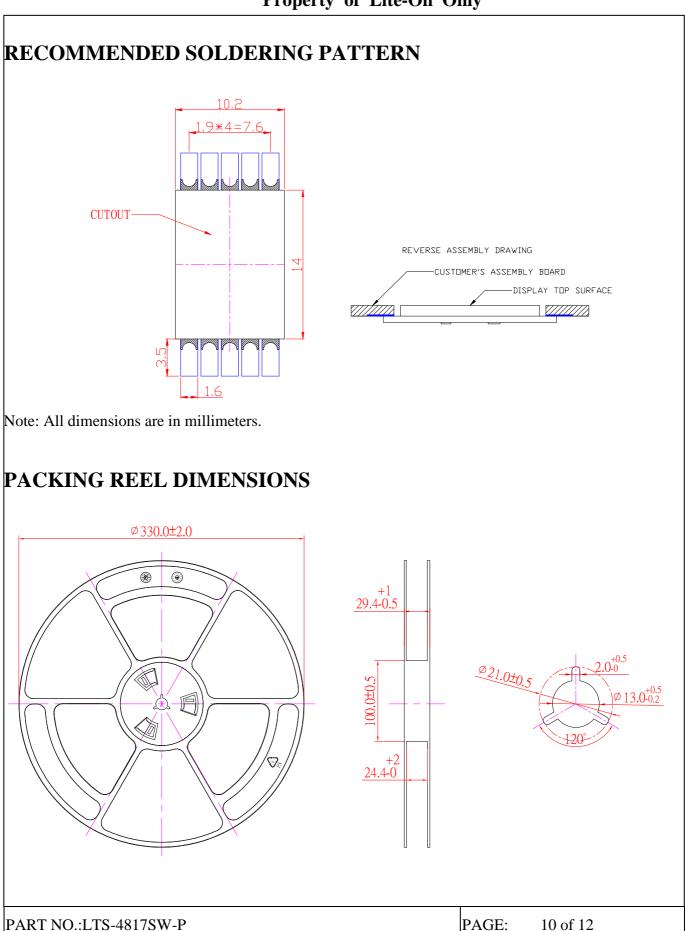
1. Recommended soldering condition:

Reflow Soldering (Two times only)		Soldering Iron (One time only)		
Pre-heat:	120~150°C.	Temperature	300°C Max.	
Pre-heat time:	120sec. Max.	Soldering time	3sec. Max.	
Peak temperature:	260°C Max.			
Soldering time:	5sec. Max.			

2. Number of reflow process shall be less than 2 times, and cooling process to normal temperature is required between the first and the second soldering process.

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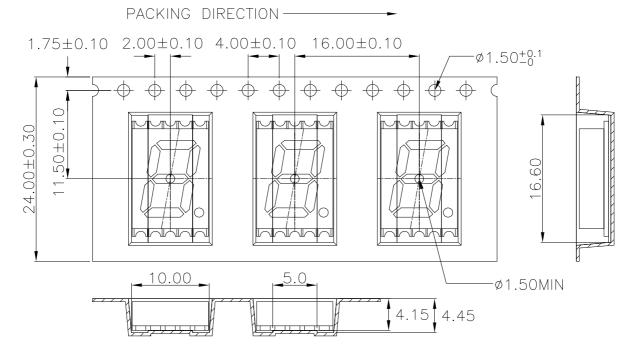
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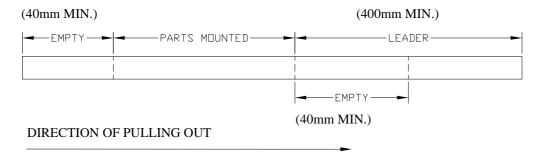
PACKING CARRIER DIMENSIONS

1. Taping parts:



10 sprocket hole pitch cumulative tolerance ± 0.20 . Carrier camber is within 1 mm in 250 mm. Thickness: 0.40 ± 0.05 mm. All dimensions meet EIA-481-C requirements.

2. Trailer part/ Leader part:



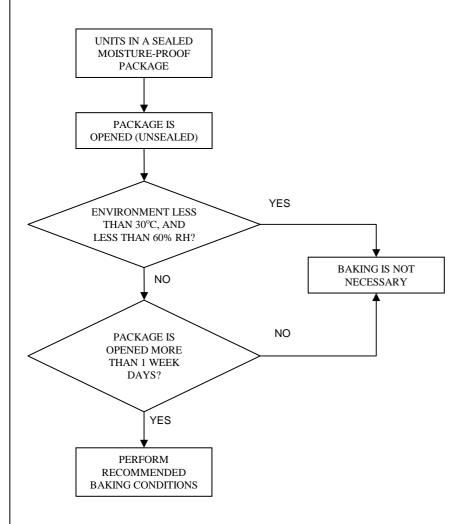
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Moisture Proof Packaging

All N/D SMD displays are shipped in moisture proof package. The displays should be stored at 30°C or less and 90% RH or less. Once the package opened, moisture absorption begins.



Baking Conditions

If the parts are not stored in dry conditions, they must be baked before reflow to prevent damage to the parts.

Package	Temperature	Time	
In Reel	60°C	≥48hours	
In Bulk	100°C	≧4hours	
	125°C	≥2hours	

Baking should only be done once.

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