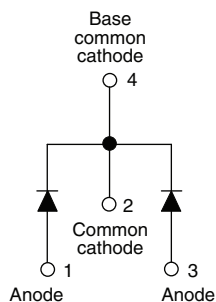


High Performance Schottky Rectifier, 2 x 3 A



FEATURES

- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?999912



PRODUCT SUMMARY

| | |
|-----------------|------------------|
| Package | D-PAK (TO-252AA) |
| $I_{F(AV)}$ | 2 x 3 A |
| V_R | 50 V, 60 V |
| V_F at I_F | 0.65 V |
| I_{RM} | 15 mA at 125 °C |
| T_J max. | 150 °C |
| Diode variation | Common cathode |
| E_{AS} | 6 mJ |

DESCRIPTION

The VS-MBRD650CTPbF, VS-MBRD660CTPbF surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS

| SYMBOL | CHARACTERISTICS | VALUES | UNITS |
|-------------|---------------------------------------|-------------|-------|
| $I_{F(AV)}$ | Rectangular waveform | 6 | A |
| V_{RRM} | | 50/60 | V |
| I_{FSM} | $t_p = 5 \mu s$ sine | 490 | A |
| V_F | 3 A_{pk} , $T_J = 125$ °C (per leg) | 0.65 | V |
| T_J | Range | -40 to +150 | °C |

VOLTAGE RATINGS

| PARAMETER | SYMBOL | VS-MBRD650CTPbF | VS-MBRD660CTPbF | UNITS |
|--------------------------------------|-----------|-----------------|-----------------|-------|
| Maximum DC reverse voltage | V_R | 50 | 60 | V |
| Maximum working peak reverse voltage | V_{RWM} | | | |

ABSOLUTE MAXIMUM RATINGS

| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
|---|-------------|---|-----------|-------|
| Maximum average forward current per leg See fig. 5 | $I_{F(AV)}$ | 50 % duty cycle at $T_C = 128$ °C, rectangular waveform | 3.0 6 | A |
| Maximum peak one cycle non-repetitive surge current See fig. 7 | I_{FSM} | 5 μs sine or 3 μs rect. pulse 10 ms sine or 6 ms rect. pulse | 490 75 | A |
| Non-repetitive avalanche energy per leg | E_{AS} | $T_J = 25$ °C, $I_{AS} = 1$ A, $L = 12$ mH | 6 | mJ |
| Repetitive avalanche current per leg | I_{AR} | Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical | 0.6 | A |



| ELECTRICAL SPECIFICATIONS | | | | | |
|---|----------------|--|-------------------------------------|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS |
| Maximum forward voltage drop per leg See fig. 1 | $V_{FM}^{(1)}$ | 3 A | $T_J = 25\text{ }^{\circ}\text{C}$ | 0.7 | V |
| | | 6 A | | 0.9 | |
| | | 3 A | $T_J = 125\text{ }^{\circ}\text{C}$ | 0.65 | |
| | | 6 A | | 0.85 | |
| Maximum reverse leakage current per leg See fig. 2 | $I_{RM}^{(1)}$ | $T_J = 25\text{ }^{\circ}\text{C}$ | $V_R = \text{Rated } V_R$ | 0.1 | mA |
| | | $T_J = 125\text{ }^{\circ}\text{C}$ | | 15 | |
| Typical junction capacitance per leg | C_T | $V_R = 5\text{ }V_{DC}$ (test signal range 100 kHz to 1 MHz), $25\text{ }^{\circ}\text{C}$ | | 145 | pF |
| Typical series inductance per leg | L_S | Measured lead to lead 5 mm from package body | | 5.0 | nH |
| Maximum voltage rate of change | dV/dt | Rated V_R | | 10 000 | V/μs |

Note(1) Pulse width < 300 μ s, duty cycle < 2 %

| THERMAL - MECHANICAL SPECIFICATIONS | | | | |
|--|----------------------|--|-------------|-----------------------------|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| Maximum junction and storage temperature range | $T_J^{(1)}, T_{Stg}$ | | -40 to +150 | $^{\circ}\text{C}$ |
| Maximum thermal resistance, junction to case <div style="display: flex; justify-content: space-between; width: 100px;"><div>per leg</div><div>per device</div></div> | R_{thJC} | DC operation See fig. 4 | 6 | $^{\circ}\text{C}/\text{W}$ |
| | | | 3 | |
| Maximum thermal resistance, junction to ambient | R_{thJA} | | 80 | |
| Approximate weight | | | 0.3 | g |
| | | | 0.01 | oz. |
| Marking device | | Case style D-PAK (similar to TO-252AA) | MBRD650CT | |
| | | | MBRD660CT | |

Note(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

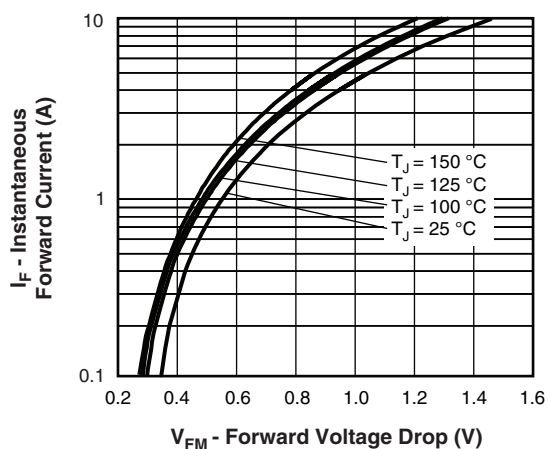


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

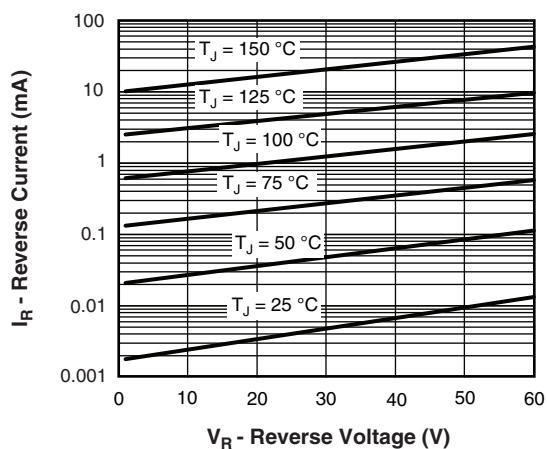


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

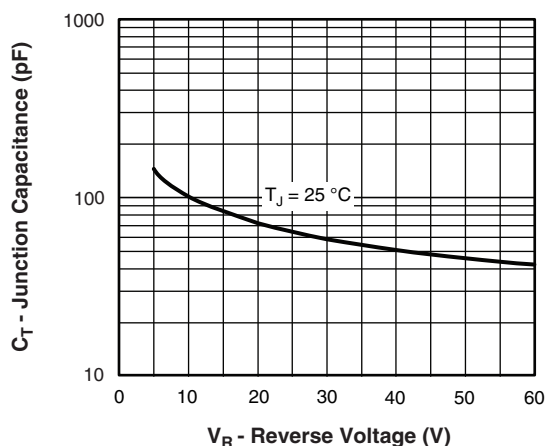


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

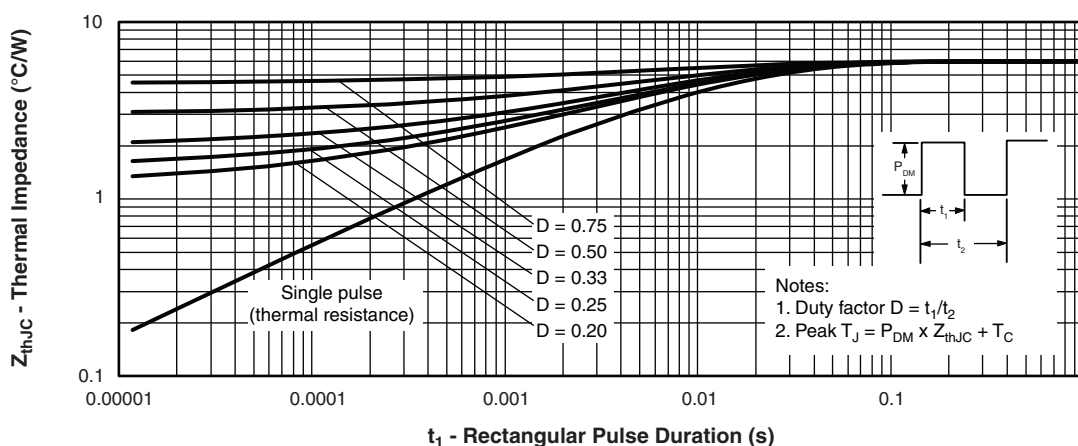


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

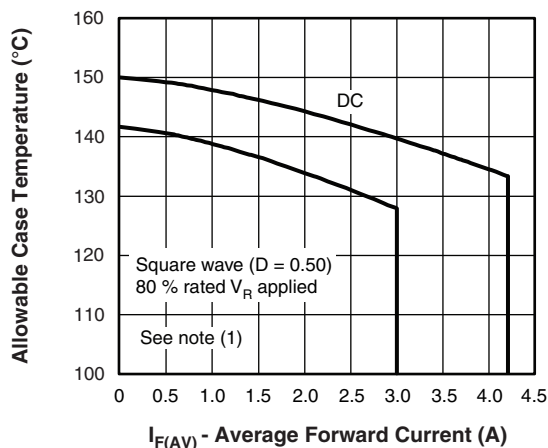


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

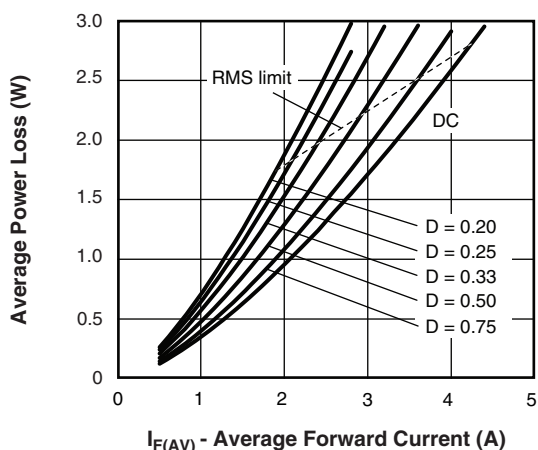


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

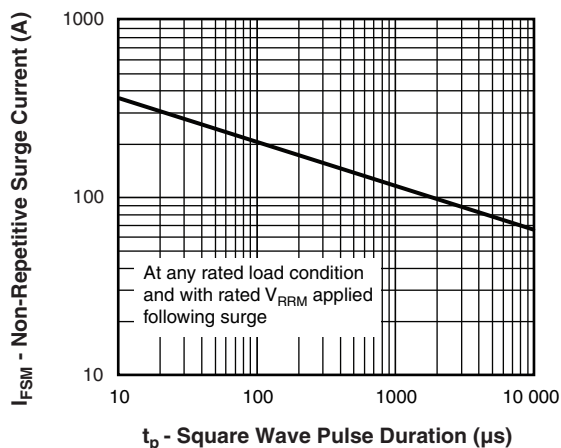


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R



ORDERING INFORMATION TABLE

| Device code | VS- | MBR | D | 6 | 60 | CT | TR | PbF |
|-------------|-----|-----|---|---|----|----|----|-----|
| | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |

- | | | |
|----------|---|---|
| 1 | - | Vishay Semiconductors product |
| 2 | - | Schottky MBR series |
| 3 | - | D = TO-252AA (D-PAK) |
| 4 | - | Current rating (6 = 6 A) |
| 5 | - | Voltage ratings |
| 6 | - | CT = center tap (dual) |
| 7 | - | <ul style="list-style-type: none">• None = tube (50 pieces)• TR = tape and reel• TRL = tape and reel (left oriented)• TRR = tape and reel (right oriented) |
| 8 | - | PbF = lead (Pb)-free |
- 50 = 50 V
60 = 60 V

| LINKS TO RELATED DOCUMENTS | |
|----------------------------|--|
| Dimensions | www.vishay.com/doc?95016 |
| Part marking information | www.vishay.com/doc?95059 |
| Packaging information | www.vishay.com/doc?95033 |

D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



| SYMBOL | MILLIMETERS | | INCHES | | NOTES |
|--------|-------------|------|--------|-------|-------|
| | MIN. | MAX. | MIN. | MAX. | |
| A | 2.18 | 2.39 | 0.086 | 0.094 | |
| A1 | - | 0.13 | - | 0.005 | |
| b | 0.64 | 0.89 | 0.025 | 0.035 | |
| b2 | 0.76 | 1.14 | 0.030 | 0.045 | |
| b3 | 4.95 | 5.46 | 0.195 | 0.215 | 3 |
| c | 0.46 | 0.61 | 0.018 | 0.024 | |
| c2 | 0.46 | 0.89 | 0.018 | 0.035 | |
| D | 5.97 | 6.22 | 0.235 | 0.245 | 5 |
| D1 | 5.21 | - | 0.205 | - | 3 |
| E | 6.35 | 6.73 | 0.250 | 0.265 | 5 |
| E1 | 4.32 | - | 0.170 | - | 3 |

| SYMBOL | MILLIMETERS | | INCHES | | NOTES |
|--------|-------------|-------|------------|-------|-------|
| | MIN. | MAX. | MIN. | MAX. | |
| e | 2.29 BSC | | 0.090 BSC | | |
| H | 9.40 | 10.41 | 0.370 | 0.410 | |
| L | 1.40 | 1.78 | 0.055 | 0.070 | |
| L1 | 2.74 BSC | | 0.108 REF. | | |
| L2 | 0.51 BSC | | 0.020 BSC | | |
| L3 | 0.89 | 1.27 | 0.035 | 0.050 | 3 |
| L4 | - | 1.02 | - | 0.040 | |
| L5 | 1.14 | 1.52 | 0.045 | 0.060 | 2 |
| Ø | 0° | 10° | 0° | 10° | |
| Ø1 | 0° | 15° | 0° | 15° | |
| Ø2 | 25° | 35° | 25° | 35° | |

Notes

- Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.010") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



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