

PHONE: (215) 631-9840 FAX: (215) 631-9855

## **MS2421**

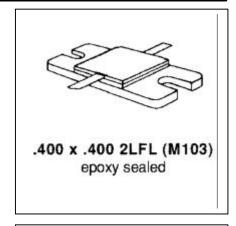
## RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

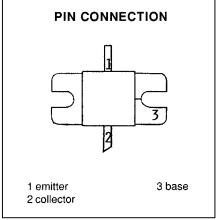
#### **Features**

- DESIGNED FOR HIGH POWER PULSED IFF, DME, AND TACAN APPLICATIONS
- 350 W (typ.) IFF 1030 1090 MHz
- 300 W (min.) DME 1025 1150 MHz
- 290 W (typ.) TACAN 960 1215 MHz
- 960 1215 MHz
- GOLD METALLIZATION
- P<sub>OUT</sub> = 300W MINIMUM
- $G_P = 6.3 \text{ dB MINIMUM}$
- INFINITE VSWR CAPABILITY @ RATED CONDITIONS
- EMITTER BALLASTED
- COMMON BASE

#### **DESCRIPTION:**

The MS2421 is a gold metallized silicon, NPN power transistor designed for applications requiring high peak power and low duty cycles such as IFF, DME, and TACAN. The MS2421 is designed with internal input/output matching resulting in improved broadband performance and low thermal resistance.





ABSOLUTE MAXIMUM RATINGS (Tcase =  $25^{\circ}$ C)

Symbol	Parameter	Value	Unit
P <sub>DISS</sub>	Power Dissipation	875	W
V <sub>CES</sub>	Collector-Emitter Voltage	65	V
V <sub>CBO</sub>	Collector-Base Voltage	65	V
V <sub>EBO</sub>	Emitter-Base Voltage	3.5	V
TJ	Junction Temperature	200	°C
Ic	Device Current	22	Α
T <sub>STG</sub>	Storage Temperature	-65 to +200	οс

## **Thermal Data**

	T		
R <sub>TH(J-C)</sub>	Junction-case Thermal Resistance	0.20	°C/W



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# **ELECTRICAL SPECIFICATIONS (Tcase = 25°C) STATIC**

Symbol	Test Conditions			Value		
			Min.	Тур.	Max.	Unit
BV <sub>CBO</sub>	I <sub>C</sub> = 10 mA	I <sub>E</sub> = 0 mA	65			V
BV <sub>EBO</sub>	I <sub>E</sub> = 5.0 mA	$I_C = 0 \text{ mA}$	3.5			V
I <sub>CES</sub>	V <sub>CE</sub> = 50 V				25	mA
HFE	V <sub>CE</sub> = 5 V	I <sub>C</sub> = 500mA	10		200	mA

## **DYNAMIC**

Complete	Test Conditions				Value		
Symbol				Min.	Min. Typ. Max.		Unit
P <sub>out</sub>	f =1025 - 1150 MHz	P <sub>IN</sub> = 70W	V <sub>CE</sub> =50V	300			W
G <sub>P</sub>	f =1025 - 1150 MHz	P <sub>IN</sub> = 70W	V <sub>CE</sub> =50V	6.3			dB
ης	f =1025 - 1150 MHz	P <sub>IN</sub> = 70W	V <sub>CE</sub> =50V	35			%
Conditions	Pulse Width = 10 μs	Duty Cycle =	1%				

## **IMPEDANCE DATA**

FREQ	$Z_IN(\Omega)$	$Z_{\mathtt{CL}}\!(\Omega)$		
960 MHz	2.6 + j6.0	2.5 - j6.0		
1090 MHz	7.4 + j4.4	2.4 - j6.2		
1215 MHz	4.3 + j1.1	2.5 – j4.9		

Pin = 70W Vce = 50V

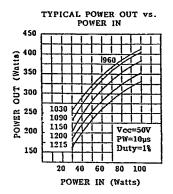


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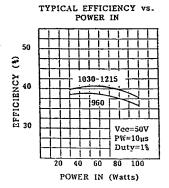
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## TYPICAL PERFORMANCE

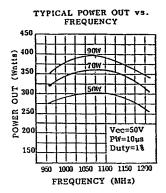
#### POWER OUTPUT vs POWER INPUT



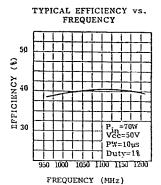
#### **EFFICIENCY vs POWER INPUT**



#### **POWER OUTPUT vs FREQUENCY**

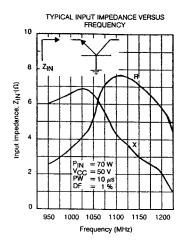


#### **EFFICIENCY vs FREQUENCY**

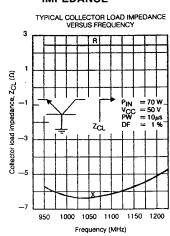


## **IMPEDANCE DATA**

#### TYPICAL INPUT IMPEDANCE



## TYPICAL COLLECTOR LOAD IMPEDANCE

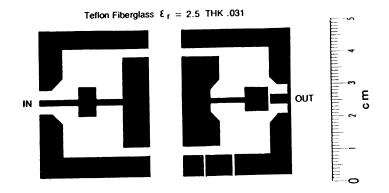


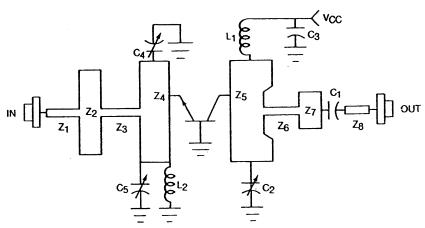


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## **TEST CIRCUIT**





#### All Dimension are in Inches

C1	:	100pF Chip Capacitor Across .120 Sq. Gap	Z1	- :	.395 X .083
C2		6 - 4 5pF JOHANSON	Z2	:	.250 x .340
		The state of the s	<b>Z</b> 3	- :	.495 x .083
			Z4	:	.360 x 1.193
01,00	•		Z5		.485 x 1.2
11		2 3/4 Turns Diameter 16 Tinned 125 LD	<b>Z</b> 6	:	.520 x .035
	•	.215 Long	<b>Z</b> 7	:	.270 x .330
L2	:		Z8	:	.270 x .110
	C2 C3 C4, C5 L1	C2 : C3 : C4, C5 :	C2 : .6 - 4.5pF JOHANSON C3 : 470pF Chip Capacitor Across .120 Sq. Gap C4, C5 : .35 - 3.5pF  L1 : 2.3/4 Turns Diameter 16 Tinned .125 l.D215 Long L2 : 2.3/4 Turns Diameter 20 Tinned .090 l.D.	C2       : .6 - 4.5pF JOHANSON       Z2         C3       : .470pF Chip Capacitor Across .120 Sq. Gap       Z3         C4, C5       : .35 - 3.5pF       Z4         L1       : 2 .3/4 Turns Diameter 16 Tinned .125 l.D.       Z6         .215 Long       .27         L2       : 2 .3/4 Turns Diameter 20 Tinned .090 l.D.       Z8	C2 : .6 - 4.5pF JOHANSON

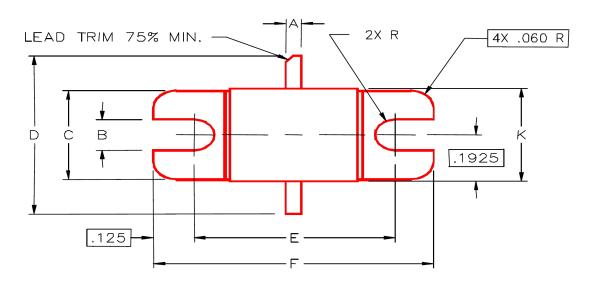


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## PACKAGE MECHANICAL DATA

## PACKAGE STYLE M103





	MINIMUM	MAXIMUM	П		MINIMUM	MAXIMUM
	INCHES/MM	INCHES/MM	Ш		INCHES/MM	INCHES/MM
Α	.045/1,14	.055/1,40	$\prod$	1	.110/2,79	.130/3,30
В	.130/	/3,30	П	J	.190/4,83	.215/5,46
С	.380/9,65	.390/9,91	П	K	.390/9,91	.410/10,41
D	.880/22,35	.920/23,37	$\prod$			
E	.645/16,38	.655/16,64	П			
F	.890/22,61	.910/23,11	П			
G	.002/0,05	.006/0,15	П			
Н	.055/1,40	.065/1,65	П			

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**Authorized Distributor** 

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