Schottky Barrier Diode

These Schottky barrier diodes are optimized for low forward voltage drop and low leakage current. The DSN2 (Dual Silicon No-lead) package is a chip level package using solderable metal contacts under the package similar to DFN style packages. The DSN style package enables 100% utilization of the package area for active silicon, offering a significant performance per board area advantage compared to products in plastic molded packages. The low thermal resistance enables designers to meet the challenging task of achieving higher efficiency and meeting reduced space requirements.

Features

- Very Low Forward Voltage Drop 370 mV @ 10 mA
- Low Reverse Current 7.0 μA @ 10 V VR
- 200 mA of Continuous Forward Current
- ESD Rating Human Body Model: Class 3B
 - Machine Model: Class C
- Very High Switching Speed
- Low Capacitance CT = 7 pF
- This is a Halide-Free Device
- This is a Pb-Free Device

Typical Applications

- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping & Protection

Markets

- Mobile Handsets
- MP3 Players
- Digital Camera and Camcorders
- Notebook PCs & PDAs
- GPS

MAXIMUM RATINGS

| Rating | | Symbol | Value | Unit |
|---|-----------------------------------|------------------|--------------|---------|
| Reverse Voltage | | V _R | 30 | V |
| Forward Current (DC) | | ΙF | 200 | mA |
| Forward Surge Current (60 Hz @ 1 cycle) | | I _{FSM} | 4.0 | А |
| ESD Rating: | Human Body Model Machine Model | ESD | >8.0 >400 | kV V |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



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30 V SCHOTTKY BARRIER DIODE





DSN2 (0201) CASE 152AA

MARKING DIAGRAM

PIN 1

V230 = Specific Device Code YYY = Year Code

ORDERING INFORMATION

| Device | Package | Shipping† |
|---------------|-------------------|--------------------|
| NSR02F30NXT5G | DSN2 (Pb-Free) | 5000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Min | Тур | Max | Unit |
|---|------------------------------------|-----|-----|-------------|------------|
| Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ T _A = 25°C | R _{θJA} P _D | | | 400 312 | °C/W mW |
| Thermal Resistance Junction-to-Ambient (Note 2) Total Power Dissipation @ T _A = 25°C | R _{θJA} P _D | | | 170 735 | °C/W mW |
| Storage Temperature Range | T _{stg} | | | -40 to +125 | °C |
| Junction Temperature | TJ | | | +125 | °C |

Mounted onto a 4 in square FR-4 board 10 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.
 Mounted onto a 4 in square FR-4 board 1 in sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|---|----------------|-----|-----|--------------|------|
| Reverse Leakage (V _R = 10 V) (V _R = 30 V) | I _R | | | 7.0 50 | μΑ |
| Forward Voltage (I _F = 10 mA) (I _F = 200 mA) | V _F | | | 0.37 0.55 | V |
| Total Capacitance (V _R = 5.0 V, f = 1 MHz) | СТ | | 7.0 | | pF |

TYPICAL CHARACTERISTICS

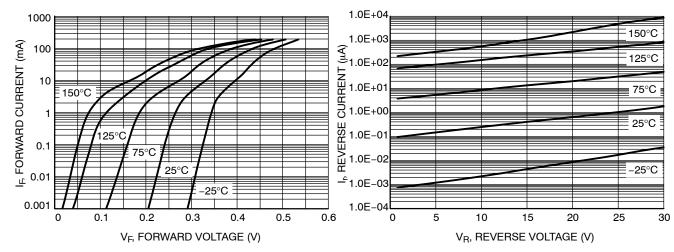


Figure 1. Forward Voltage

Figure 2. Leakage Current

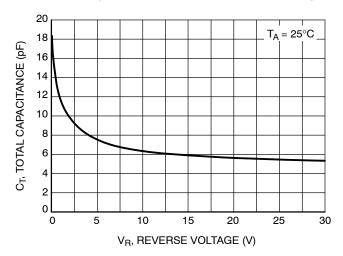
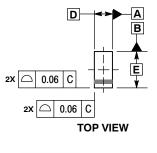
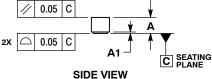


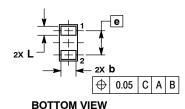
Figure 3. Total Capacitance

PACKAGE DIMENSIONS

DSN2, **0.6x0.3**, **0.4P**, **(0201)**CASE 152AA-01
ISSUE O





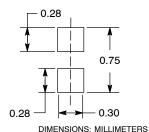


NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M. 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS.

| | MILLIMETERS | | |
|-----|-------------|------|--|
| DIM | MIN | MAX | |
| Α | 0.24 | 0.30 | |
| A1 | 0.00 | 0.01 | |
| b | 0.22 | 0.28 | |
| D | 0.30 BSC | | |
| Е | 0.60 BSC | | |
| е | 0.40 BSC | | |
| L | 0.12 | 0.18 | |

MOUNTING FOOTPRINT*



See Application Note AND8398/D for more mounting details

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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