

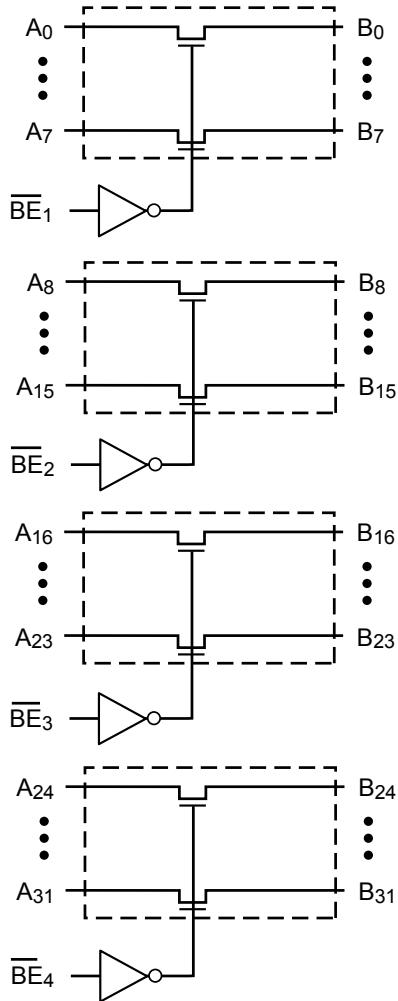
## Features

- Near-Zero propagation delay
- 25Ω series resistor termination
- 5Ω switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra-low quiescent power (0.2µA typical)
  - Ideally suited for notebook applications
- Pin compatible with QS34X245
- Industrial operating temperature: -40°C to +85°C
- Packaging (Pb-free & Green available):
  - 80-pin 150 mil wide BQSOP (B)

## Description

Pericom Semiconductor's PI5C34X2245 is a 32-bit, 2-port bus switch. Four enable signals ( $\overline{BE}_1$ ) turn the switches on. The bus switch creates no additional propagational delay or additional ground bounce noise. The device has a built-in 25Ω resistor to reduce noise resulting from reflection, thus eliminating the need for an external terminating resistor.

## Block Diagram



## Pin Configuration

NC	1	80	Vcc
A0	2	79	$\overline{BE}_1$
A1	3	78	$B_0$
A2	4	77	$B_1$
A3	5	76	$B_2$
A4	6	75	$B_3$
A5	7	74	$B_4$
A6	8	73	$B_5$
A7	9	72	$B_6$
GND	10	71	$B_7$
NC	11	70	Vcc
A8	12	69	$\overline{BE}_2$
A9	13	68	$B_8$
A10	14	67	$B_9$
A11	15	66	$B_{10}$
A12	16	65	$B_{11}$
A13	17	64	$B_{12}$
A14	18	63	$B_{13}$
A15	19	62	$B_{14}$
GND	20	61	$B_{15}$
NC	21	60	Vcc
A16	22	59	$\overline{BE}_3$
A17	23	58	$B_{16}$
A18	24	57	$B_{17}$
A19	25	56	$B_{18}$
A20	26	55	$B_{19}$
A21	27	54	$B_{20}$
A22	28	53	$B_{21}$
A23	29	52	$B_{22}$
GND	30	51	$B_{23}$
NC	31	50	Vcc
A24	32	49	$\overline{BE}_4$
A25	33	48	$B_{24}$
A26	34	47	$B_{25}$
A27	35	46	$B_{26}$
A28	36	45	$B_{27}$
A29	37	44	$B_{28}$
A30	38	43	$B_{29}$
A31	39	42	$B_{30}$
GND	40	41	$B_{31}$

## Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature .....	-65°C to +150°C
Ambient Temperature with Power Applied .....	-40°C to +85°C
Supply Voltage to Ground Potential .....	-0.5V to +7.0V
DC Input Voltage .....	-0.5V to +7.0V
DC Output Current.....	120 mA
Power Dissipation .....	0.5W

### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## Truth Table<sup>(1)</sup>

Function	$\overline{BEn}$	$A0 - 31$
Disconnect	H	Hi-Z
Connect	L	B0 - 3

### Notes:

1. H = High Voltage Level, L = Low Voltage Level  
Hi-Z = High Impedance

## Pin Description

Pin Name	I/O	Description
$\overline{BEx}$	I	Bus Enable Input (Active LOW)
$A_0-A_{31}$	I/O	Bus A
$B_0-B_{31}$	I/O	Bus B

## DC Electrical Characteristics (Over the Operating Range, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ , $V_{CC} = 5\text{V} \pm 10\%$ )

Parameters	Description	Test Conditions <sup>(1)</sup>	Min.	Typ <sup>(2)</sup>	Max.	Units
$V_{IH}$	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
$V_{IL}$	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	
$I_{IH}$	Input HIGH Current	$V_{CC} = \text{Max.}$ , $V_{IN} = V_{CC}$			$\pm 1$	$\mu\text{A}$
$I_{IL}$	Input LOW Current	$V_{CC} = \text{Max.}$ , $V_{IN} = \text{GND}$			$\pm 1$	
$I_{OZH}$	High Impedance Output Current	0 - A, B - $V_{CC}$			$\pm 1$	
$V_{IK}$	Clamp Diode Voltage	$V_{CC} = \text{Min.}$ , $I_{IN} = -18\text{mA}$		-0.7	-1.2	V
$I_{OS}$	Short Circuit Current <sup>(3)</sup>	A (B) = 0V, B (A) = $V_{CC}$	100			$\text{mA}$
$R_{ON}$	Switch On-Resistance <sup>(4)</sup>	$V_{CC} = \text{Min.}$ , $V_{IN} = 0\text{V}$ , $I_{ON} = 48\text{mA}$		28	40	$\Omega$
		$V_{CC} = \text{Min.}$ , $V_{IN} = 2.4\text{V}$ , $I_{ON} = 15\text{mA}$		35	48	

### Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at  $V_{CC} = 5.0\text{V}$ ,  $T_A = 25^\circ\text{C}$  ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (A,B) pins.

Capacitance ( $T_A = 25^\circ\text{C}$ ,  $f = 1 \text{ MHz}$ )

Parameters <sup>(1)</sup>	Description	Test Conditions	Type	Units
$C_{IN}$	Input Capacitance	$V_{IN} = 0\text{V}$	6	pF
$C_{OFF}$	A/B Capacitance, Switch Off		6	
$C_{ON}$	A/B Capacitance, Switch On		12	

## Notes:

1. This parameter is determined by device characterization but is not production tested.

## Power Supply Characteristics

Parameters	Description	Test Conditions <sup>(1)</sup>		Min.	Typ <sup>(2)</sup>	Max.	Units
$I_{CC}$	Quiescent Power Supply Current	$V_{CC} = \text{Max.}$	$V_{IN} = \text{GND or } V_{CC}$		0.1	3.0	$\mu\text{A}$
$\Delta I_{CC}$	Supply Current per Input @ TTL HIGH	$V_{CC} = \text{Max.}$	$V_{IN} = 3.4\text{V}^{(3)}$			2.5	mA
$I_{CCD}$	Supply Current per Input per MHz <sup>(4)</sup>	$V_{CC} = \text{Max.},$ A & B Pins Open $BE_n = \text{GND},$ Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

## Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at  $V_{CC} = 5.0\text{V}$ ,  $+25^\circ\text{C}$  ambient.
3. Per TTL driven input ( $V_{IN} = 3.4\text{V}$ , control inputs only); A and B pins do not contribute to  $I_{CC}$ .
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

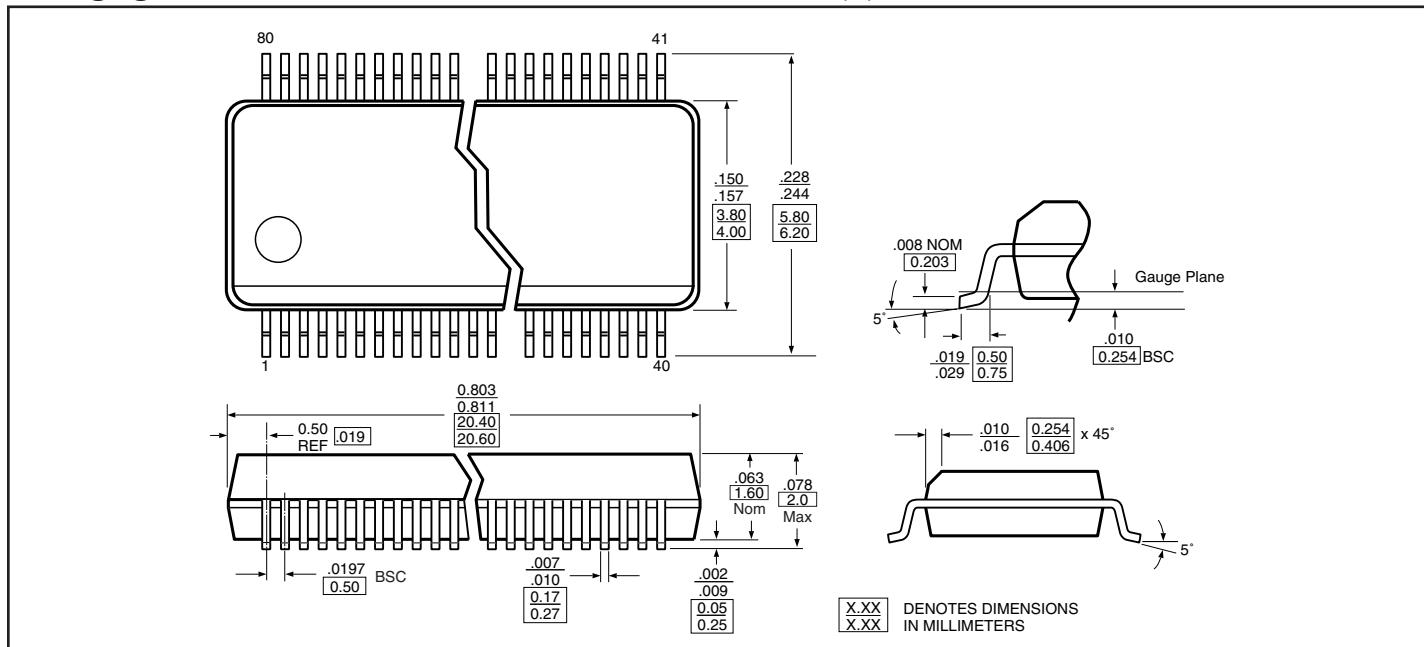
## Switching Characteristics over Operating Range

Parameter	Description	Conditions	Com.		Units
			Min.	Max.	
$t_{PLH}$ $t_{PHL}$	Propagation Delay <sup>(1,2)</sup> Ax to Bx, Bx to Ax	$C_L = 50\text{pF}$ $R_L = 500\Omega$		1.25	ns
$t_{PZH}$ $t_{PZL}$	Bus Enable Time $\overline{BEx}$ to Ax or Bx		1.5	7.5	
$t_{PHZ}$ $t_{PLZ}$	Bus Disable Time $\overline{BEx}$ to Ax or Bx		1.5	5.5	

## Notes:

1. This parameter is guaranteed but not tested on Propagation Delays.
2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## Packaging Mechanical: 80-Pin 150 Mil Wide Plastic BQSOP (B)



## Ordering Information

Ordering Code	Package Code	Package Description
PI5C34X2245B	B	80-Pin 150 Mil Wide Plastic BQSOP
PI5C34X2245BE	B	Pb-free & Green, 80-Pin 150 Mil Wide Plastic BQSOP

## Notes:

- Thermal characteristics can be found on the company web site at [www.pericom.com/packaging/](http://www.pericom.com/packaging/)
  - E = Pb-free & Green
  - Adding an X suffix = Tape/Reel