

PRODUCT BRIEF



PI7C9X754

8BIT Bus® Quad UART I/O Bridge

The PI7C9X754 is a 1.62V to 3.6V quad Universal Asynchronous Receiver and Transmitter(UART) with 5V tolerant serial (modem) inputs. The highly integrated device is designed for high bandwidth requirement in communication systems. The global interrupt source register providers a complete interrupt status indication for all 4 channels to speed up interrupt parsing. Each UART has its own 16C550 compatible set of configuration registers, TX and RX FIFOs of 64 bytes, fully programmable transmit and receive FIFO trigger levels, TX and RX FIFO level counters, automatic RTS/CTS or DTR/DSR hardware flow control with programmable hysteresis, automatic software(Xon/Xoff) flow control, RS-485 half-duplex direction control, and auto address detection, Intel or Motorola bus interface and sleep mode with a wake-up indicator.

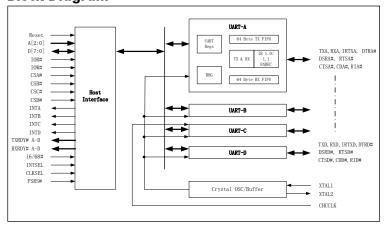
Applications

- → Remote Access Servers
- → Ethernet Network to Serial Ports
- → Factory Automation and Process Control
- → Instrumentation
- → Multi-port RS-232/ RS-422/ RS-485 Cards
- → Point-of-Sale Systems (PoS)
- → Industrial Control (IPC)
- → Gaming Machines
- → Building Automation
- **→** Embedded Systems
- → Network Management

Software support

- → PI7C9X754 Programming guide
- → PI7C9X754 Sample code
- → PI7C9X754 implementation guide

Block Diagram



Features

- → 1.62V to 3.6V with 5V tolerant serial inputs
- → Programmable Sleep Mode with automatic wake-up
 - Intel Data Bus Interface
- → Each UART is independently controlled with:
 - □ 16C550 Compatible Register Set
 - 64-byte transmit and receive FIFOs
 - Transmit and Receive FIFO level counters
 - Programmable TX and RX FIFO trigger level for DMA and Interrupt Generation
 - » Programmable Receive FIFO Trigger Levels for Software/ Hardware Flow Control
 - » Programmable hysteresis (Table A-D) for Software/Hardware Flow Control
 - Automatic RTS/CTS flow control
 - Automatic Xon/Xoff software flow control with Optional Data Flow Resume by Xon any Character
 - » DMA signaling Capability for both received and transmitted data
 - RS485 HDX Control output
 - RS485 auto address detection
 - □ Infrared (IrDA 1.0/1.1) Data encoder/decoder
 - Programmable data rate with prescaler
- → Up to 16Mbps serial data rate with 64MHz external clock input
- → Crystal oscillator(up to 24MHz) or external clock(up to 80MHz) input
- → Built in Power-On-Reset circuit

Order Information

Part Number	Package	PB-Free& Green	Temperature
PI7C9X754MEX	MQFP-100	YES	-40°C TO 85°C
PI7C9X754FFEX	LQFP-80	YES	-40°C TO 85°C
PI7C9X754FCEX	LQFP-64	YES	-40°C TO 85°C
PI7C9X754ZBEX	TQFN-48	YES	-40°C TO 85°C
PI7C9X754FF_PI7C9X754FCEVB	Board	Evaluation Board for PI7C9X754FF and PI7C9X754FC	
PI7C9X754M_PI7C9X754ZBEVB	Board	Evaluation Board for PI7C9X754M and PI7C9X754ZB	

*Note:

- Thermal characteristics can be found on the company web site at www.pericom. com/packaging
- E= Pb-free and Green
- X suffix =Tape/Ree