

**PN5432 PN5433 PN5434** 



# **N-Channel Switch**

This device is designed for analog or digital switching applications where very low On Resistance is mandatory. Sourced from Process 58. See J108 for characteristics.

# **Absolute Maximum Ratings\***

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DG}$	Drain-Gate Voltage	25	V
V <sub>GS</sub>	Gate-Source Voltage	-25	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range -55 to +150 °C		°C

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

### **Thermal Characteristics**

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		PN5432 / 5433 / 5434	7
P <sub>D</sub>	Total Device Dissipation	350	mW
	Derate above 25°C	2.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

# **N-Channel Switch**

(continued)

Symbol	Parameter	Test Conditions	Min	Max	Units
	RACTERISTICS				
$V_{(BR)GSS}$	Gate-Source Breakdown Voltage	$I_G = 1.0 \mu A, V_{DS} = 0$	-25		V
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = 15 \text{ V}, V_{DS} = 0$ $V_{GS} = 15 \text{ V}, V_{DS} = 0, T_A = 150 \text{ °C}$		-200 -200	pA nA
$I_{D(off)}$	Drain Cutoff Leakage Voltage	$V_{GS} = 10 \text{ V}, V_{DS} = 5.0 \text{ V}$ $V_{GS} = 10 \text{ V}, V_{DS} = 5.0 \text{ V},$		-200	pA
\/	Cata Cauraa Cutoff Valtage	$T_A = 150 \text{ °C}$ $V_{DS} = 5.0 \text{ V}, I_D = 3.0 \text{ nA}$ 5432	4.0	-200	nA V
$V_{GS(off)}$	Gate-Source Cutoff Voltage	$V_{DS} = 5.0 \text{ V}, I_D = 3.0 \text{ nA}$ 5432 5433	-4.0 -3.0	-10 -9.0	V
		5434	-1.0	-4.0	V
	, and the second	5433 5434	100 30		mA mA
ON CHAR	RACTERISTICS				
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current*	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 5432 5433	150 100		mA mA
		5434	30		mA
$V_{DS(on)}$	Drain-Source On Voltage	$I_D = 10 \text{ mA}, V_{GS} = 0$ 5432		50	mV
		5433		70	mV
_	Drain-Source On Resistance	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 5432	2.0	100 5.0	mV
r <sub>DS(on)</sub>	Dialii-Source Off Resistance	1D = 10 IIIA, V <sub>GS</sub> = 0 5432 5433	2.0	7.0	$\Omega$
		5434		10	Ω
		$I_D = 0$ , $V_{GS} = 0$ , $f = 1.0 \text{ kHz}$			32
		5432	2.0	5.0	Ω
		5433		7.0	Ω
		5434		10	Ω
	IGNAL CHARACTERISTICS				
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 0$ , $V_{GS} = 10$ V, $f = 1.0$ MHz		30	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	$V_{DS} = 0$ , $V_{GS} = 10 \text{ V}$ , $f = 1.0 \text{ MHz}$		15	pF
SWITCHIN	NG CHARACTERISTICS				
t <sub>d</sub>	Delay Time	$V_{DD} = 1.5 \text{ V}, \ V_{GS(on)} = 0,$		4.0	ns
t <sub>r</sub>	Rise Time	I <sub>D(on)</sub> = 10 mA		1.0	ns
ts	Storage Time	$V_{GS(off)} = 12 \text{ V},$			
ıs		$V_{GS(off)} = 12 \text{ V},$ $V_{DS(on)} = 50 \text{ mV}$ 5432		6.0	ns
		V <sub>DS(on)</sub> = 50 IIIV 5432		6.0	ns

 $V_{DS(on)} = 70 \text{ mV}$ 

 $V_{GS(off)} = 12 \text{ V}$ 

 $V_{DS(on)} = 100 \text{ mV}$ 

5433

5434

6.0

6.0

30

ns

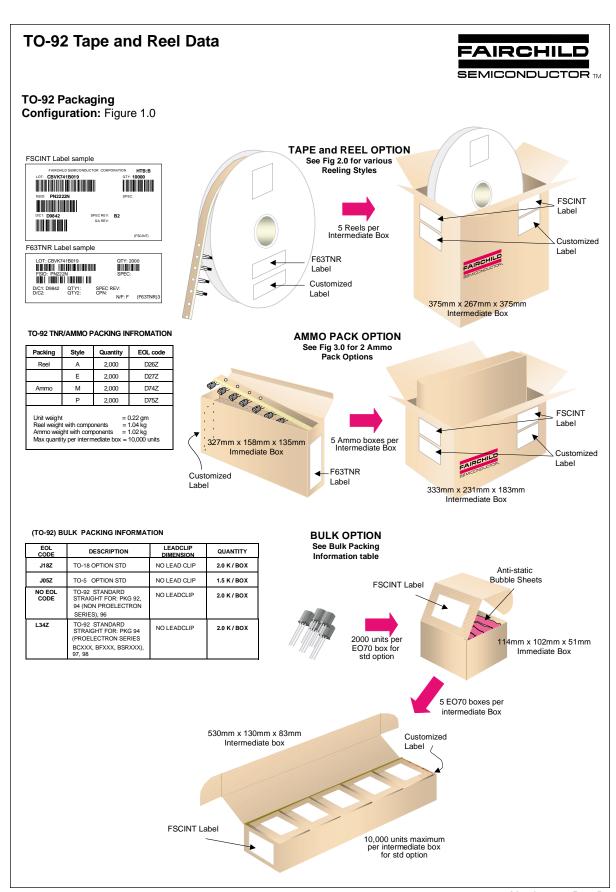
ns

ns

Fall Time

tf

<sup>\*</sup>Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%

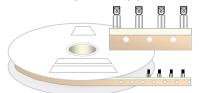


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# TO-92 Tape and Reel Data, continued

### **TO-92 Reeling Style** Configuration: Figure 2.0

### Machine Option "A" (H)

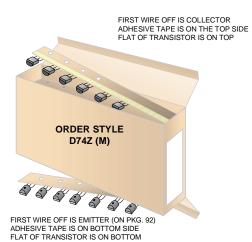


Style "A", D26Z, D70Z (s/h)

# Machine Option "E" (J)

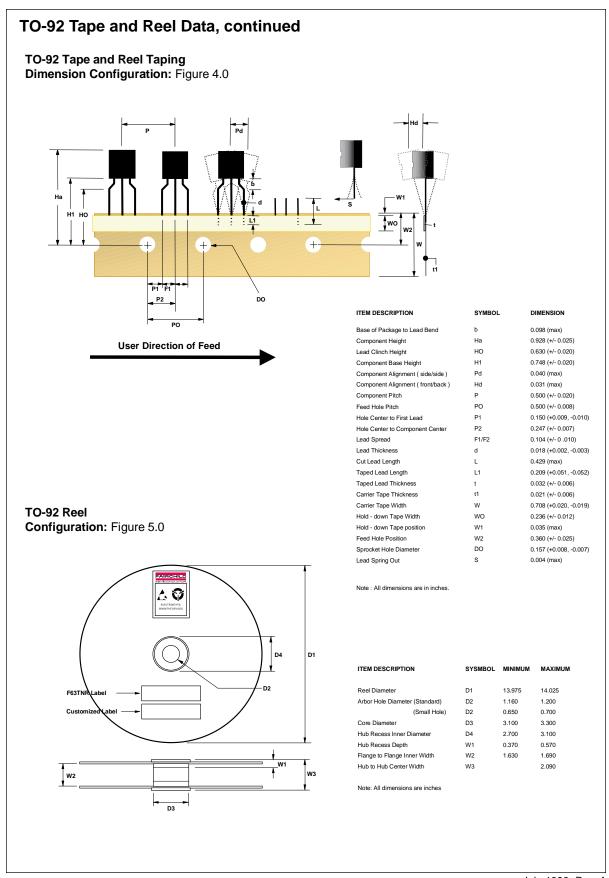
Style "E", D27Z, D71Z (s/h)

### **TO-92 Radial Ammo Packaging** Configuration: Figure 3.0





FIRST WIRE OFF IS COLLECTOR (ON PKG. 92) ADHESIVE TAPE IS ON BOTTOM SIDE FLAT OF TRANSISTOR IS ON TOP



# **TO-92 Package Dimensions** FAIRCHILD SEMICONDUCTOR TM TO-92 (FS PKG Code 92, 94, 96) Scale 1:1 on letter size paper Dimensions shown below are in: inches [millimeters] Part Weight per unit (gram): 0.1977 0.185 4.70 0.170 4.32 TO-92 (92,94,96) 94 96 B F В В В D D 2 В S С G Ε Ø0.060 [Ø1.52] G В S С G 0.010 [0.254] DEEP 5.0°TYP.

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0.095 0.084 2.13

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E <sup>2</sup> CMOS <sup>TM</sup>	LittleFET™	Quiet Series™	$VCX^{TM}$
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