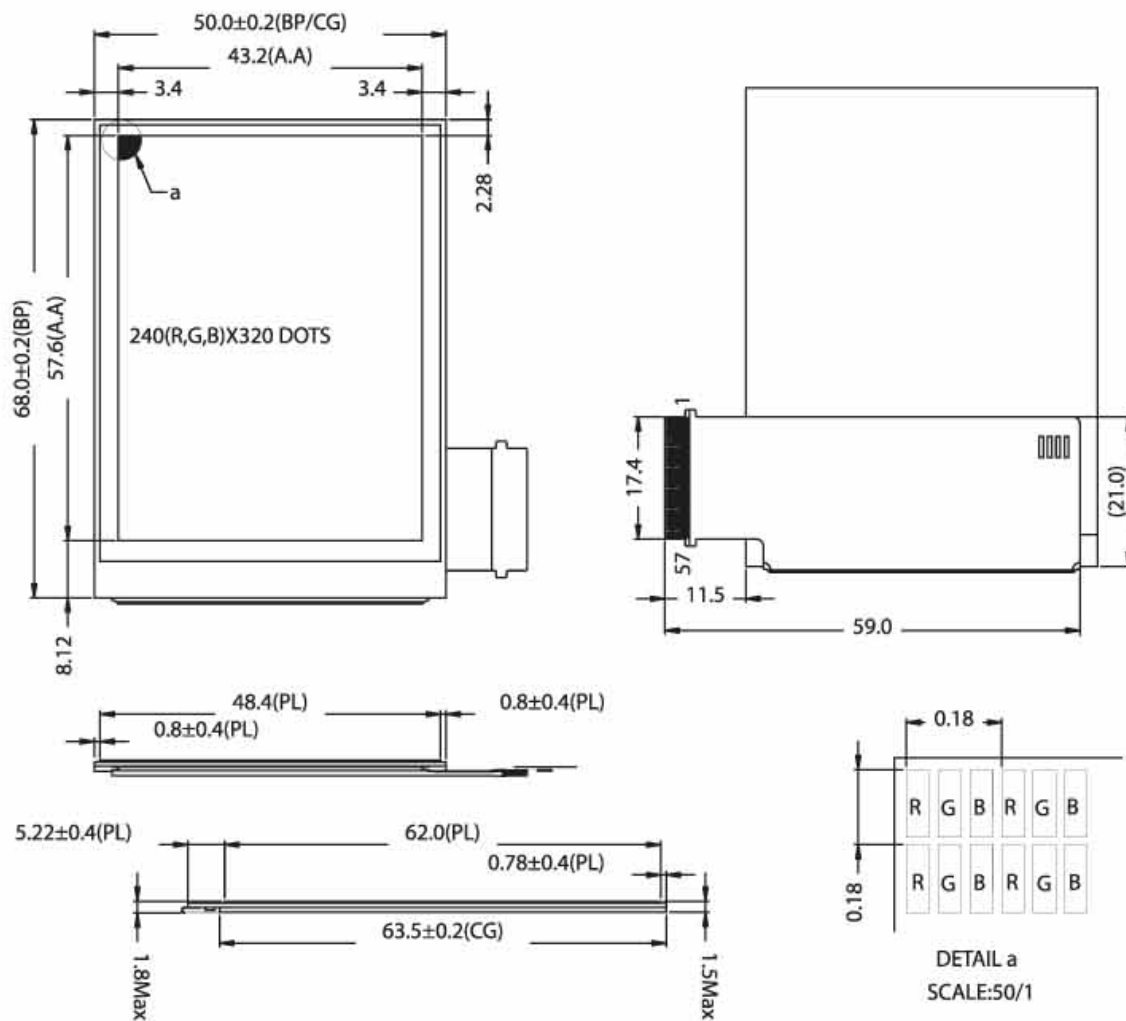




2.8 INCH OLED



1	X+	31	D16
2	Y-	32	DE
3	X-	33	DCLK
4	Y+	34	VSYN
5	C21P	35	HSYN
6	C21N	36	D15
7	C22P	37	D14
8	C22N	38	D13
9	VGH	39	D12
10	HVO	40	D11
11	VGL	41	D10
12	LVO	42	D9
13	AR_REF	43	D8
14	VCC	44	D7
15	VSSD	45	D6
16	C12P	46	D5
17	C12N	47	D4
18	C11P	48	D3
19	C11N	49	D2
20	DDVDH	50	D1
21	VGAMA1OUT	51	D0
22	VDDD	52	SCEN
23	DC_DC_EN	53	SCL
24	D23	54	SDI
25	D22	55	NRESET
26	D21	56	AR_VDD
27	D20	57	AR_VSS
28	D19		
29	D18		
30	D17		

Features:

- Resolution: 240*(RGB) x 320
- LCD option: AMOLED
- Controller IC: HX8351 or equivalent

Pin Assignment		
Pin No.	Symbol	Function
1~4	NC	NC
5~8	CAP	Capacitor selection to DC/DC voltage.
9	VGH	Positive output voltage of the booster2
10	HVO	High voltage output of regulator VGH
11	VGL	Negative output voltage of the booster2
12	LVO	Low voltage output of regulator VGL
13	AR_REF	Panel refer voltage
14	VCC	Power supply for the internal Digital circuit
15	VSSD	Digital ground
16~19	CAP	Capacitor selection to DC/DC voltage.
20	DDVDH	Output voltage of the booster1.
21	VGAMA1OUT	Output voltage of the VGAM1OUT regulator.
22	VDDD	Internal logic voltage input or output pin.
23	DC_DC_EN	External DC/DC enable pin.
24~31	D23~D16	The display data input pins
32	DE	Data enable pin
33	DCLK	CLK pin
34	VSYS	Frame synchronizing signal
35	HSYS	Line synchronizing signal
36~51	D15~D0	The display data input pins
52	SCEN	Serial interface chip enable pin.
53	SCL	Serial interface clock input pin
54	SDI	Serial interface input data line
55	NRESET	Reset pin
56	AR_VDD	Positive voltage for OLED.
57	AR_VSS	Negative voltage for OLED.

