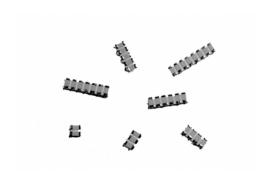
Vishay Sfernice



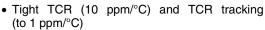
High Precision Resistor Chip Arrays



PRA arrays can be used in most applications requiring a matched pair (or set) of resistor elements. The networks provide 1 ppm/°C TCR tracking, a ratio tolerance as tight as 0.01 % and outstanding stability. They are available in 1 mm, 1.35 mm and 1.82 mm pitch.

FEATURES

 \bullet High stability passivated nichrome resistive layer 0.02 % on ratio, 1000 h at Pn at + 70 $^{\circ}C$



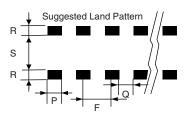
- Very low noise < 35 dB and voltage coefficient < 0.01 ppm/V
- Ratio tolerance to 0.01 % (R≥ 200R)
- Pre-tinned terminations over nickel barrier
- High temperature option (200 °C)
- SMD wraparound chip resistor array
- Compliant to RoHS directive 2002/95/EC



TYPICAL PERFORMANCE

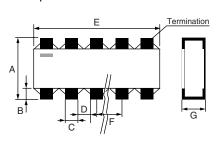
	ABS	TRACKING
TCR	10 ppm/°C	2 ppm/°C
	ABS	RATIO
TOL	0.1 %	0.05 %

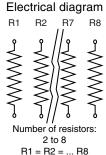
DIMENSIONS



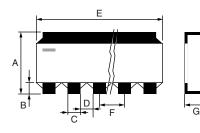
DIM.	PRA 10	0	PRA 135		PRA 182	
DIIVI.	mm	mil	mm	mil	mm	mil
Α	1.6 + 0.2	63	1.85 + 0.2	72	3.0 + 0.2	118
В	0.4 + 0.2 - 0.2	16	0.4 + 0.2 - 0.2	16	0.4 + 0.2 - 0.2	16
С	0.65 + 0.15 - 0.15	25.5	1.05 + 0.15 - 0.15	41	1.3 + 0.35 - 0.15	51
D	0.25	10	0.25	10	0.25	10
E (1)	E = (N x F) ± 0.2 mm			E = (N x F) ± 8 mil		
F	1	40	1.35	53.1	1.82	72
G	0.4 + 0.1	15	0.4 + 0.1	15	0.4 + 0.1	15
Р	0.7	27.5	1.05	41.3	1.52	59.8
Q	0.3	12	0.3	12	0.3	12
R	1	40	1	40	1	40
S	0.6	23.5	0.8	31.5	1.8	70.8

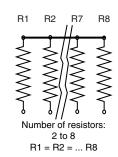
I: Independent resistors





C: One common point N resistors





Note

(1) E depends on number of resistors

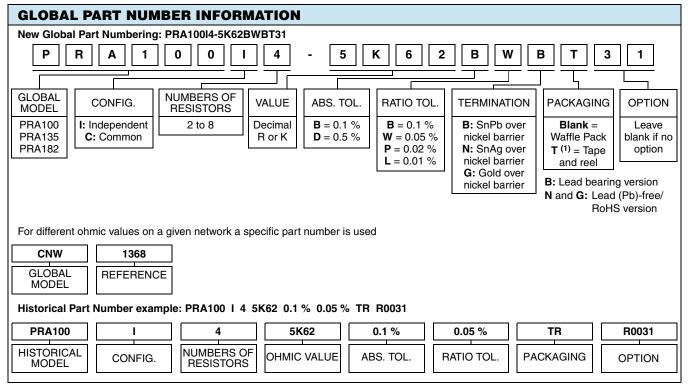
^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

^{**} Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902



High Precision Resistor Chip Arrays

Vishay Sfernice



Note

⁽¹⁾ Tape and reel not available for all sizes - see table

STANDARD ELECTRICAL SPECIFICATIONS				
TEST		SPECIFICATIONS	CONDITION	
	PRA 100	100 Ω to 250 kΩ		
Resistance range:	PRA 135	100 Ω to 400 kΩ		
	PRA 182	100 Ω to 1.6 MΩ		
Tolerance:	Absolute	± 0.5 % to ± 0.1 %		
	Ratio	0.1 %, 0.05 %, 0.02 %, 0.01 % (R ≥ 200 R)		
TCR:	Absolute	± 10 ppm/°C	- 40 °C + 155 °C	
	Ratio	2 ppm/°C (1 ppm/°C on request)	- 40 °C + 155 °C	
	PRA 100	100 mW per resistor	at + 70 °C	
Power rating:	PRA 135	125 mW per resistor	at + 70 °C	
	PRA 182	200 mW per resistor	at + 70 °C	
Operating tempera	ture range ⁽²⁾	- 55 °C to + 155 °C		
Noise		≤ - 35 dB		
Voltage coefficient		≤ 0.01 ppm/V		
Limiting voltage:	PRA 100	50 V		
	PRA 135	100 V		
	PRA 182	150 V		

Note

Document Number: 53033 Revision: 17-Aug-09

 $^{^{(2)}}$ For temperature up to 200 $^{\circ}\text{C},$ please consult factory

PRA 100, 135, 182 (CNW)

Vishay Sfernice

High Precision Resistor Chip Arrays



HIGH TEMPERATURE OPTION: 0031

Vishay Sfernice offers a high temperature option for applications requiring withstanding to temperature up to 200 °C. Please refer to application note "Power Dissipation - High Temperature SCH" www.vishay.com/doc?53047 for recommendations and mounting considerations.

Operating temperature range: - 55 °C; + 200 °C

Minimum ohmic value: 250 Ω

Absolute TCR: 10 ppm/°C typical (maximum 15 ppm/°C)

TCR tracking: 2 ppm/°C typical (maximum 5 ppm/°C)

Load life stability at 200 °C: < 0.1 % at 0.1 Pn after 1000 h

QUICK PROTOTYPING

Vishay Sfernice can offer quick prototyping service in 3 weeks production time for standard and custom networks (independent resistors - For networks with common point lead time might be longer). (Logistics time to add to this lead time). No NRE require for prototyping (small quantity).

Tooling charges might be required for custom networks series (definitive set of masks).

MECHANICAL SPECIFICATIONS			
Substrate	Alumina		
Technology	Thin Film		
Film	Nickel chromium with mineral passivation		
	B type: SnPb over nickel barrier		
Terminations	N type: SnAg over nickel barrier		
	G type: Gold over nickel barrier		

SPECIAL FEATURES

Resistance values can be different on a given network (R max./R min. as high as 300). Tooling charges might be required depending on the ohmic values in the same network. Please, consult Vishay Sfernice for ohmic values, tolerances and also temperature coefficient (e.g. \pm 1 ppm/°C) outside the standard range.

PACKAGING

Several types of packaging are available: Waffle-pack and tape and reel.

		NUMBER OF PIECES	S PER PACKAGE	
SIZE	MOO	WASSI S DAOK MAY QUANTITY DED DOY	TAPE AND REEL (1)	
SIZE	MOQ WAFFLE PACK MAX. QUANTITY PER BOX	MIN.	MAX.	
PRA100 X 2		100	100	4000
PRA100 X 3		140	100	4000
PRA100 X 4		60	100	4000
PRA100 X 5	100	50		
PRA100 X 6		50	100	4000
PRA100 X 7		50		
PRA100 X 8		28	100	4000
PRA135 X 2		140	100	4000
PRA135 X 3		60		
PRA135 X 4		60	100	4000
PRA135 X 5	100	50		
PRA135 X 6		28	100	4000
PRA135 X 7		24		
PRA135 X 8		24		
PRA182 X 2		60	100	4000
PRA182 X 3		60	100	4000
PRA182 X 4		50	100	4000
PRA182 X 5	100	21	100	4000
PRA182 X 6		24		
PRA182 X 7		24		
PRA182 X 8		20		

Note

(1) Other sizes upon request

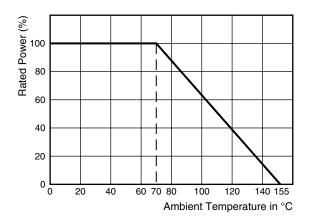
For technical questions, contact: sfer@vishay.com
Document Number: 53033
Revision: 17-Aug-09



High Precision Resistor Chip Arrays

Vishay Sfernice

POWER RATING



MARKING

On the primary package, printed information includes VISHAY S.A. trademark series and model, schematic number of resistors, ohmic value, absolute tolerance, ratio tolerance, type of termination: B tinned over nickel barrier.

PERFORMANCE			
	CONDITIONS	DRIF	TS
TESTS	CONDITIONS CECC REQUIREMENTS	ABSOLUTE PER (Typical Values)	RATIO
Overload	2.5 Un/2 s	0.05 % Rn + 0.05 Ω	0.01 % Rn
Climatic sequences	- 55 °C + 155 °C/5 moisture cycles	0.1 % Rn + 0.05 Ω	0.01 % Rn
Thermal shock	- 55 °C + 155 °C/5 cycles 30'	0.05 % Rn + 0.05 Ω	0.01 % Rn
Load life	1000 h/Pn at + 70 °C	0.1 % Rn + 0.05 Ω	0.02 % Rn
Resistance to solder heat	260 °C/10 s	0.05 % Rn + 0.05 Ω	0.01 % Rn
Moisture resistance	0.01 Pn at + 40 °C 93 % RH	0.1 % Rn + 0.05 Ω	0.01 % Rn
High temperature storage	1000 h/no load at + 155 °C	0.1 % Rn + 0.05 Ω	0.02 % Rn

Note

• Rn: Nominal resistance

Document Number: 53033 Revision: 17-Aug-09



Vishay

Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.

Document Number: 91000 Revision: 18-Jul-08