## PS2705A-1

**Data Sheet** 

R08DS0073EJ0500 Rev.5.00 Jan 9, 2013

#### HIGH ISOLATION VOLTAGE SOP PHOTOCOUPLER

#### **DESCRIPTION**

The PS2705A-1 is an optically coupled isolator containing a GaAs light emitting diode and an NPN silicon phototransistor to realize an excellent cost performance.

This package is SOP (Small Outline Package) type and has shield effect to cut off ambient light.

It is designed for high density mounting applications.

#### **FEATURES**

- AC input response
- High isolation voltage (BV = 3 750 Vr.m.s.)
- SOP (Small Outline Package) type
- Ordering number of taping product: PS2705A-1-F3: 3 500 pcs/reel
- Pb-Free product

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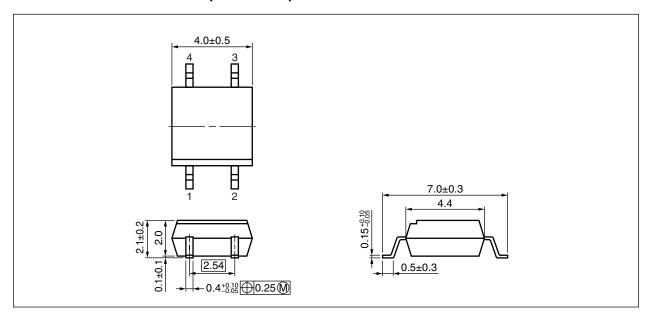
- · Safety standards
  - UL approved: No. E72422
  - CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950)
  - SEMKO, NEMKO, DEMKO, FIMKO approved (EN 60065, EN 60950)
  - DIN EN 60747-5-5 (VDE 0884-5) approved (Option)

# PIN CONNECTION (Top View) 1. Anode, Cathode 2. Cathode, Anode 3. Emitter 4. Collector

#### **APPLICATIONS**

- Hybrid IC
- Telephone/FAX.
- FA/OA equipment
- Programmable logic controllers
- Power supply

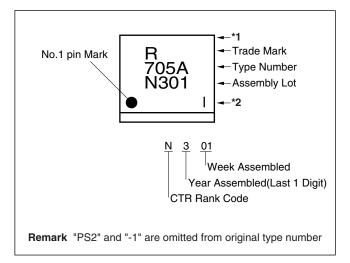
## PACKAGE DIMENSIONS (UNIT: mm)



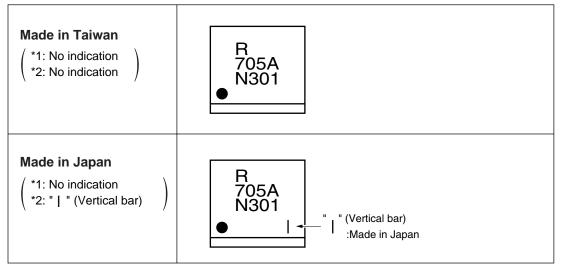
## PHOTOCOUPLER CONSTRUCTION

Parameter	Unit (MIN.)	
Air Distance	5 mm	
Outer Creepage Distance	e 5 mm	
Inner Creepage Distance	2.5 mm	
Isolation Distance	0.3 mm	

### <R> MARKING EXAMPLE



Note: Bar indication contents of \*1 and \*2.



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## <R> ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number*1
PS2705A-1-F3	PS2705A-1-F3-A	Pb-Free	Embossed Tape 3 500 pcs/reel	Standard products (UL, CSA, SEMKO, NEMKO, DEMKO, FIMKO approved)	PS2705A-1
PS2705A-1-V-F3	PS2705A-1-V-F3-A		Embossed Tape 3 500 pcs/reel	DIN EN 60747-5-5 (VDE 0884-5) Approved (Option)	

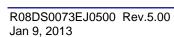
Note: \*1. For the application of the Safety Standard, following part number should be used.

## ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise specified)

	Parameter	Symbol	Ratings	Unit	
Diode	Forward Current (DC)	I <sub>F</sub>	±50	mA	
	Power Dissipation Derating	⊿P <sub>D</sub> /°C	0.8	mW/°C	
	Power Dissipation	P <sub>D</sub>	80	mW	
	Peak Forward Current*1	I <sub>FP</sub>	±0.5	А	
Transistor	Collector to Emitter Voltage	V <sub>CEO</sub>	70	V	
	Emitter to Collector Voltage		5	V	
	Collector Current		30	mA	
	Power Dissipation Derating		1.5	mW/°C	
Power Dissipation		Pc	150	mW	
Isolation Vo	Isolation Voltage*2		3 750	Vr.m.s.	
Operating Ambient Temperature		T <sub>A</sub>	-55 to +100	°C	
Storage Te	mperature	T <sub>stg</sub>	-55 to +150	°C	

Note: \*1. PW = 100  $\mu$ s, Duty Cycle = 1%

\*2. AC voltage for 1 minute at  $T_A$  = 25°C, RH = 60% between input and output. Pins 1-2 shorted together, 3-4 shorted together.







## ELECTRICAL CHARACTERISTICS ( $T_A = 25$ °C)

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	V <sub>F</sub>	$I_F = \pm 5 \text{ mA}$		1.2	1.4	V
	Terminal Capacitance	Ct	V = 0 V, f = 1.0 MHz		20		pF
Transistor	Collector to Emitter Dark Current	I <sub>CEO</sub>	I <sub>F</sub> = 0 mA, V <sub>CE</sub> = 70 V			100	nA
Coupled	Current Transfer Ratio (I <sub>C</sub> /I <sub>F</sub> ) <sup>*1</sup>	CTR	$I_F = \pm 5 \text{ mA}, V_{CE} = 5 \text{ V}$	50		300	%
	Collector Saturation Voltage	V <sub>CE (sat)</sub>	$I_F = \pm 10 \text{ mA}, I_C = 2 \text{ mA}$		0.13	0.3	V
	Isolation Resistance	R <sub>I-O</sub>	$V_{I-O} = 1.0 \text{ kV}_{DC}$	10 <sup>11</sup>			Ω
	Isolation Capacitance	C <sub>I-O</sub>	V = 0 V, f = 1.0 MHz		0.4		pF
	Rise Time*2	t <sub>r</sub>	$V_{CC} = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$		5		μS
	Fall Time*2	t <sub>f</sub>			7		
	Turn-on Time*2	t <sub>on</sub>			8		
	Turn-off Time*2	t <sub>off</sub>			10		

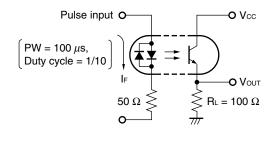


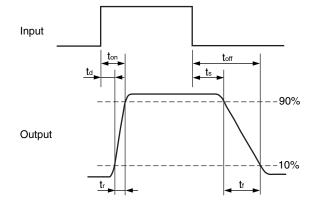
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Note: \*1. CTR rank

N: 50 to 300 (%) L: 100 to 300 (%) M: 50 to 150 (%)

\*2. Test Circuit for Switching Time

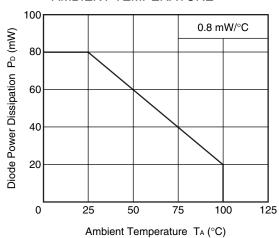




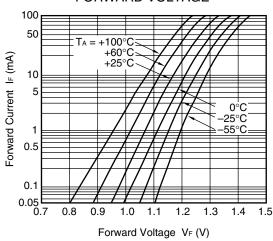
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## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C, unless otherwise specified)

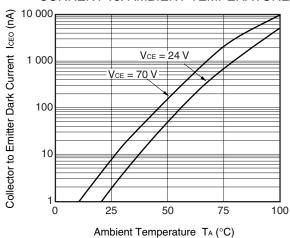
# DIODE POWER DISSIPATION vs. AMBIENT TEMPERATURE



# FORWARD CURRENT vs. FORWARD VOLTAGE

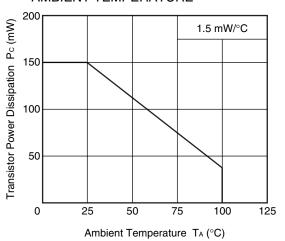


# COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE

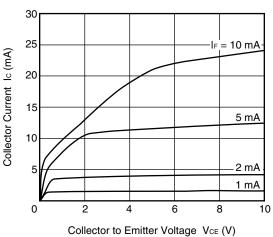


#### **Remark** The graphs indicate nominal characteristics.

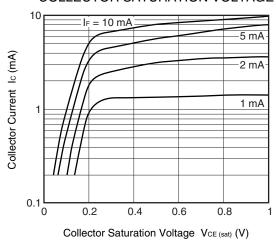
# TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



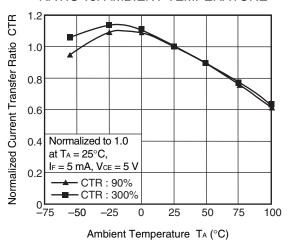
# COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



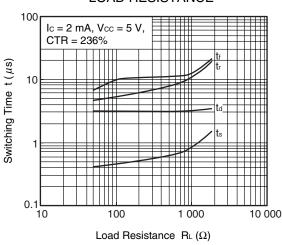
# COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE



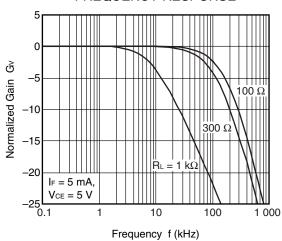
# NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



# SWITCHING TIME vs. LOAD RESISTANCE

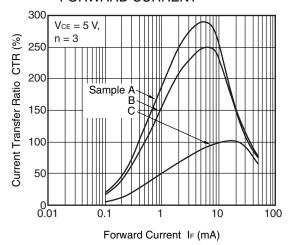


#### FREQUENCY RESPONSE

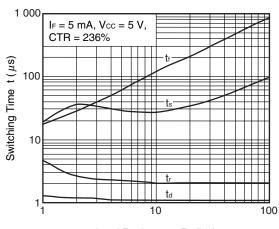


Remark The graphs indicate nominal characteristics.

# CURRENT TRANSFER RATIO vs. FORWARD CURRENT

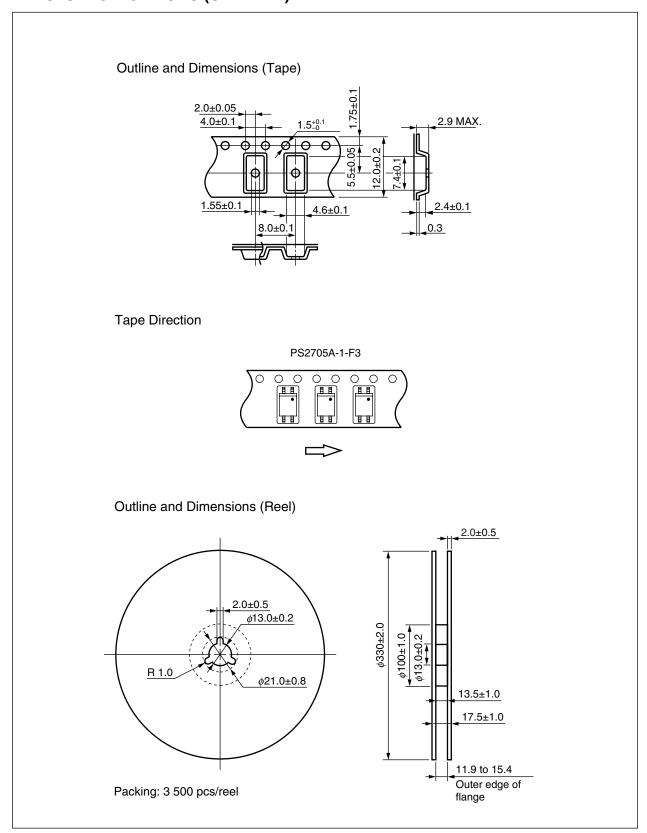


# SWITCHING TIME vs. LOAD RESISTANCE



Load Resistance R<sub>L</sub> (kΩ)

## <R> TAPING SPECIFICATIONS (UNIT: mm)



#### NOTES ON HANDLING

- 1. Recommended soldering conditions
  - (1) Infrared reflow soldering

Peak reflow temperature 260°C or below (package surface temperature)

Time of peak reflow temperature 10 seconds or less Time of temperature higher than 220°C 60 seconds or less

Time to preheat temperature from 120 to 180°C 120±30 s Three

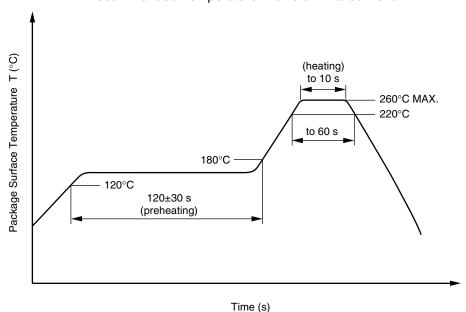
Number of reflows

Flux Rosin flux containing small amount of chlorine (The flux

with a maximum chlorine content of 0.2 Wt% is

recommended.)

#### Recommended Temperature Profile of Infrared Reflow



#### (2) Wave soldering

Temperature 260°C or below (molten solder temperature)

Time 10 seconds or less

Preheating conditions 120°C or below (package surface temperature)

Number of times One (Allowed to be dipped in solder including plastic mold portion.)

Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

#### (3) Soldering by Soldering Iron

Peak Temperature (lead part temperature) 350°C or below

Time (each pin) 3 seconds or less

Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

#### (4) Cautions

 Fluxes Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent. 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collectoremitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler
 Check the setting values before use, since the forward current conditions at CTR measurement differ according to
 product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. This tendency may sometimes be obvious, especially below IF = 1 mA.

Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

#### **USAGE CAUTIONS**

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.



## SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

	Parameter	Symbol	Spec.	Unit	
	Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/100/21		
	Dielectric strength				
	maximum operating isolation voltage	$U_{IORM}$	707	$V_{peak}$	
(R>	Test voltage (partial discharge test, procedure a for type test and	$U_pr$	1 131	$V_{peak}$	
	random test)				
:R>	$Upr = 1.6 \times U_{IORM}, P_d < 5 pC$				
	Test voltage (partial discharge test, procedure b for all devices)	Upr	1 325	$V_{peak}$	
	$U_{pr} = 1.875 \times U_{IORM}, P_d < 5 pC$				
	Highest permissible overvoltage	U <sub>TR</sub>	6 000	$V_{peak}$	
	Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2		
-	Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303	CTI	175		
	Part 11))				
	Material group (DIN EN 60664-1 VDE0110 Part 1)		III a		
	Storage temperature range	T <sub>stg</sub>	-55 to +150	°C	
	Operating temperature range	T <sub>A</sub>	-55 to +100	°C	
	Isolation resistance, minimum value				
	$V_{IO} = 500 \text{ V dc at } T_A = 25^{\circ}\text{C}$	Ris MIN.	10 <sup>12</sup>	Ω	
	$V_{IO}$ = 500 V dc at $T_A$ MAX. at least 100°C	Ris MIN.	10 <sup>11</sup>	Ω	
	Safety maximum ratings (maximum permissible in case of fault, see				
	thermal derating curve)				
	Package temperature	Tsi	150	°C	
	Current (input current I <sub>F</sub> , Psi = 0)	lsi	300	mA	
	Power (output or total power dissipation)	Psi	500	mW	
	Isolation resistance				
	$V_{IO} = 500 \text{ V dc at } T_A = Tsi$	Ris MIN.	10 <sup>9</sup>	Ω	

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
  - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

## PS2705A-1 Data Sheet

		Description				
Rev.	Date	Page	Summary			
1.00	1.00 May 20, 2004 –		This data sheet was released as PN10397EJ01V0DS.			
5.00	5.00 Jan 9, 2013 Throughout		Renesas format is applied to this data sheet.			
		p.1	PS2705A-1-F4 is deleted from the ordering number of taping product.			
			The safety standards are revised.			
		p.3	The explanation in MARKING EXAMPLE is revised.			
		p.4	ORDERING INFORMATION is modified with the revision of the safety standards.			
			The value in Ratings of Parameter "Forward Current (DC)" is changed from 30 to 50.			
		p.5	Turn-on Time (ton) and Turn-off Time (toff) are added to the table in ELECTRICAL CHARACTERISTICS.			
p.7			The timing chart for Rise Time, Fall Time, Turn-on Time, and Turn-off Time is added.			
		p.7	The graph of LONG TERM CTR DEGRADATION is deleted from those in TYPICAL CHARACTERISTICS.			
	p.8		PS2705A-1-F4 is deleted form Tape Direction image in TAPING SPECIFICATIONS.			
		p.11	The explanation in SPECIFICATION OF VDE MARKS LICENSE DOCUMENT is revised.			

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