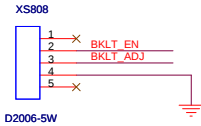
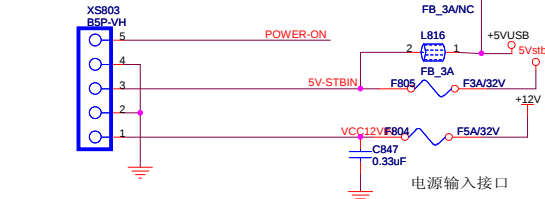


	5	4	3	2	1
D	2012.02.01 增加R428/100R 3.3Vear供电更改到3.3Vstb 增加N808 增加L805, L817 增加L825, L826（L825需要贴！！！！） 增加RU22,RU23 C633 , C634更改为0805 增加R454, R450				D
C	2012.04.25 增加wifi/USB1兼容部分				C
	2012.04.27 wifi/USB1兼容部分增加了电阻RU24、RU25				
	2012.05.14 增加了按键接口XS650/XS651				
B					B
A					A
	5	4	3	2	1

针对5V/1A的降成本电源，POWER-ON控制信号从此处走，跟DC-DC共用一个控制信号，R831需要更改为47K。否则POWER-ON电平会被拉低。



Feedback Voltage = 0.923

The diagram shows a voltage regulation circuit. A 3.3V input is connected to the input of an N803 AZ1117-1.5 LDO regulator. The regulator's output is connected to a 1.5V output terminal. The circuit includes several bypass capacitors: C890 (10uF) at the input, C885 (0.1uF) at the regulator's input, C886 (0.1uF) at the regulator's output, C887 (100uF) at the output, and C888 (100uF) at the 1.5V output. An inductor L819 is connected between the 3.3V input and the regulator's input. A resistor R851 (15R/1%) is connected between the 3.3V input and ground. The text '什么作用' (What is the function?) is placed below the circuit.

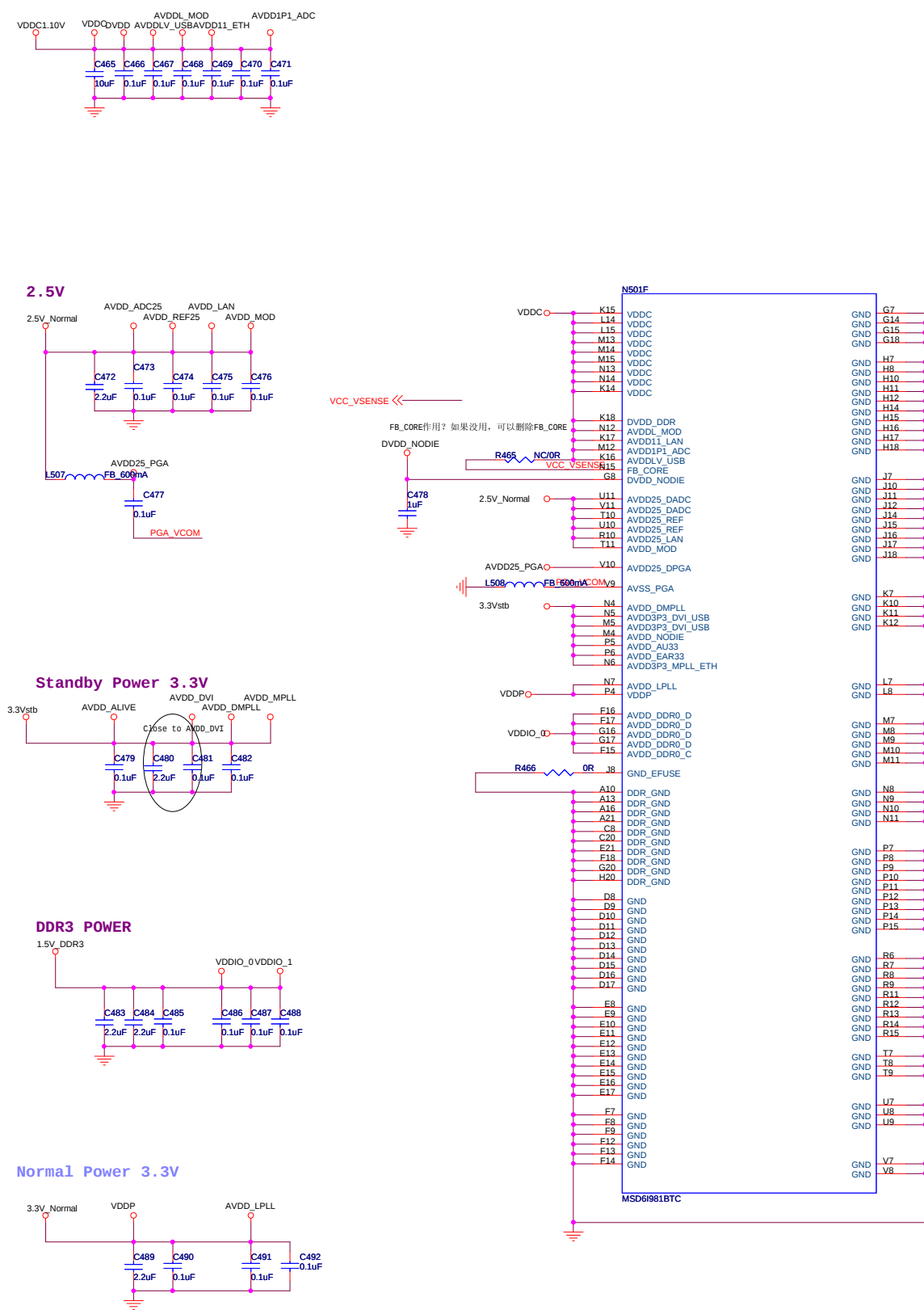
[illegible]

The schematic diagram illustrates a 2.5V LDO regulator circuit. The input is connected to a 5V source through a 0.1μF capacitor (C883). The input is also connected to the LDO's input pin. The LDO's output pin is connected to the 2.5V output terminal. The feedback pin is connected to the output through a feedback resistor (L820 FB_600mA). The LDO is an N804 AMS1117-2.5. The output is also connected to a 0.1μF capacitor (C889). The LDO's ground pin is connected to ground through a 10μF capacitor (C880) and a 0.1μF capacitor (C881). The output is also connected to a 10μF capacitor (C882) and a 0.1μF capacitor (C889). The output is also connected to a 2.5V output terminal.

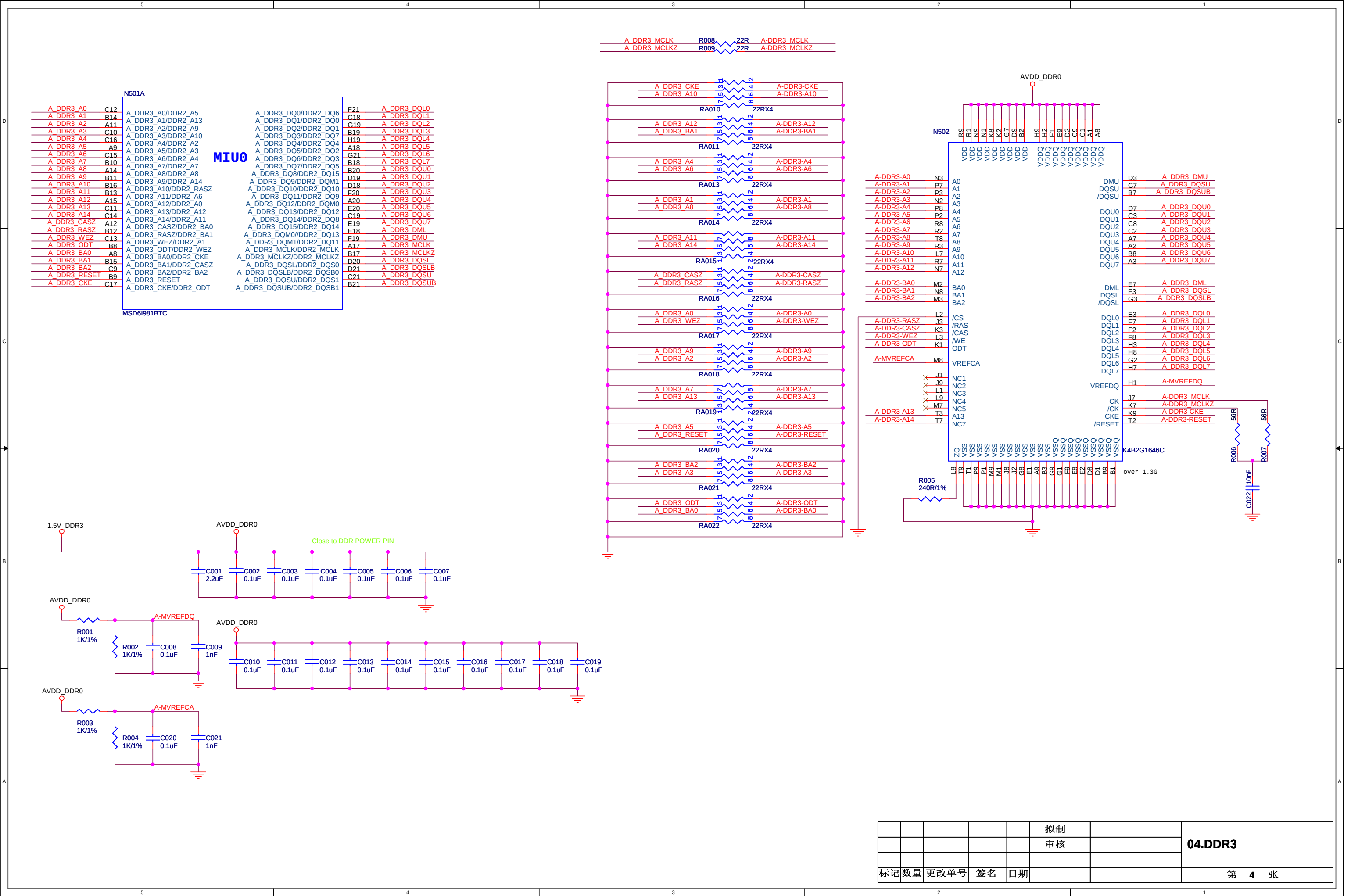
The diagram illustrates a 5V to 3.3V DC-DC converter circuit. The input is a 5V source connected to the ADJ pin (pin 1) of the AMS1117-3.3 voltage regulator. The GND pin (pin 2) is connected to ground. The OUT pin (pin 3) is connected to the output of the regulator, which is also connected to the FB pin (pin 4). The output is filtered by a 10μF capacitor (C876) and a 0.1μF capacitor (C875) in parallel. The output voltage is 3.3V Normal. The regulator is labeled NB07 AMS1117-3.3. The output is connected to a load through a 10μF capacitor (C879) and a 0.1μF capacitor (C891) in parallel. The load is connected to ground. The output voltage is 3.3V Normal.

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Power interface

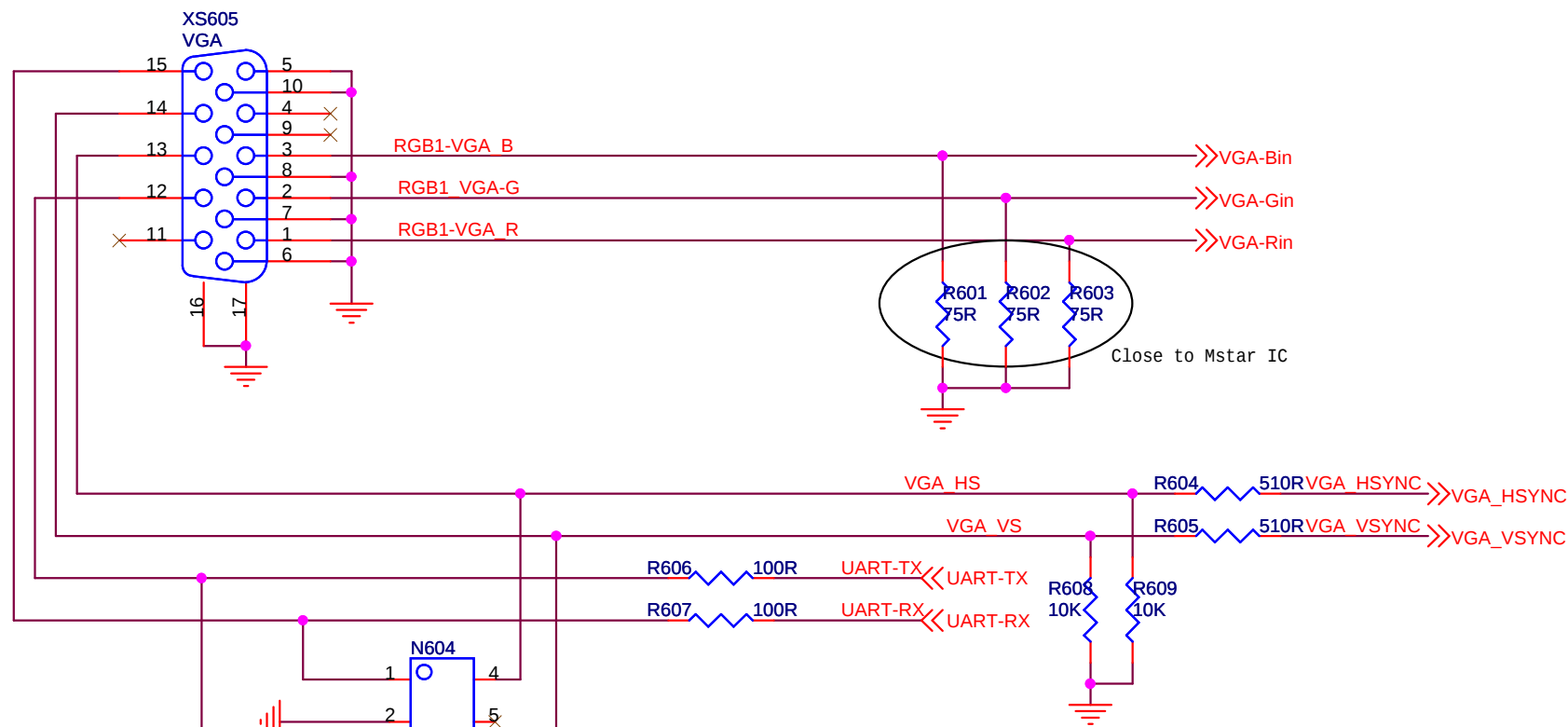


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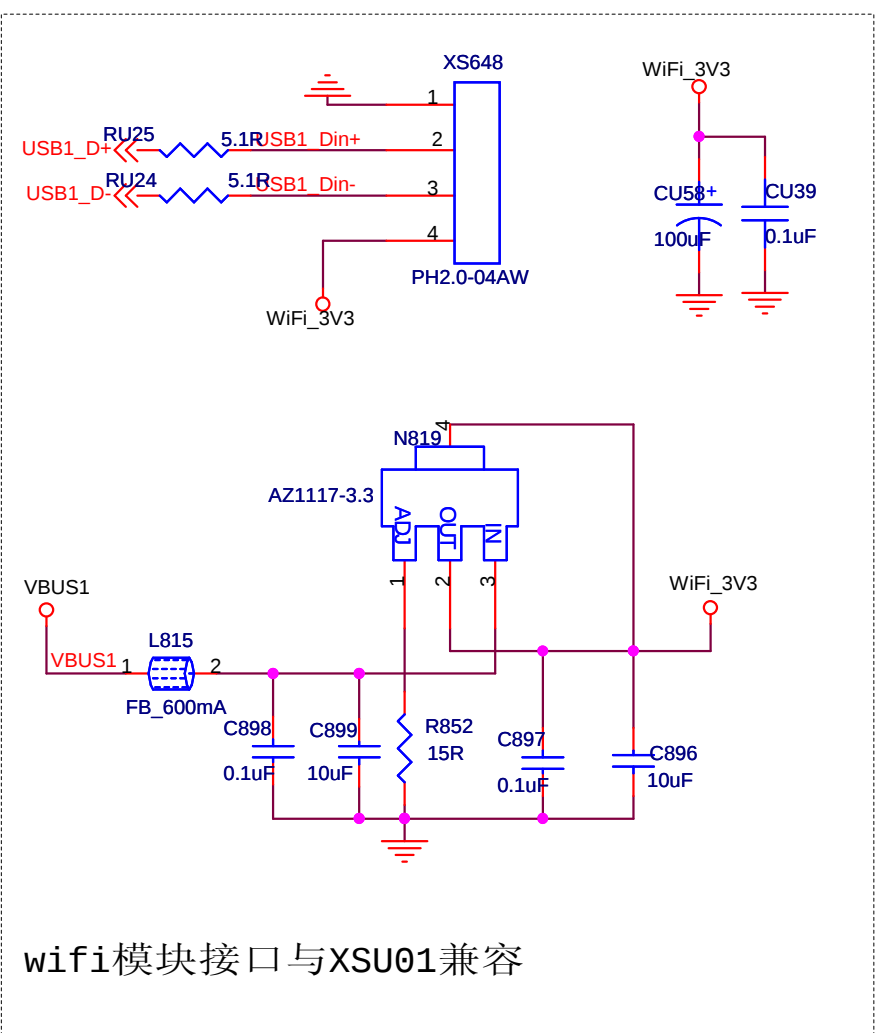
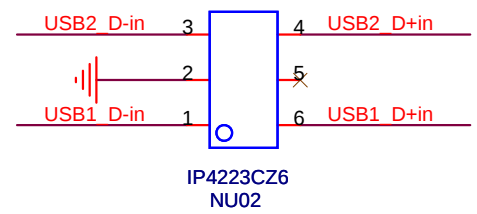
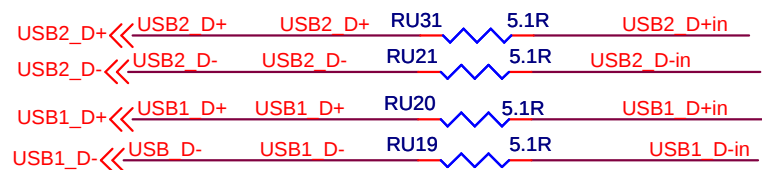


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VGA

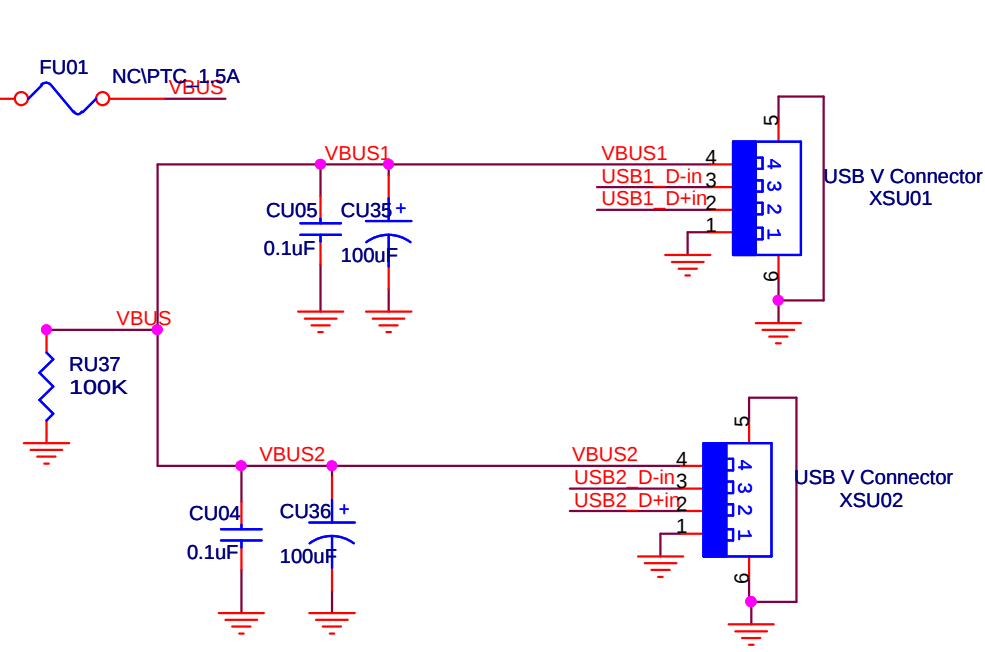
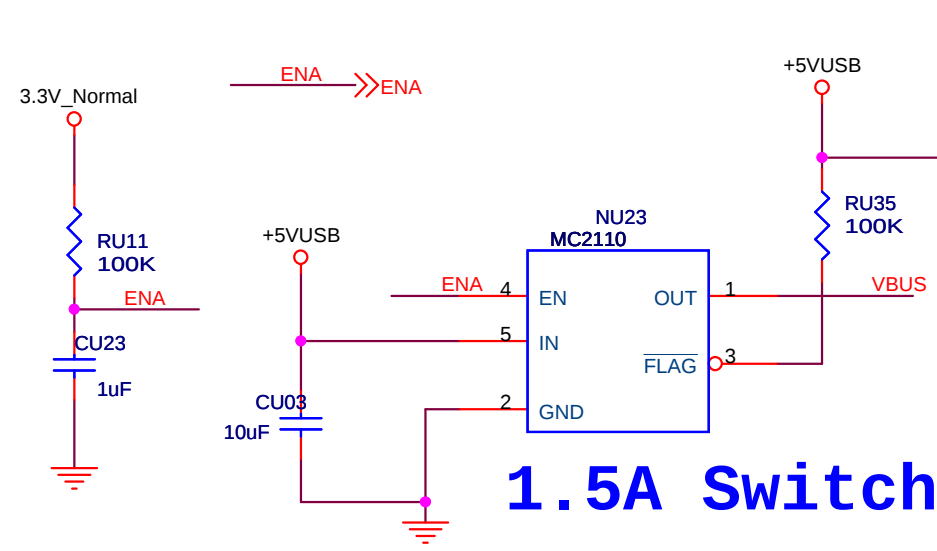


Connector



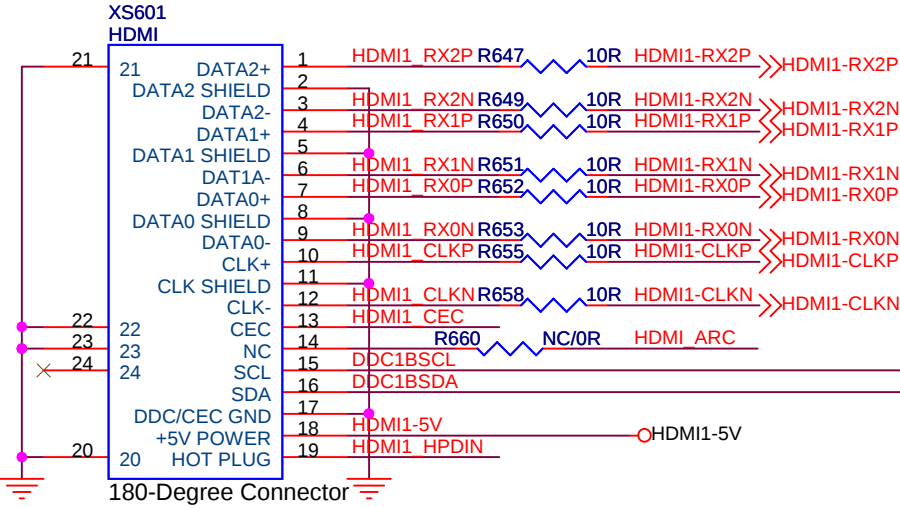
wifi模块接口与XSU01兼容

1.5A Switch

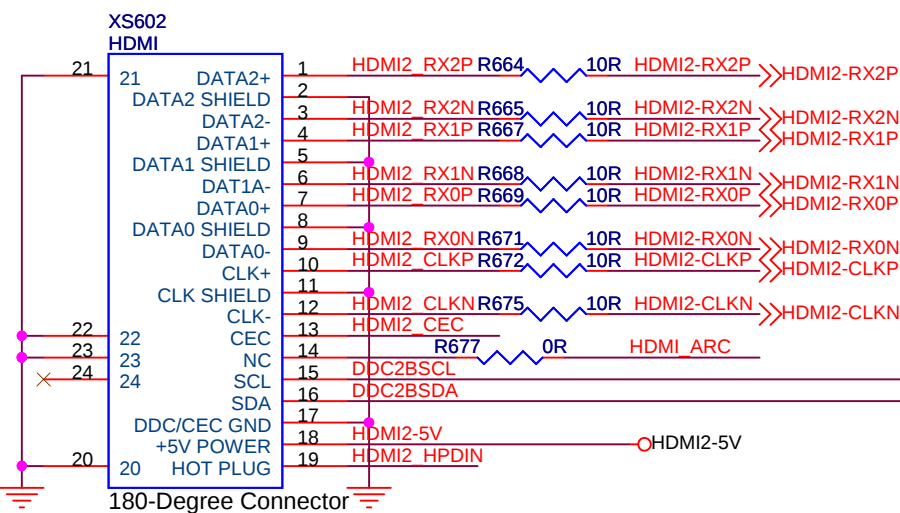


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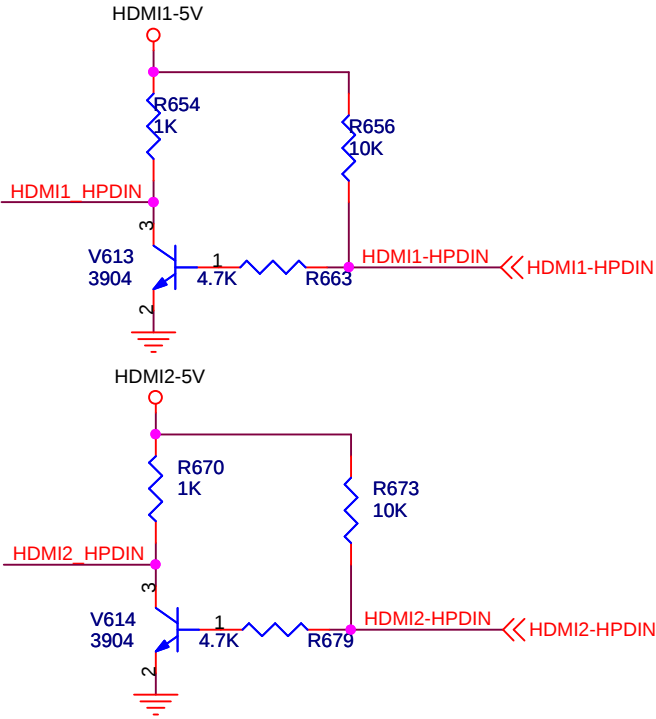
1st HDMI



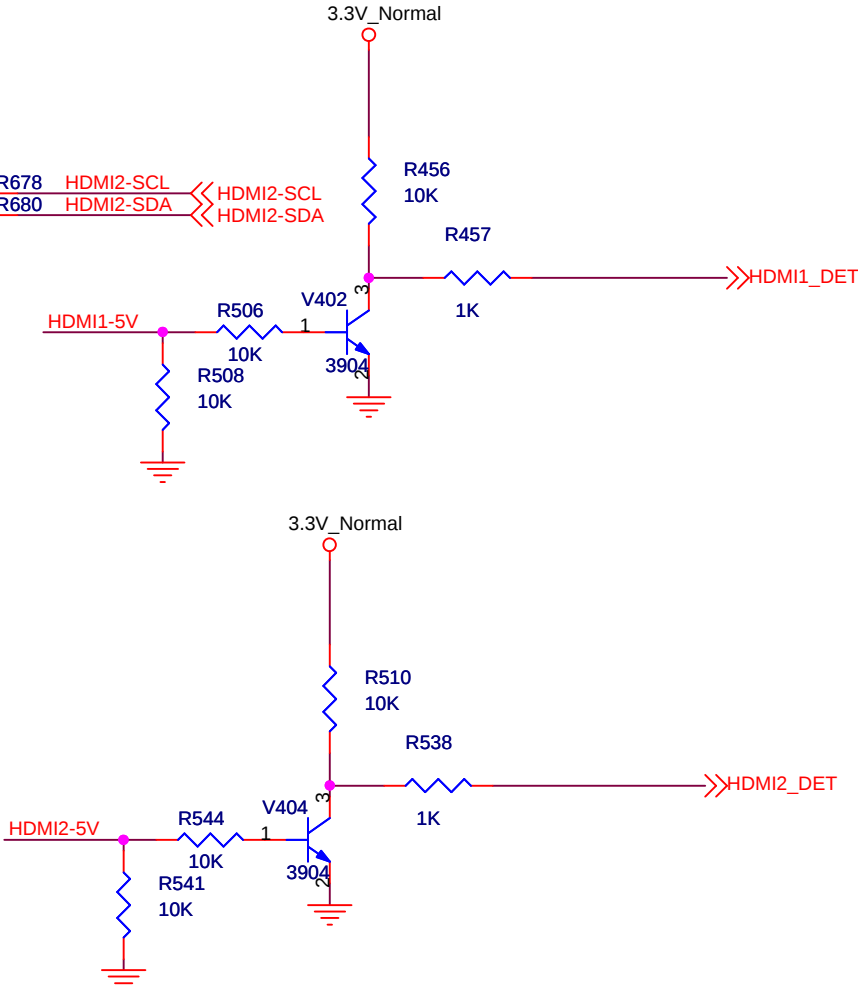
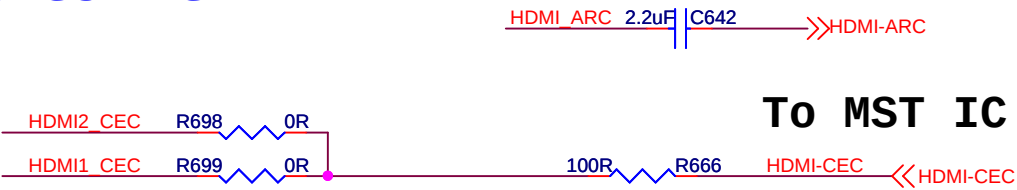
2nd HDMI



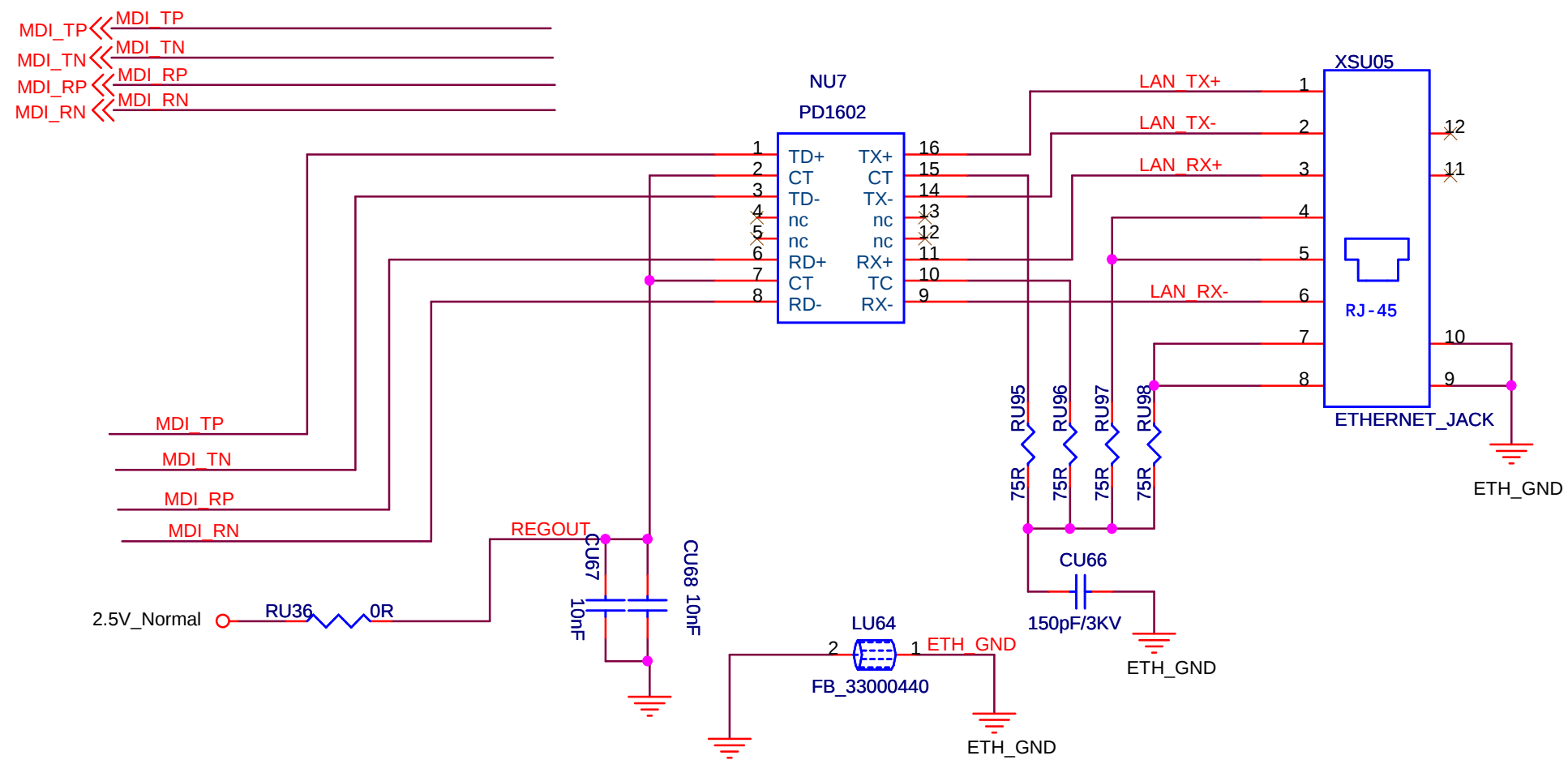
Hot-Plug Control



For CEC&ARC

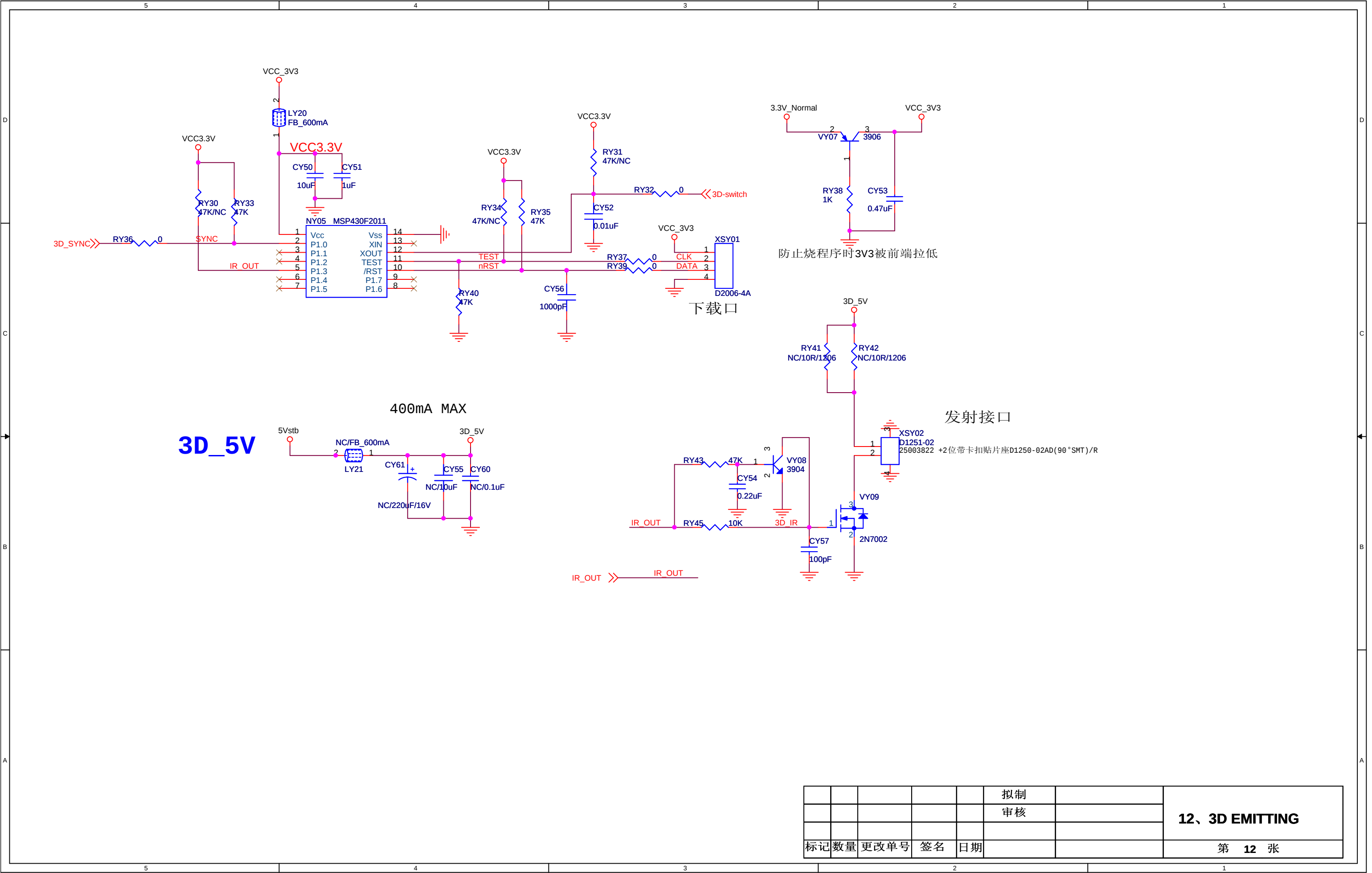


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第 11 张



					拟制		12、3D EMITTING
					审核		
标记	数量	更改单号	签名	日期			第 12 张