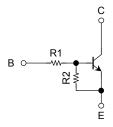
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor Built-in Transistor)

RN1901FS,RN1902FS,RN1903FS RN1904FS,RN1905FS,RN1906FS

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into a fine pitch small mold (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
 Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.
- Complementary to RN2901FS~RN2906FS

Equivalent Circuit and Bias Resistor Values



Type No.	R1 (kΩ)	R2 (kΩ)
RN1901FS	4.7	4.7
RN1902FS	10	10
RN1903FS	22	22
RN1904FS	47	47
RN1905FS	2.2	47
RN1906FS	4.7	47

1.0±0.05 0.1±0.05 0.8±0.05 0.1±0.05 0.07±0.09 0.07

Unit: mm

3.COLLECTOR2 (C2)
4. EMIITTER2 (E2)
5.BASE2 (B2)
6.COLLECTOR1 (C1)

JEDEC —

JEITA —
TOSHIBA 2-1F1D

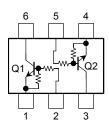
Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage	RN1901FS~	V_{CBO}	20	V	
Collector-emitter voltage	1906FS	V _{CEO}	20	V	
Emitter-base voltage	RN1901FS~ 1904FS	\/ a	10	V	
	RN1905FS, 1906FS	V _{EBO}	5		
Collector current		IC	50	mA	
Collector power dissipation	RN1901FS~	P _C (Note)	50	mW	
Junction temperature	RN1906FS	Tj	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Note: Total rating

Equivalent Circuit (top view)

Weight: 0.001g (typ.)

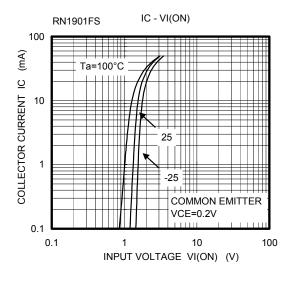


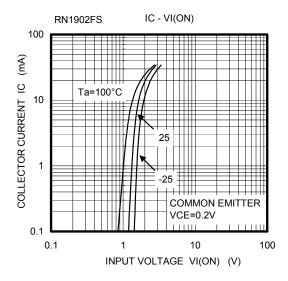


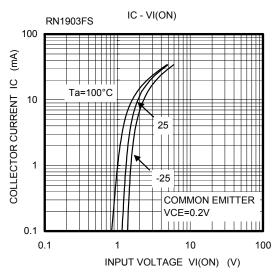
Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

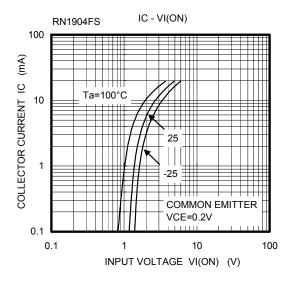
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	RN1901FS~1906FS	I _{CBO}	$V_{CB} = 20 \text{ V}, I_E = 0$	_		100	nA
		I _{CEO}	$V_{CE} = 20 \text{ V}, I_B = 0$	_	_	500	
Emitter cut-off current	RN1901FS	- ІЕВО	V _{EB} = 10 V, I _C = 0	0.89	_	1.33	- mA
	RN1902FS			0.41	_	0.63	
	RN1903FS			0.18	_	0.29	
	RN1904FS			0.088	_	0.133	
	RN1905FS		V _{EB} = 5 V, I _C = 0	0.085	_	0.127	
	RN1906FS			0.08		0.121	
DC current gain	RN1901FS			30	_	_	
	RN1902FS			60		_	
	RN1903FS	h _{FE}	$V_{CE} = 5 \text{ V}, I_{C} = 10 \text{ mA}$	100		_	
	RN1904FS			120		_	
	RN1905FS			120		_	
	RN1906FS			120	_	_	
Collector-emitter saturation voltage	RN1901FS~1906FS	V _{CE (sat)}	$I_C = 5 \text{ mA},$ $I_B = 0.25 \text{ mA}$	_	_	0.15	V
	RN1901FS		$V_{CE} = 0.2 \text{ V}, I_C = 5 \text{ mA}$	1.0	_	2.0	V
	RN1902FS	VI (ON)		1.0	_	2.2	
Innut valtage (ON)	RN1903FS			1.1	_	2.7	
Input voltage (ON)	RN1904FS			1.2	_	3.6	
	RN1905FS			0.6		1.1	
	RN1906FS			0.6		1.2	
Input voltage (OFF)	RN1901FS~1904FS	V _{I (OFF)}	V _{CE} = 5 V, I _C = 0.1 mA	0.8		1.5	V
	RN1905FS, 1906FS			0.4	_	0.8	
Collector output capacitance	RN1901FS~1906FS	C _{ob}	$V_{CB} = 10 \text{ V}, I_{E} = 0,$ f = 1 MHz	_	1.2	_	pF
Input resistor	RN1901FS	- R1	_	3.76	4.7	5.64	kΩ
	RN1902FS			8	10	12	
	RN1903FS			17.6	22	26.4	
	RN1904FS			37.6	47	56.4	
	RN1905FS			1.76	2.2	2.64	
	RN1906FS			3.76	4.7	5.64	
Resistor ratio	RN1901FS~1904FS		_	0.8	1.0	1.2	
	RN1905FS	R1/R2		0.0376	0.0468	0.0562	
	RN1906FS			0.08	0.1	0.12	

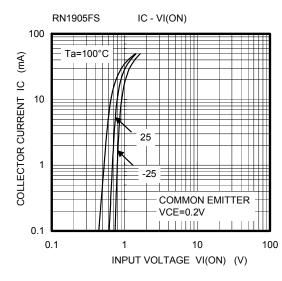
(Q1, Q2 Common)

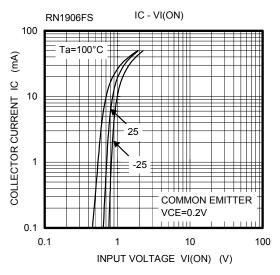




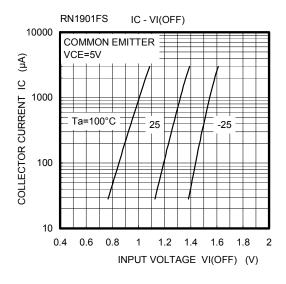


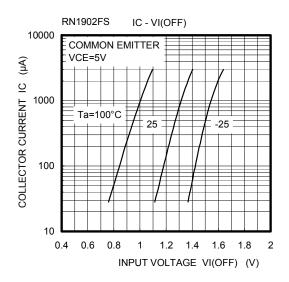


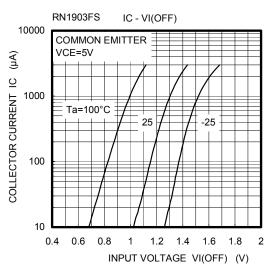


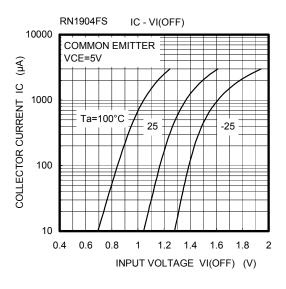


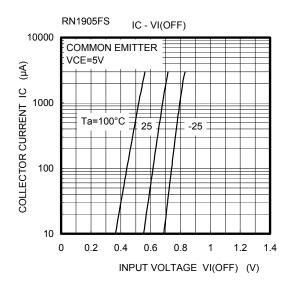
(Q1,Q2 Common)

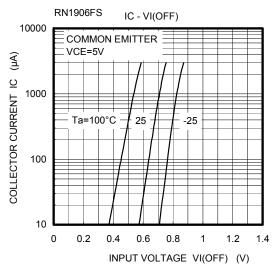




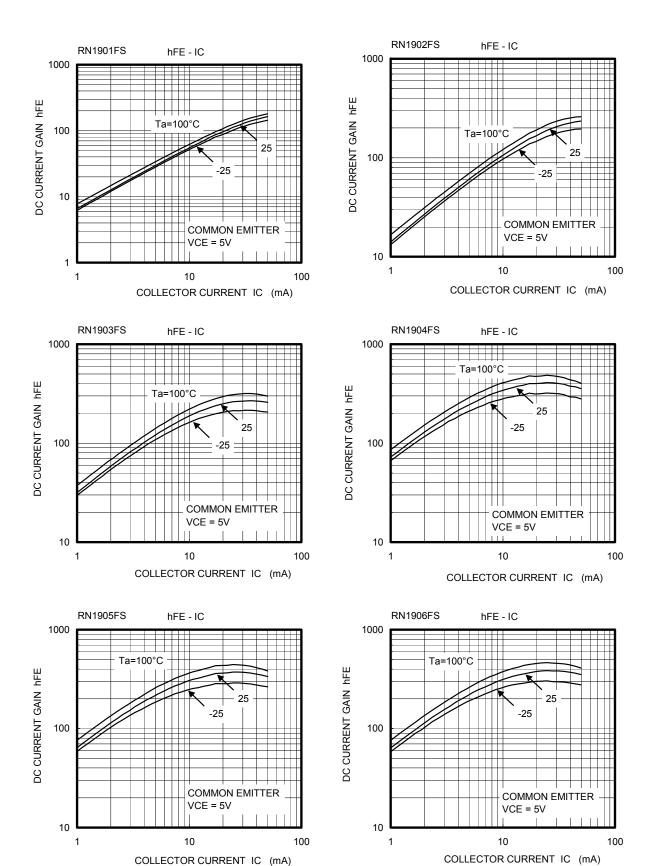




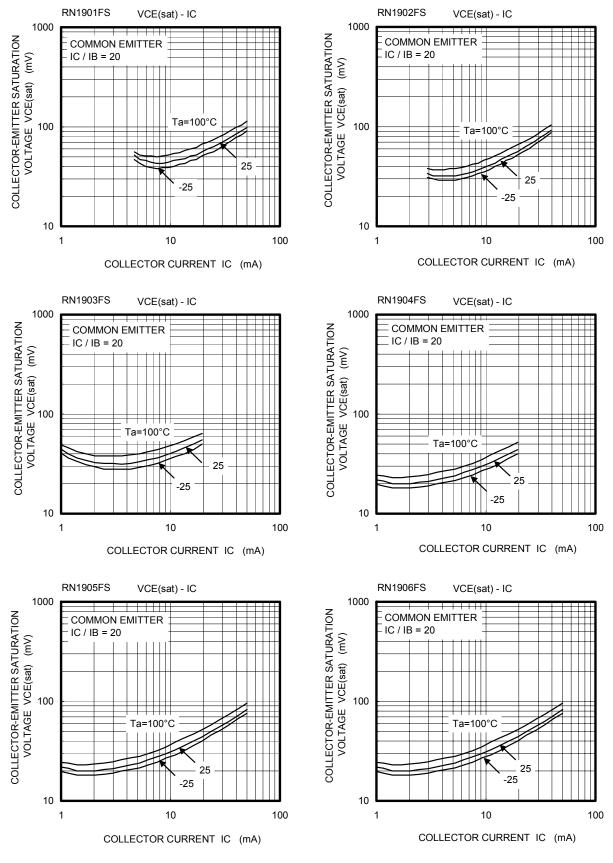


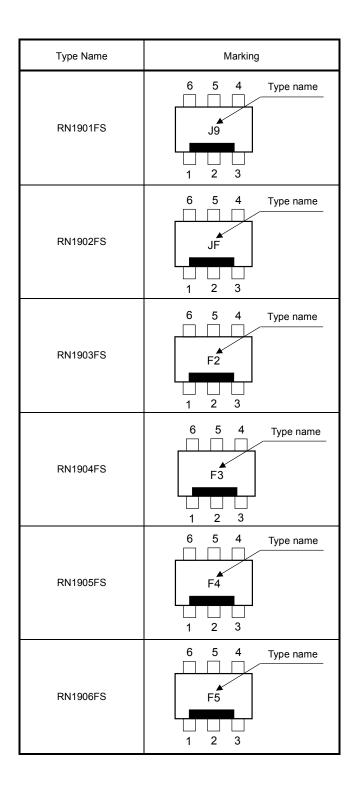


(Q1,Q2 Common)



(Q1, Q2 Common)





HANDLING PRECAUTION

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic discharge. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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