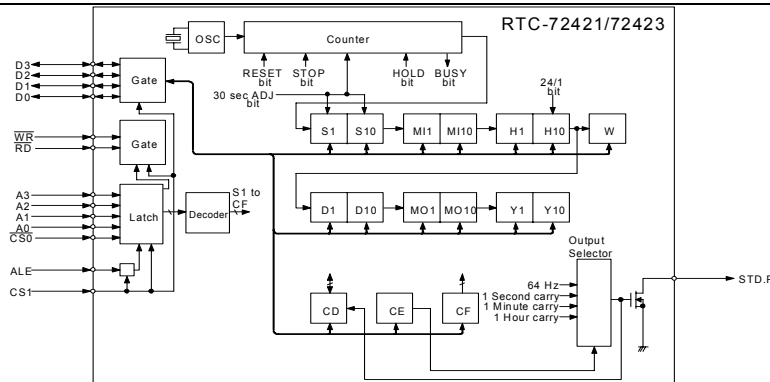
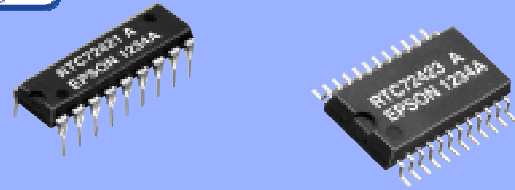
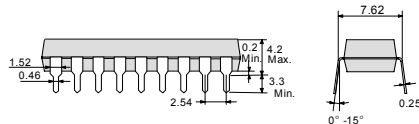


RTC - 72421 / RTC - 72423

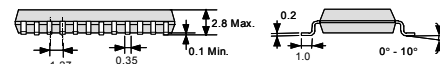


Top view of the RTC72421 A component. The component is rectangular with a width of 23.1 Max. and a height of 6.3. The top edge has pins #18 on the left and #10 on the right. The bottom edge has pins #1 on the left and #9 on the right. The component is labeled "RTC72421 A" and "EPSON 1234A". There are also two circular features on the left side.

No.	Pin terminal	No.	Pin terminal
1	STD.P	18	VDD
2	/CS0	17	(VDD)
3	ALE	16	(VDD)
4	A0	15	CS1
5	A1	14	C0
6	A2	13	D1
7	A3	12	D2
8	/RD	11	D3
9	GND	10	/WR



No.	Pin terminal	No.	Pin terminal
1	STD_P	24	VDD
2	/CS ₀	23	(VDD)
3	N.C.	22	(VDD)
4	ALE	21	N.C.
5	A ₀	20	CS ₁
6	N.C.	19	D ₀
7	A ₁	18	N.C.
8	N.C.	17	N.C.
9	A ₂	16	D ₁
10	A ₃	15	D ₂
11	/RD	14	D ₃
12	GND	13	/WR



Metal may be exposed on the top or bottom of this product. This will not affect any quality, reliability or electrical specifications.

Item	Symbol	Condition	Min.	Max.	Unit
Supply voltage	V _{DD}	T _a =+25 °C	-0.3	+7.0	V
Input voltage	V _{I/O}	T _a =+25 °C	GND-0.3	V _{DD} +0.3	
Storage temperature *	T _{STG}	RTC-72421	-55	+85	°C
		RTC-72423	-55	+125	

Item	Symbol	Condition	Min.	Max.	Unit
Power voltage	V _{DD}	—	4.5	5.5	V
Clock voltage	V _{CLK}	—	2.0	5.5	
Operating temperature	T _{OPR}	RTC-72421	-10	+70	°C
		RTC-72423	-40	+85	

Item	Symbol	Condition	Range	Unit
Frequency precision	$\Delta f/f$	$T_a = +25^\circ\text{C}$ $V_{DD} = 5.0\text{ V}$	72421A ± 10 72421B ± 50 72423A ± 20 72423 ± 50	$\times 10^{-6}$
Frequency temperature characteristics	TOP	-10°C to $+75^\circ\text{C}$ ($+25^\circ\text{C}$) -40°C to $+85^\circ\text{C}$ ($+25^\circ\text{C}$)	± 10 / -120 ± 10 / -220	
Frequency voltage characteristics	f/V	$T_a = +25^\circ\text{C}, V_{DD} = 2.0\text{ V}$ to 5.5 V	± 5.0 Max.	$\times 10^{-6}/\text{V}$
Aging	fa	$T_a = +25^\circ\text{C}, V_{DD} = 5.0\text{ V}$, First year	± 5.0 Max.	$\times 10^{-6}/\text{year}$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Applicable terminal
Current consumption	I _{DD1}	CS ₁ = 0 V	—	1	10	μA	—
	I _{DD2}	Exclude input/output current V _{DD} =2 V		0.9	5		—
HIGH input voltage (1)	V _{IH1}	—	2.2	—	—	V	All inputs other than CS ₁
LOW input voltage (1)	V _{IL1}		—		0.8	V	Do to D ₃
LOW output voltage (1)	V _{OL1}	I _{OL} =2.5 mA	—		0.4		
HIGH output voltage	V _{OH}	I _{OH} =-400 μA	2.4		—		
LOW output voltage (2)	V _{OL2}	I _{OL} =2.5 mA	—	10	0.4	μA	STD.P
OFF leak current	I _{OFFLK}	V _I =V _{DD} /0 V			10/-10		
Input capacity	C _i	Input frequency 1 MHz	—	10	—	pF	Input other than Do to D ₃
				20			Do to D ₃ , STD.P
HIGH input voltage (2)	V _{IH2}	V _{DD} =2.0 V to 5.5 V	4/5 V _{DD}	—	1/5 V _{DD}	V	CS ₁
LOW input voltage (2)	V _{IL2}		—				
Input leak current (1)	I _{LK1}	V _I =V _{DD} /0 V	—	—	1/-1	μA	Input other than Do to D ₃
Input leak current (2)	I _{LK2}				10/-10		Do to D ₃