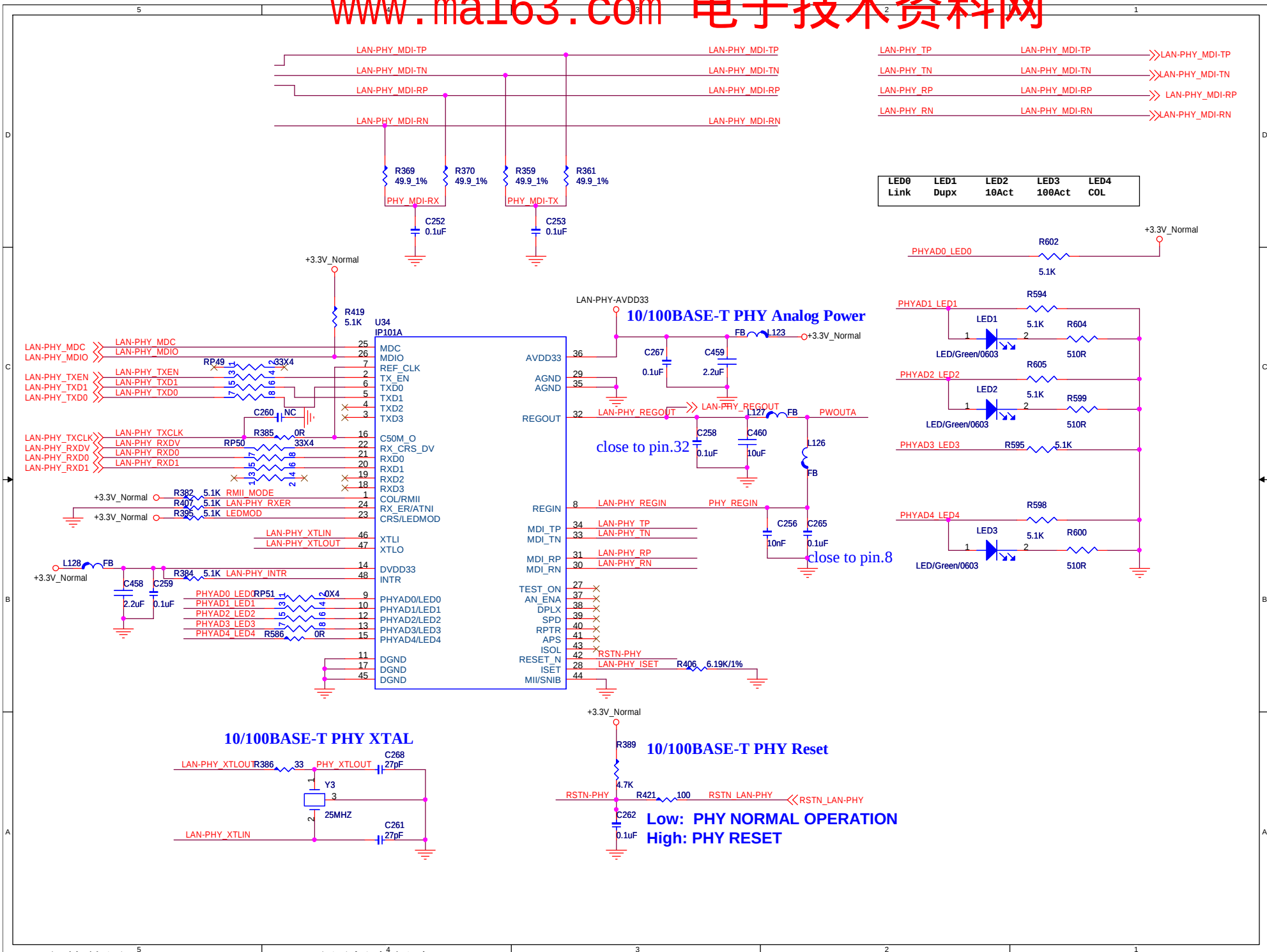
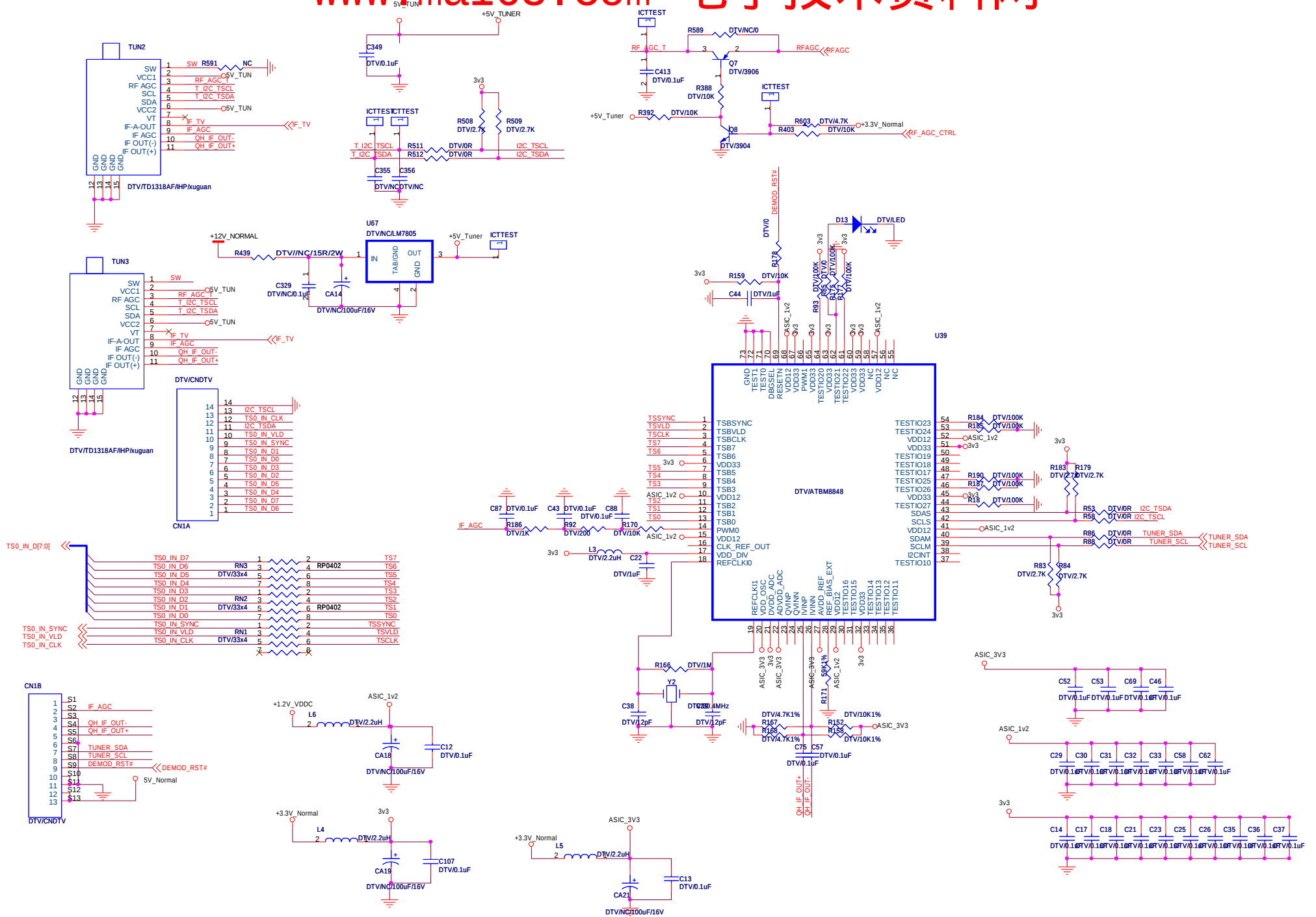
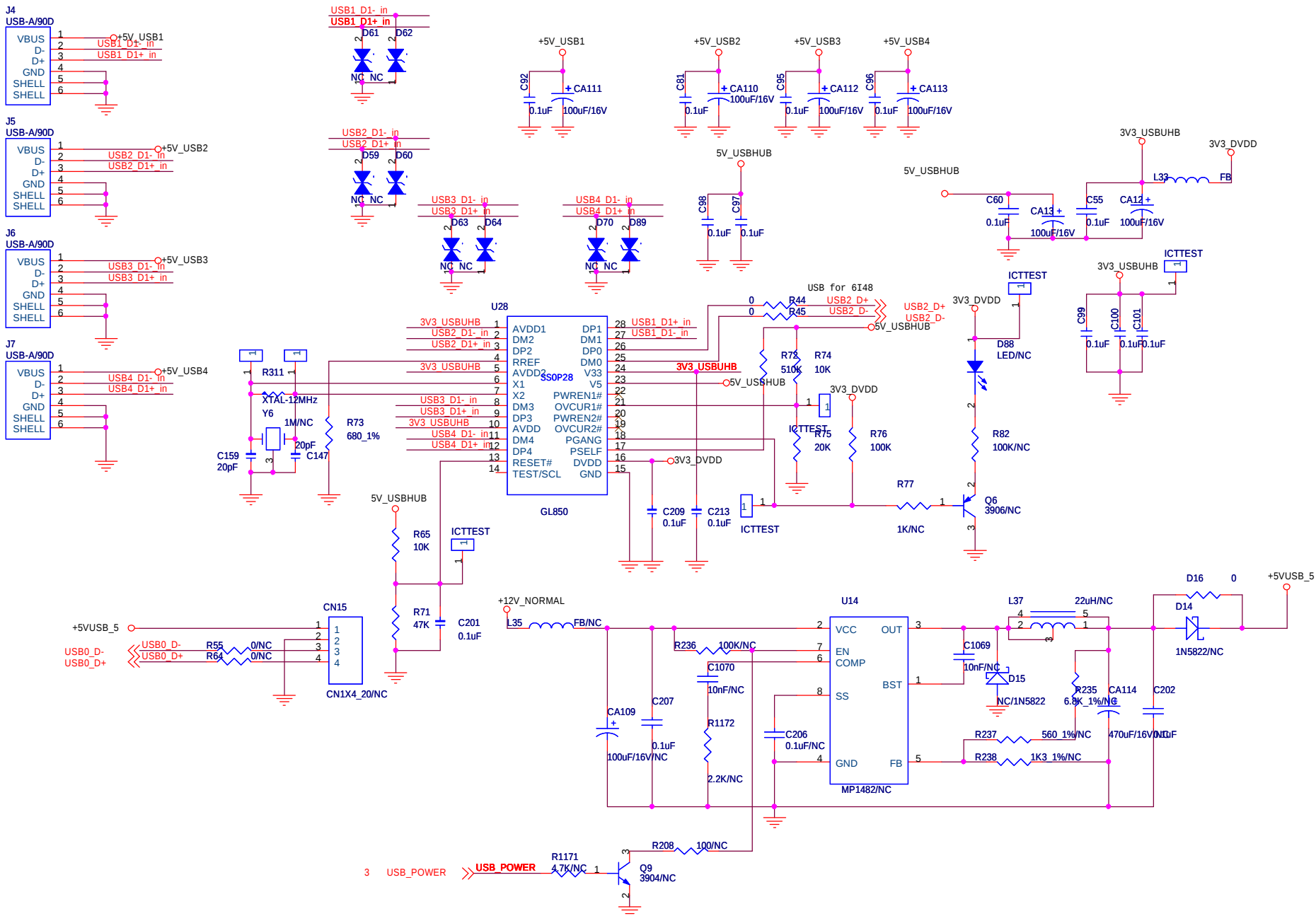
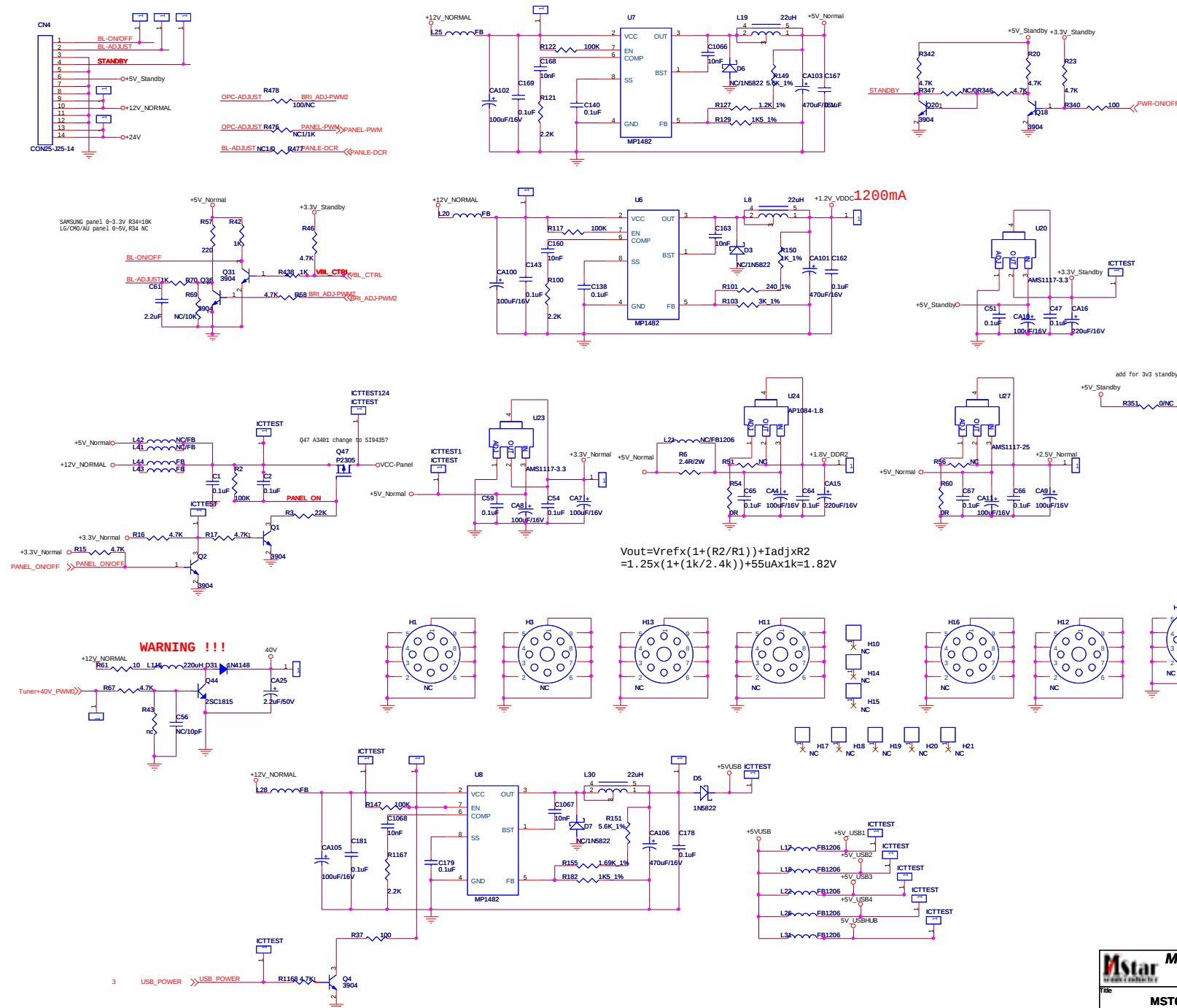
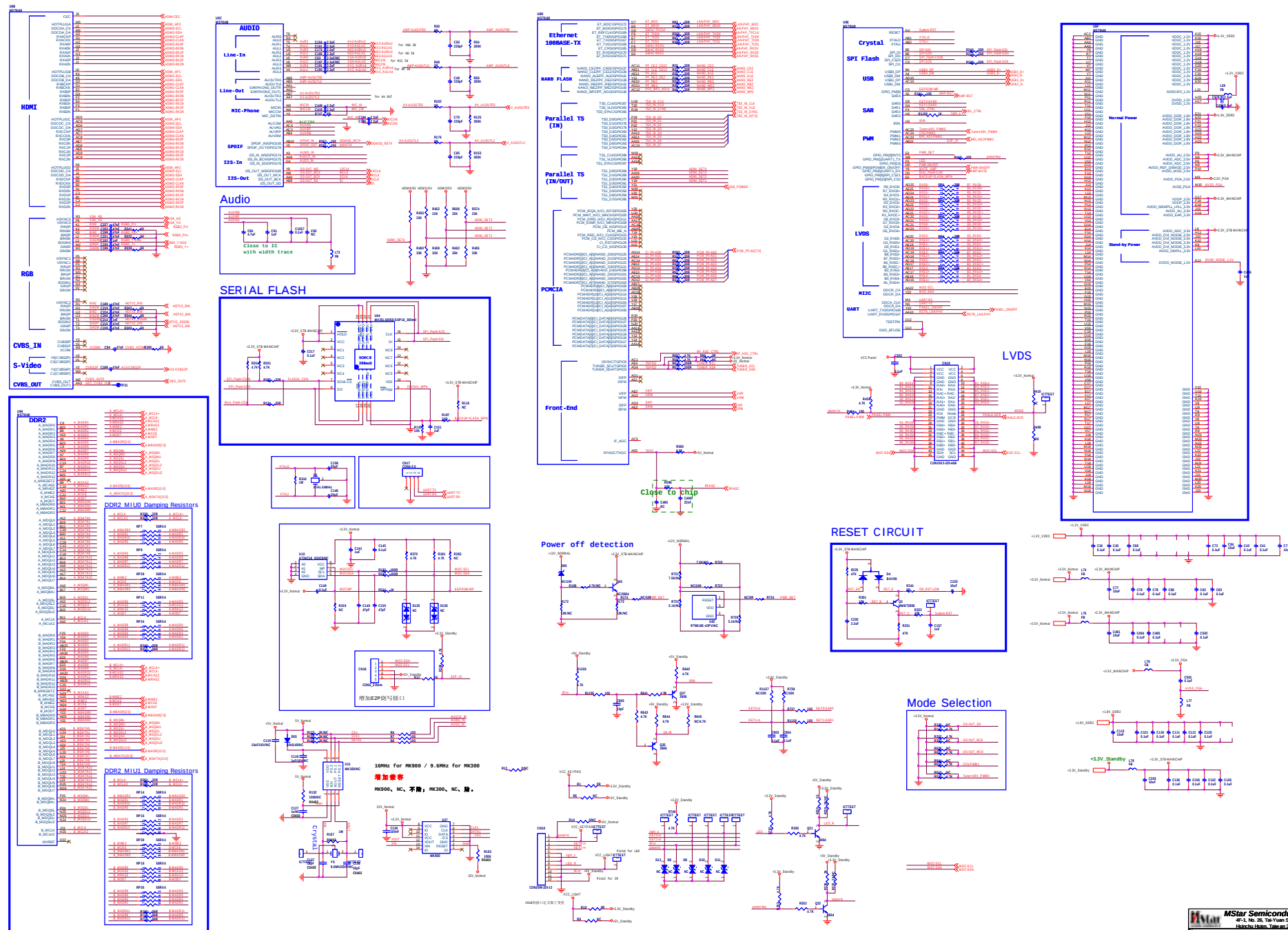




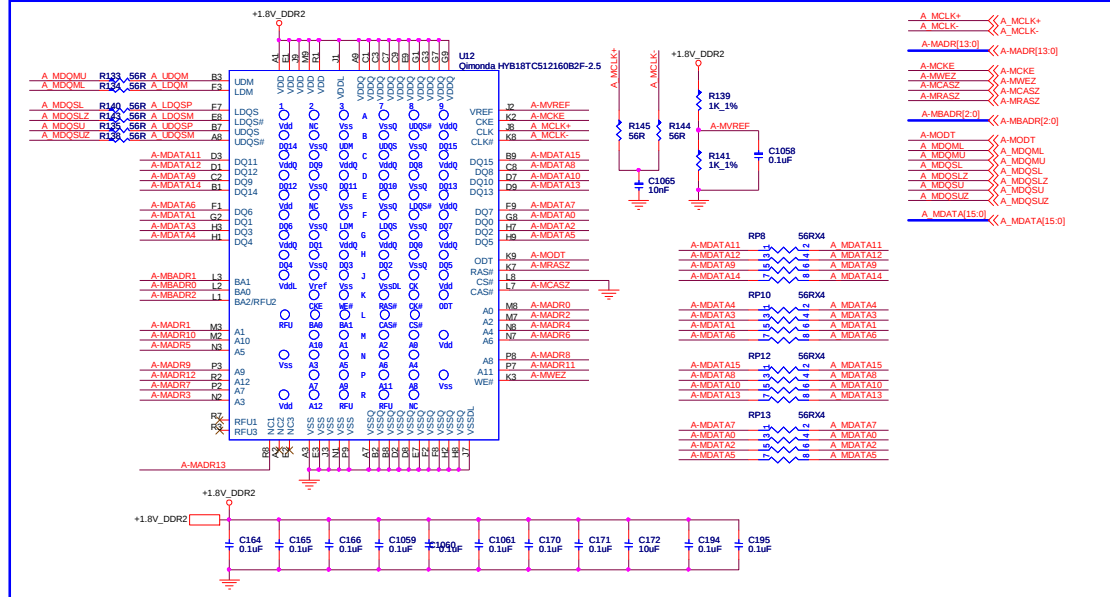




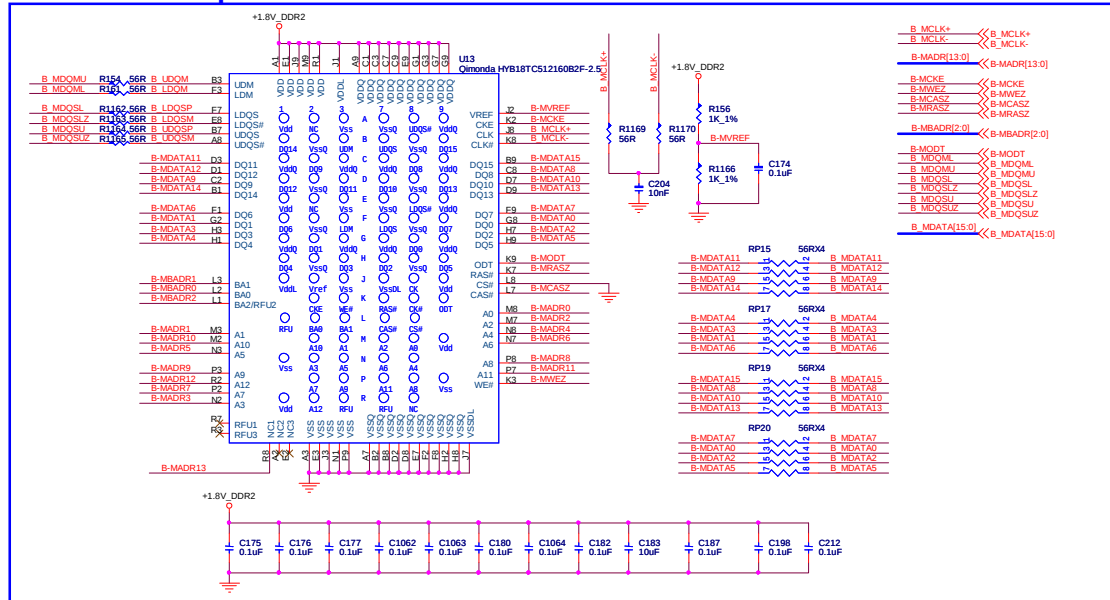


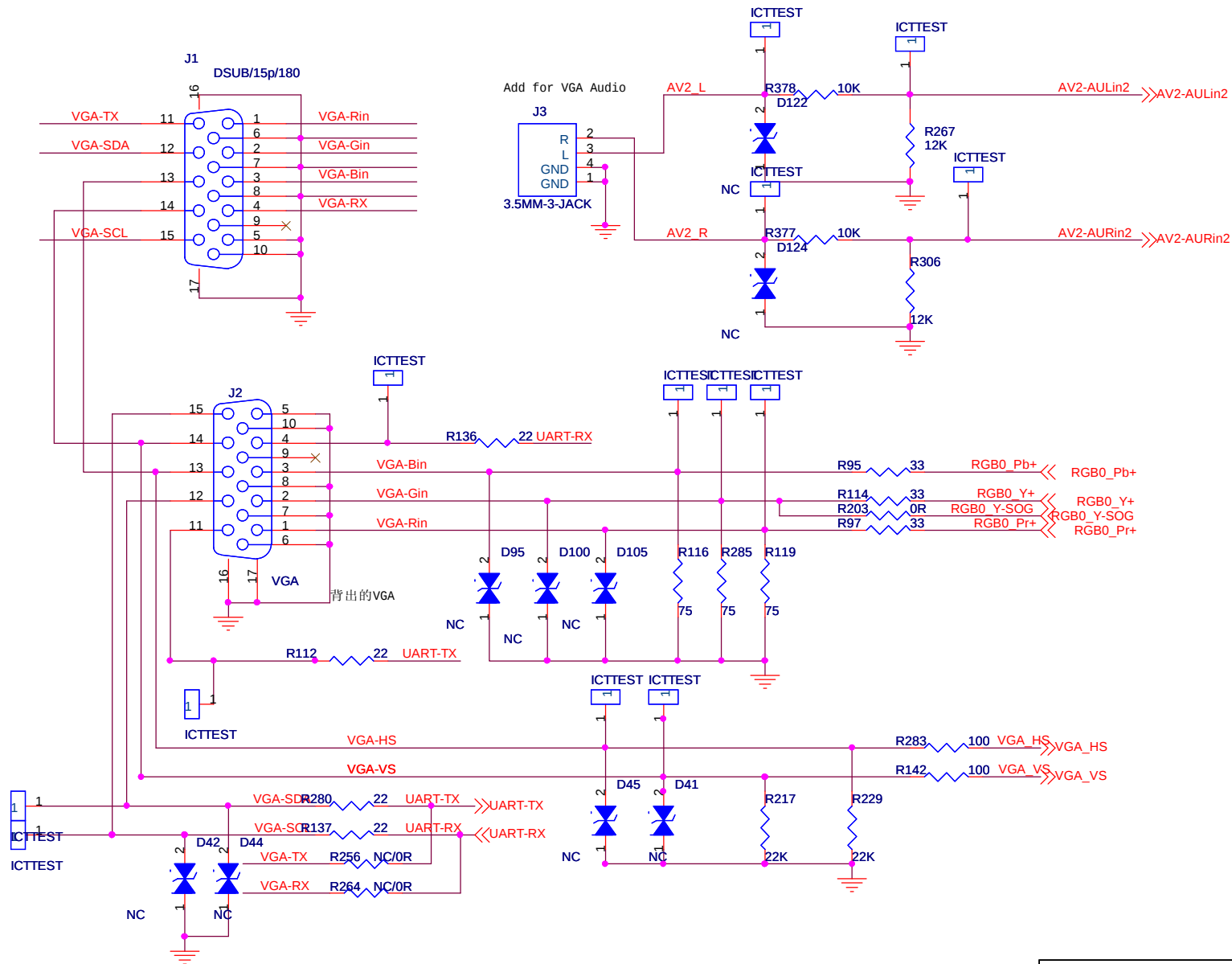


1st DDR2 Chip

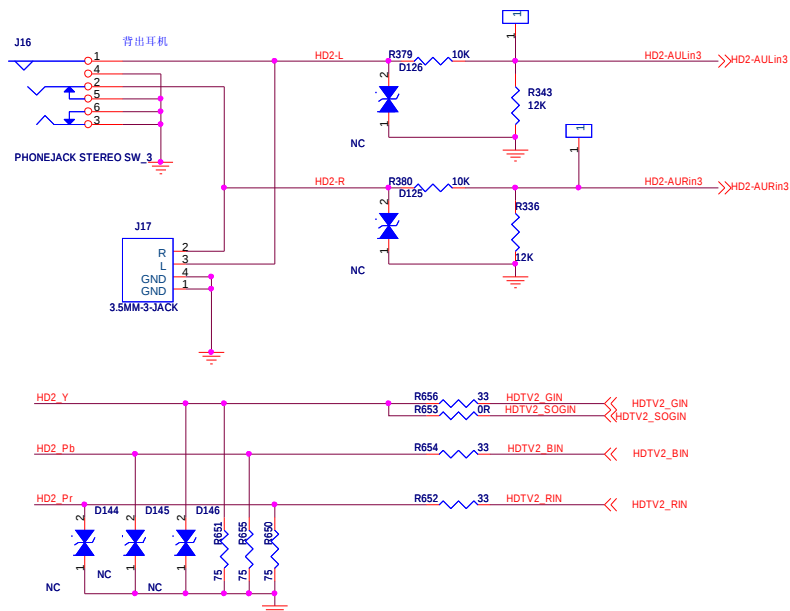
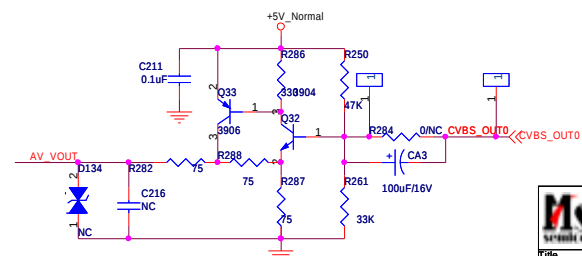
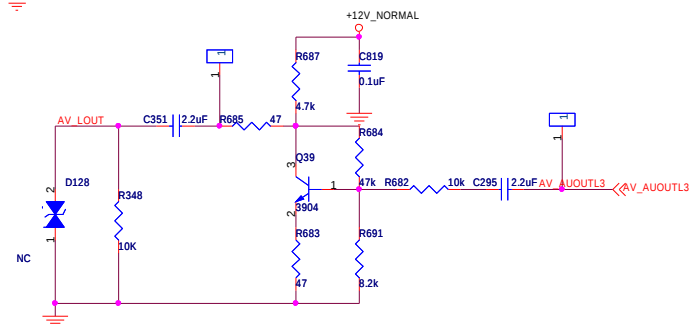
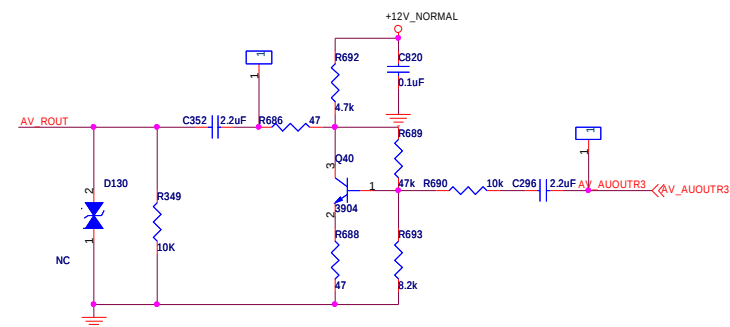
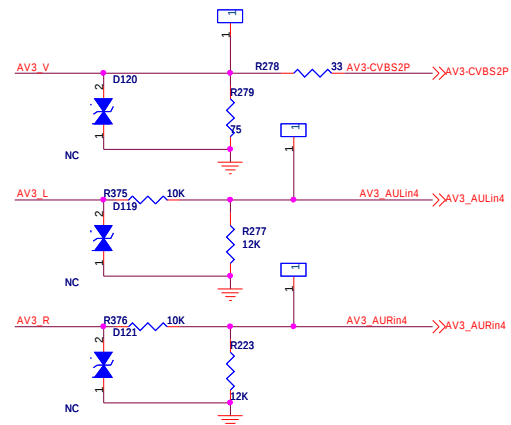
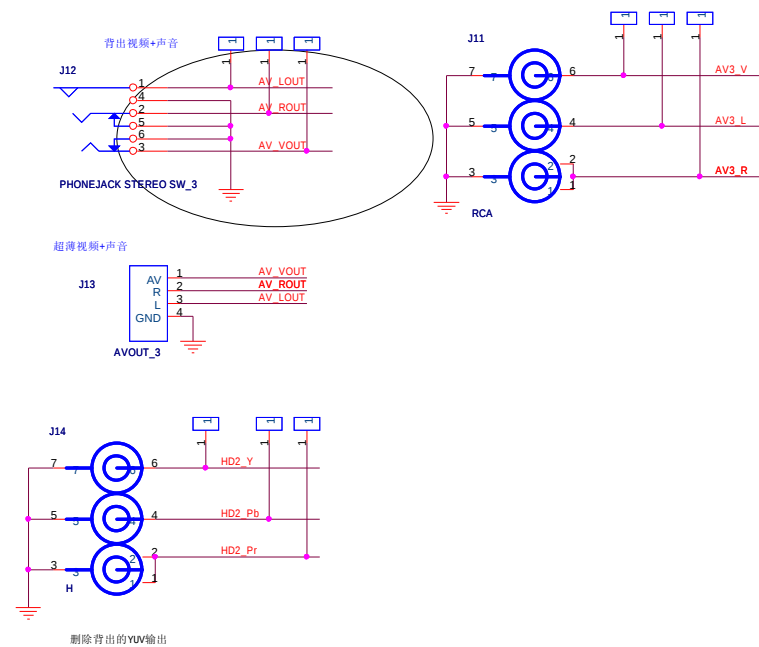


2nd DDR2 Chip





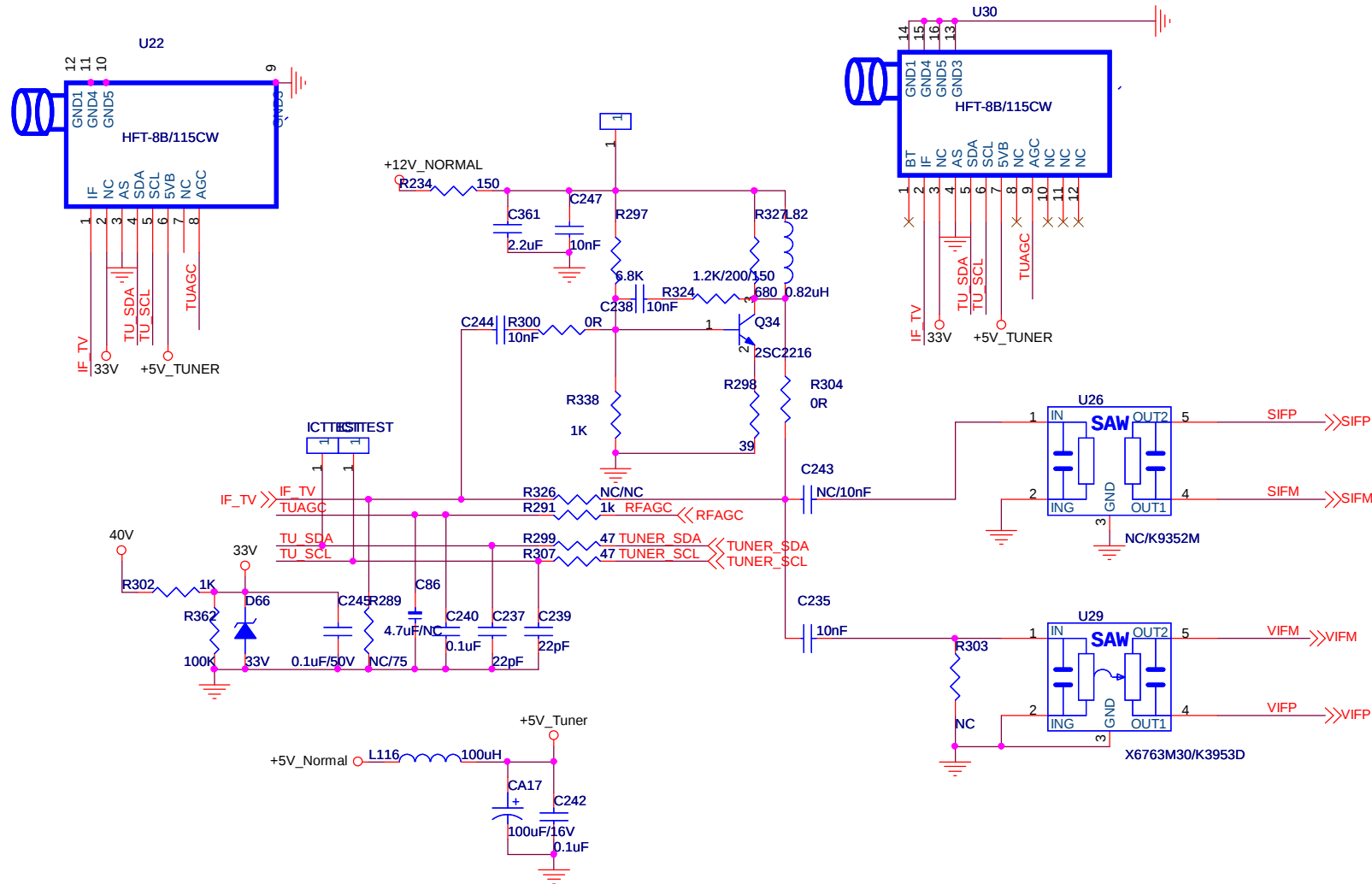
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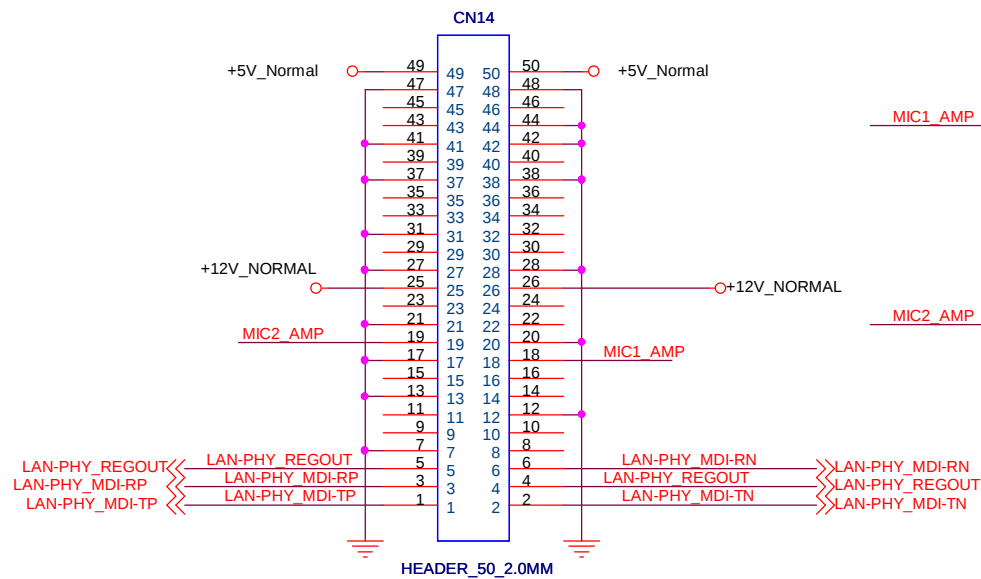

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 4F-1, No. 26, Tai-Yuan St., Chupei,
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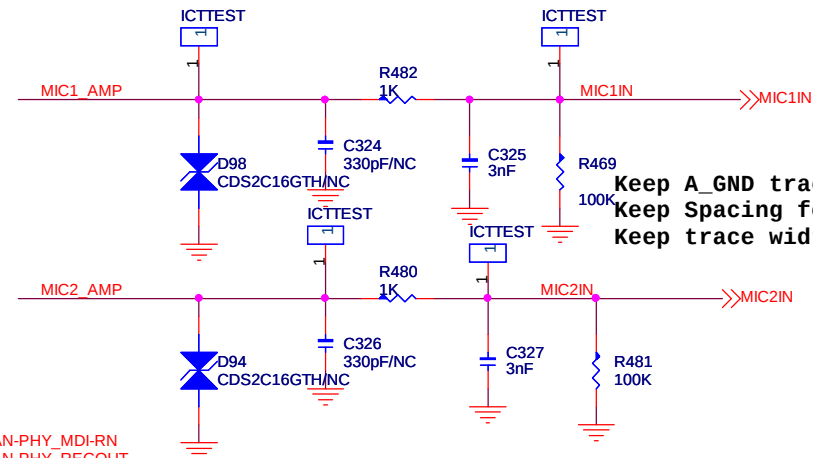
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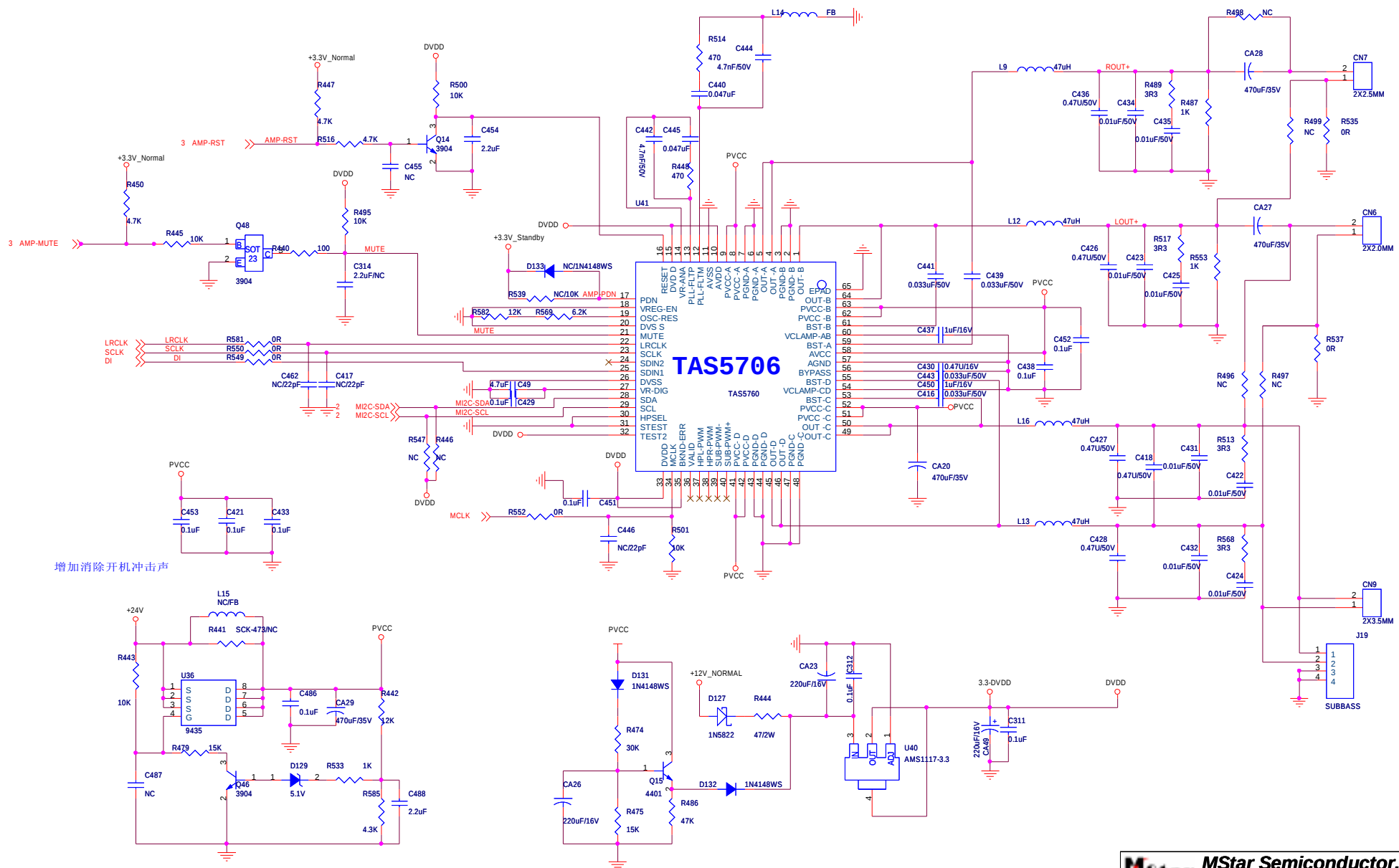


Close to connector

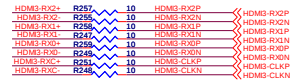
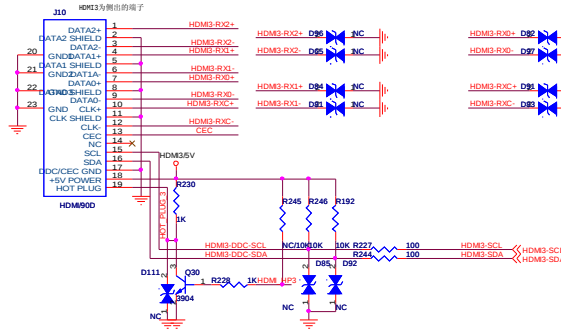
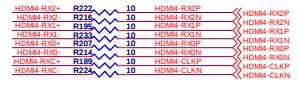
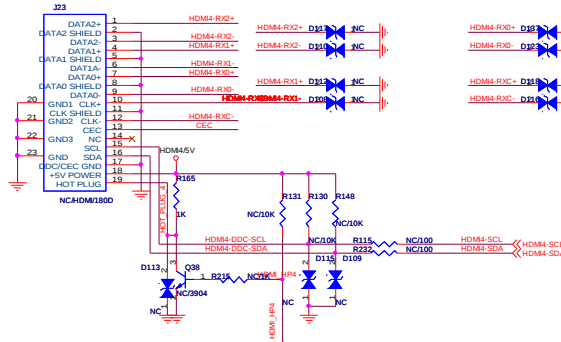
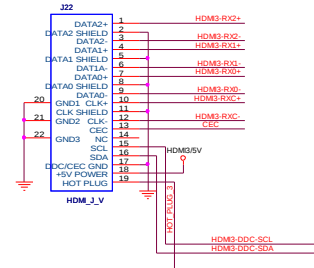
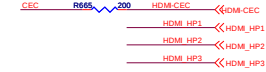
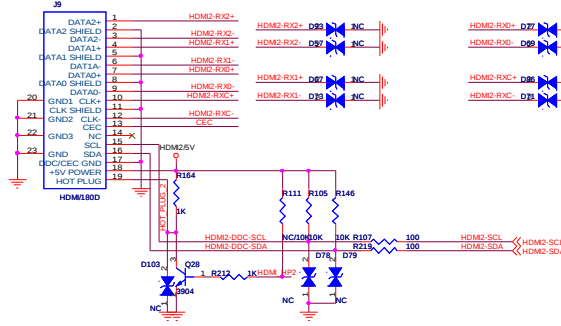
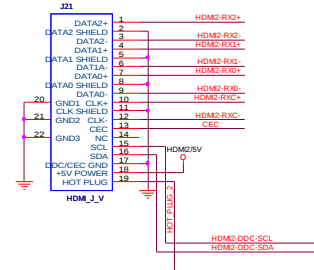
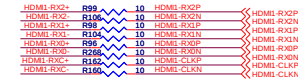
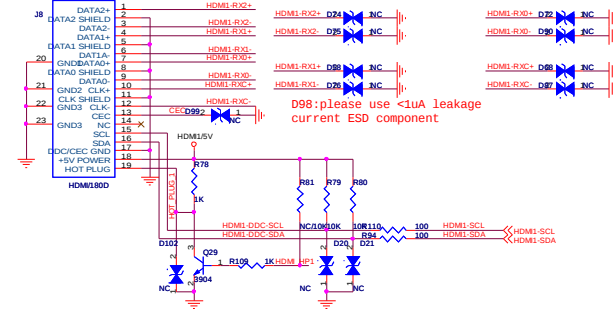
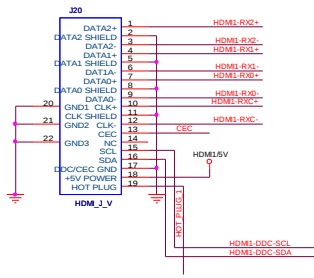


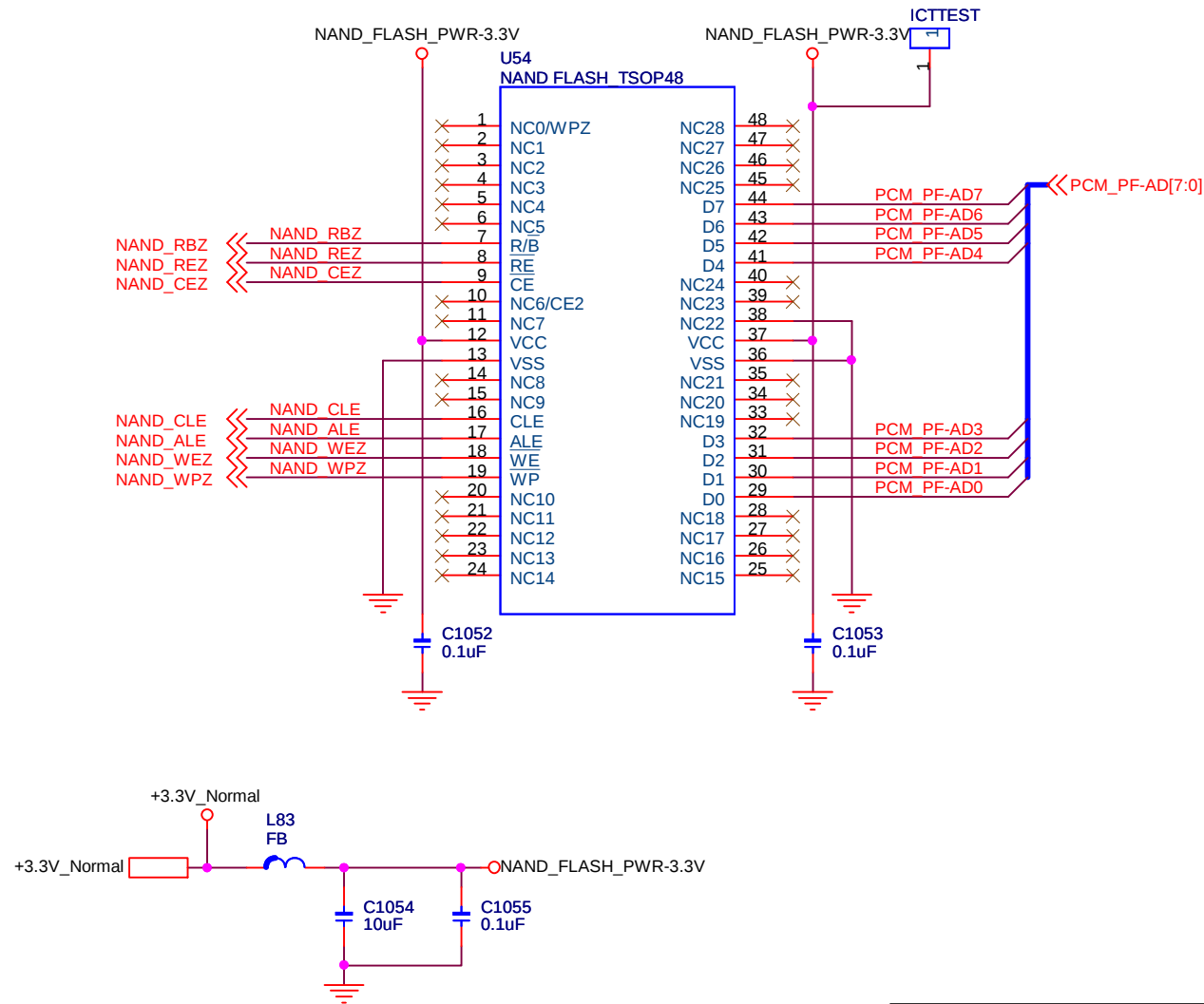
Keep A_GND trace with Signal.
Keep Spacing for L/R channel.
Keep trace width(12mil+).

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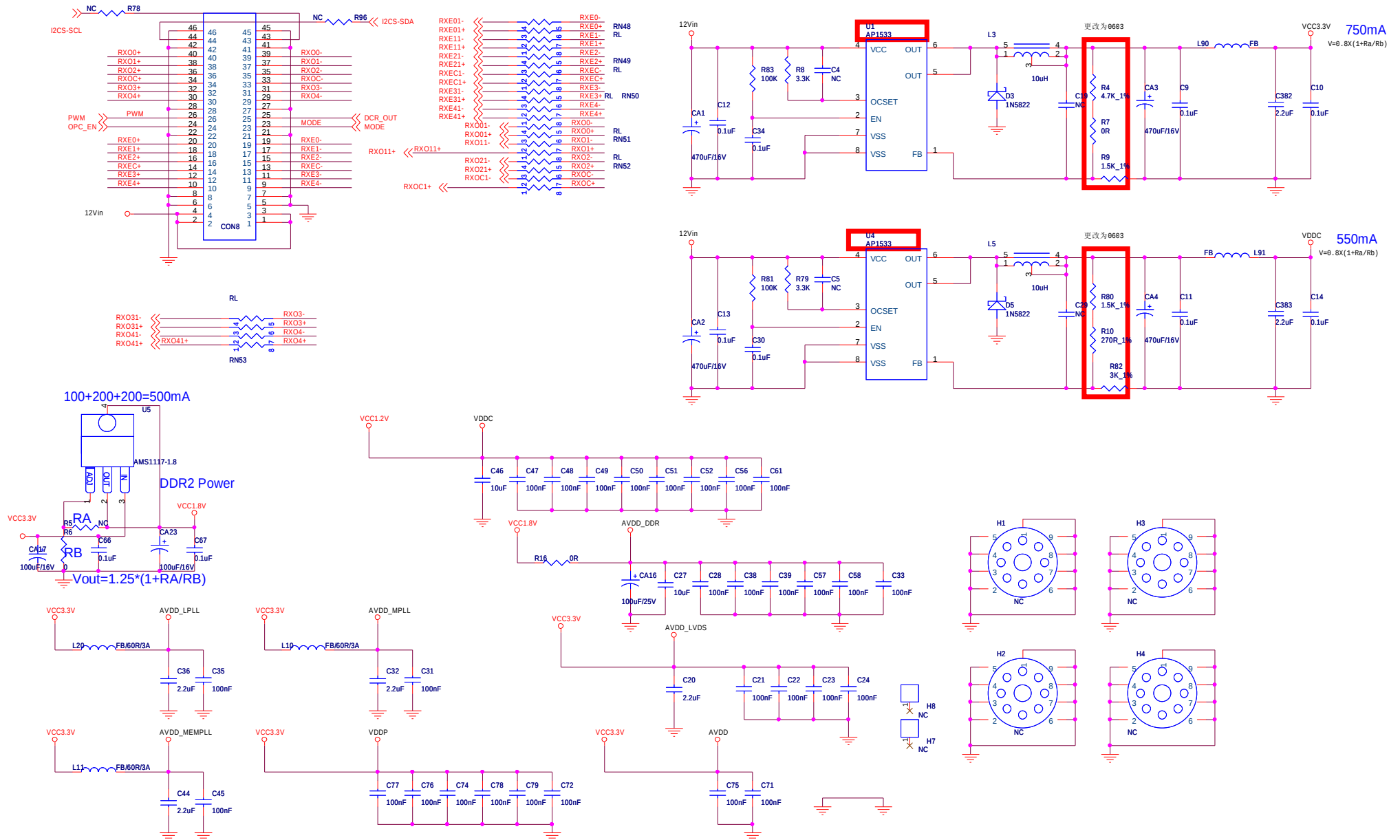


增加3路背出的HDMI

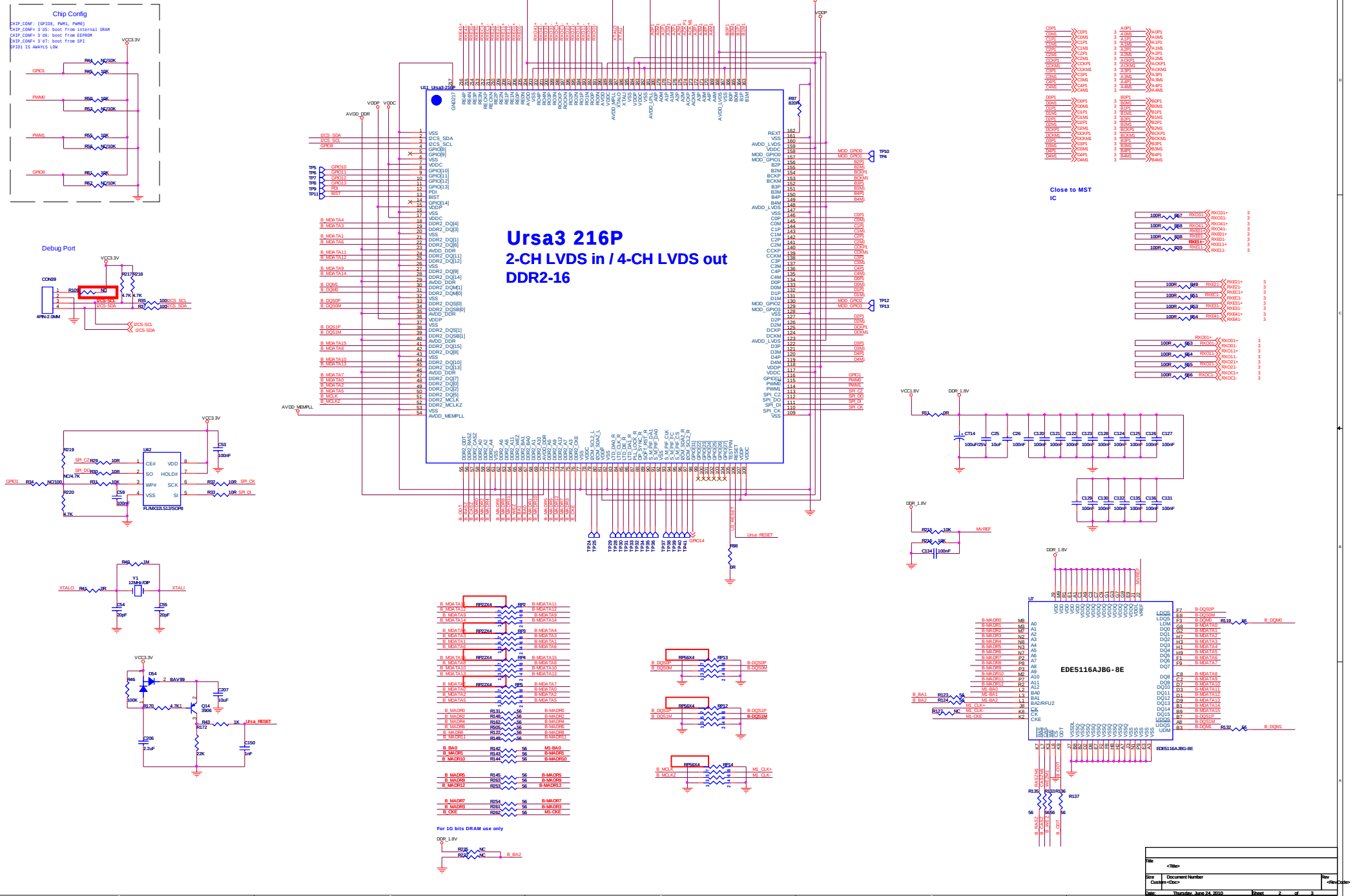




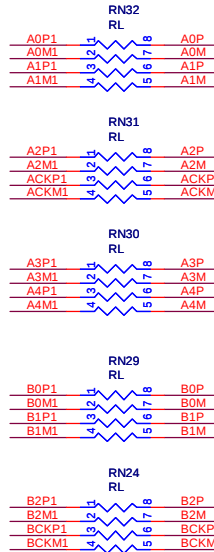
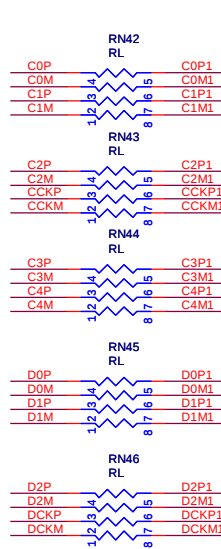
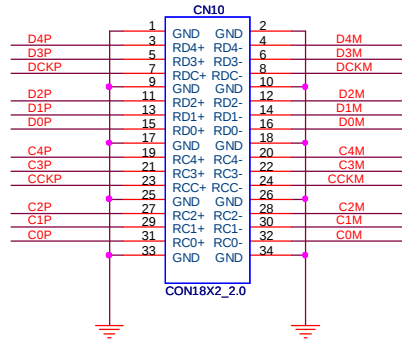
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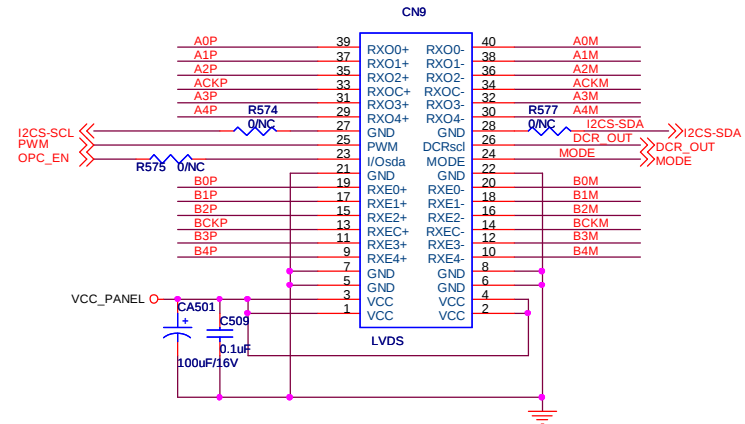
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LVDS OUT1

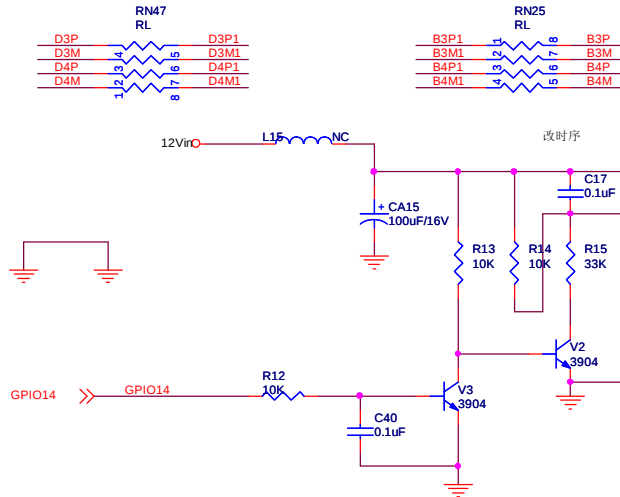


LVDS OUT2



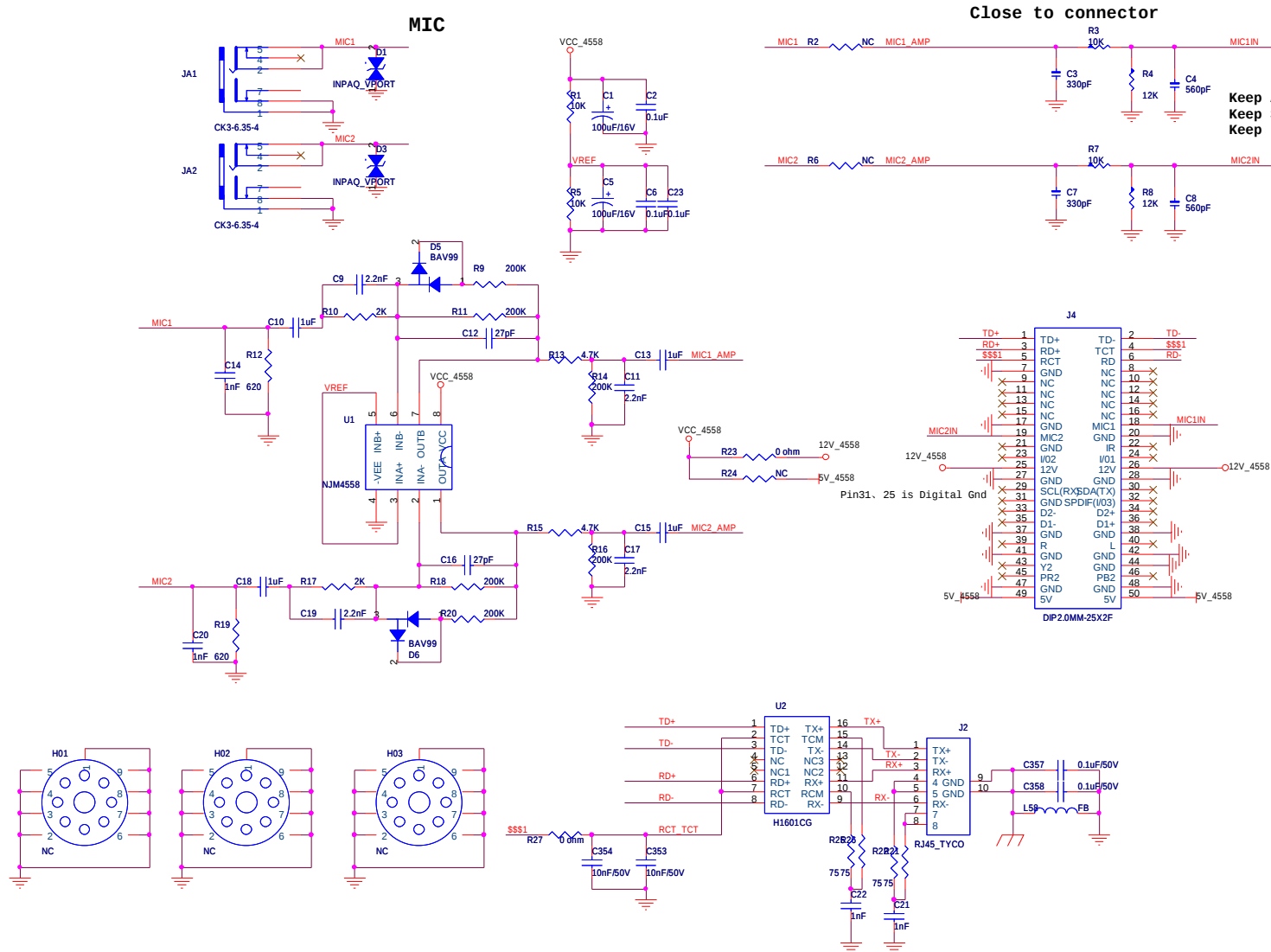
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LVDS_SEL
L: VESA

H: ENABLE
DCR_ENABLE
L: DISABLE

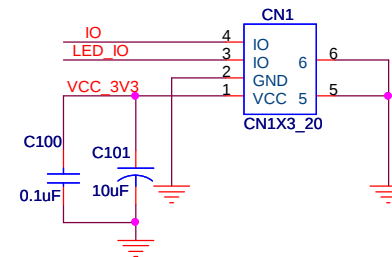
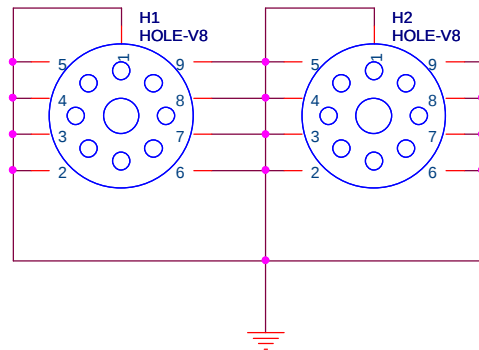
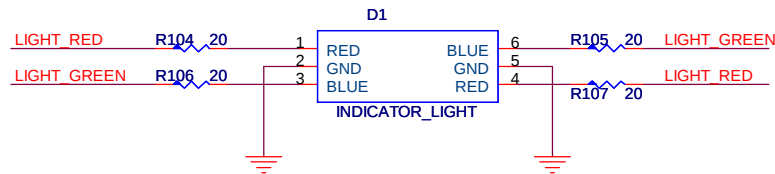
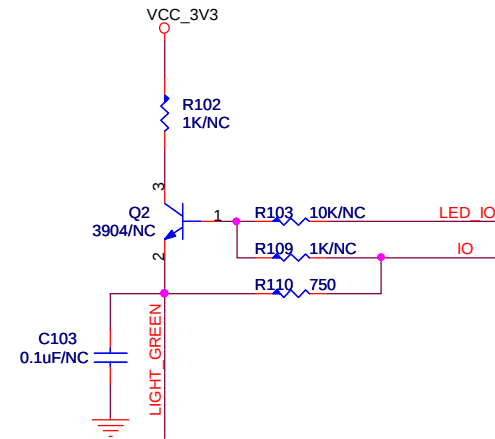
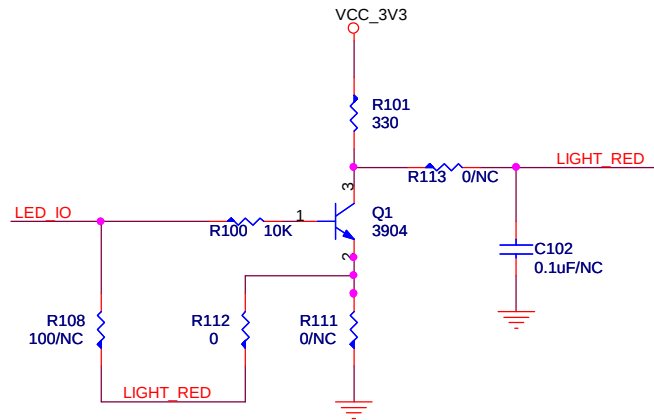


LCD Power

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Size Custom	Document Number <Doc>		Rev <RevCode>
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