Superior high Flux for High Voltage System

# Acrich MJT- 5630 Series SAW9KG0B (Warm, CRI90)











### **Product Brief**

### **Description**

- This White Colored surface-mount LED comes in standard package dimension.
   Package Size: 5.6x3.0x0.75mm
- It has a substrate made up of a molded plastic reflector sitting on top of a lead frame.
- The die is attached within the reflector cavity and the cavity is encapsulated by silicone.
- The package design coupled with careful selection of component materials allow these products to perform with high reliability.

#### **Features and Benefits**

- Super high Flux output and Luminance
- Designed for high voltage operation
- SMT solderable
- High Color Quality with CRI Min. 90 CRI R9>50
- Lead free product
- RoHS compliant

### **Key Applications**

- Interior lighting
- General lighting
- Indoor and Outdoor displays
- · Architectural and Decorative lighting
- Traffic signals

**Table 1. Product Selection Table** 

David Marrish and	сст					
Part Number	Color	Min.	Тур.	Max.		
SAW9KG0B	Warm White	2600K	3000K	3700K		



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### **Performance Characteristics**

Table 2. Product Selection Guide, I<sub>F</sub>=20mA , T<sub>a</sub>=25°C, RH30%

Part Number	CCT (K) [1]	RANK	Luminous Intensity <sup>[2]</sup> RANK l <sub>v</sub> (cd)		Luminous Flux $^{[3]}$ $\Phi_{_{V}}$ (lm)		CRI R <sub>a</sub>
	Тур.		Min	Max	Min	Max	Min.
		W5	13.5	14.8	40.5	44.4	90
	3500	X8	14.8	16.1	44.4	48.3	90
	3500	Y1	16.1	17.4	48.3	52.2	90
		Z4	17.4	19.0	52.2	57.0	90
		W5	13.5	14.8	40.5	44.4	90
SAW9KG0B	3000	X8	14.8	16.1	44.4	48.3	90
SAWARGOB	3000	Y1	16.1	17.4	48.3	52.2	90
		Z4	17.4	19.0	52.2	57.0	90
		W5	13.5	14.8	40.5	44.4	90
	2700	X8	14.8	16.1	44.4	48.3	90
	2100	Y1	16.1	17.4	48.3	52.2	90
		Z4	17.4	19.0	52.2	57.0	90

#### Notes:

(1) Correlated Color Temperature is derived from the CIE 1931 Chromaticity diagram.

Color coordinate :  $\pm 0.007$ 

(2) Seoul Semiconductor maintains a tolerance of  $\pm 7\%$  on Intensity and power measurements.

The luminous intensity IV was measured at the peak of the spatial pattern which may not be aligned with the mechanical axis of the LED package.

(3) The lumen table is only for reference.

### **Performance Characteristics**

Table 3. Characteristics, I<sub>F</sub>=20mA , T<sub>a</sub>=25°C, RH30%

Parameter	Cumbal		Value		Unit
Farameter	Symbol	Min.	Тур.	Max.	Unit
Forward Current	I <sub>F</sub>		20	25	mA
Forward Voltage	V <sub>F</sub> (20mA)	20.7	22	23	V
Reverse Voltage	V <sub>R</sub> (I <sub>R</sub> =10mA)	0.7	-	-	V
Luminous Intensity (2,600~3,700 K)	I <sub>v</sub> (20mA)	-	15.0 (45.0)*	-	cd (lm)
Color Rendering Index [1]	R <sub>a</sub>	90	92	-	-
Viewing Angle [2]	2Θ <sub>1/2</sub>	-	115	-	deg.
Power Dissipation	P <sub>D</sub>	-	-	0.58	w
Junction Temperature	T <sub>j</sub>	-	-	125	°C
Operating Temperature	$T_{opr}$	- 30	-	+ 85	°C
Storage Temperature	$T_{stg}$	- 40	-	+ 100	°C
Thermal resistance (J to S) [3]	Rθ <sub>j-s</sub>	-	27	-	°C/W
ESD Sensitivity(HBM)	-	5	-	-	KV

#### Notes:

- (1) Tolerance is ±2.0 on CRI measurements.
- (2)  $2\Theta_{1/2}$  is the off-axis where the luminous intensity is 1/2 of the peak intensity
- (3) Thermal resistance: Rth<sub>JS</sub> (Junction to Solder)
- LED's properties might be different from suggested values like above and below tables if
  operation condition will be exceeded our parameter range. Care is to be taken that power
  dissipation does not exceed the absolute maximum rating of the product.
- All measurements were made under the standardized environment of Seoul Semiconductor.

<sup>\*</sup> The lumen value is only for reference.

# **Relative Spectral Distribution**

Fig 1. Color Spectrum, T<sub>a</sub>=25℃, RH30%

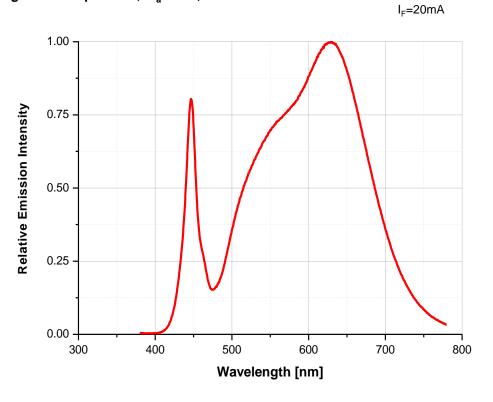
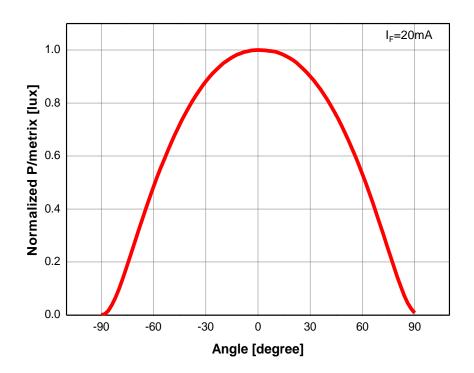


Fig 2. Viewing Angle Distribution



### **Forward Current Characteristics**

Fig 3. Forward Voltage vs. Forward Current ,  $T_a=25\,^{\circ}{\rm C}$ 

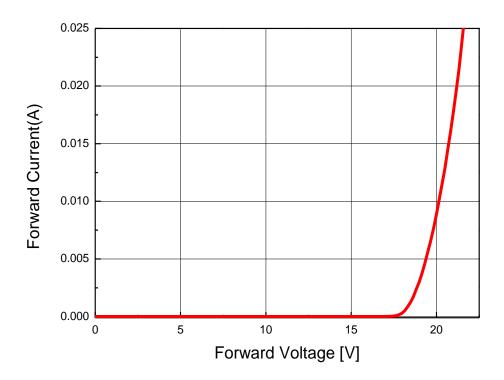
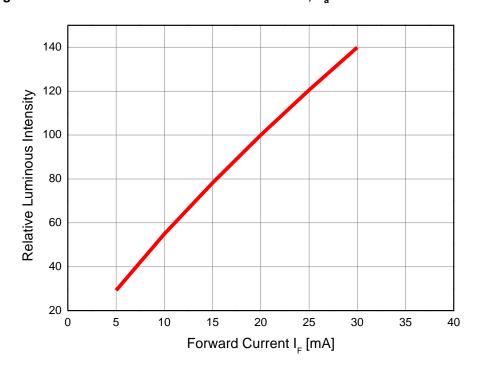
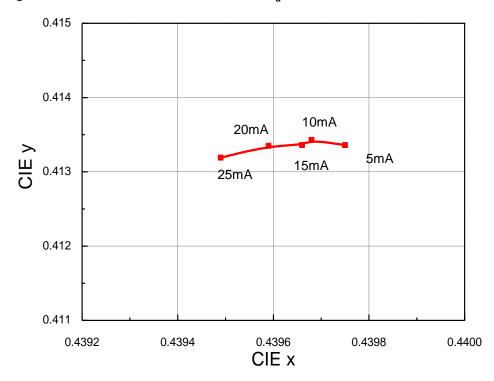


Fig 4. Forward Current vs. Relative Luminous Flux, T<sub>a</sub>=25 ℃



### **Forward Current Characteristics**



# **Junction Temperature Characteristics**

Fig 6. Relative Light Output vs. Junction Temperature

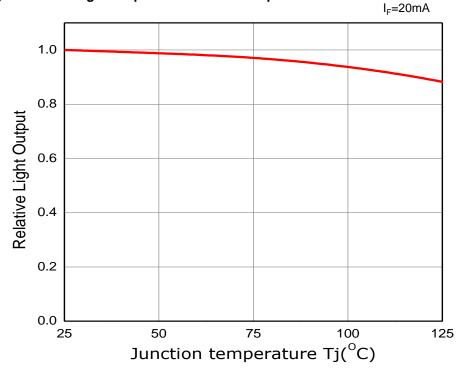
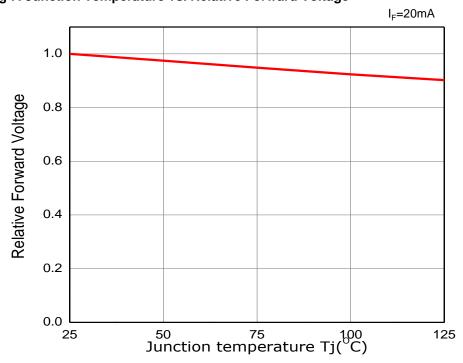
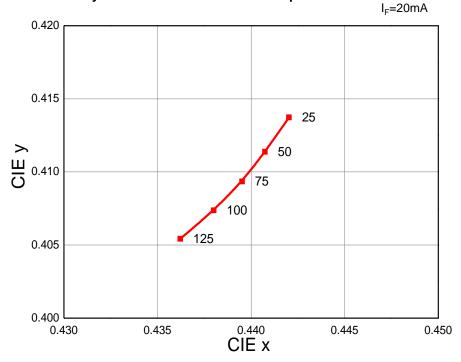


Fig 7. Junction Temperature vs. Relative Forward Voltage



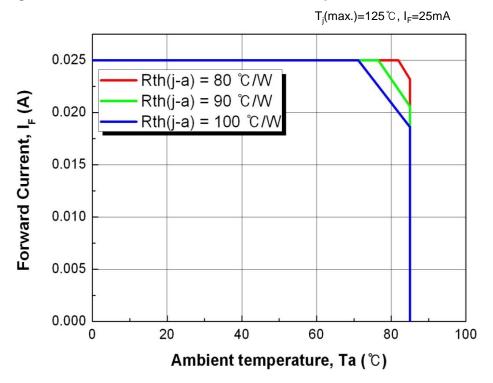
# **Junction Temperature Characteristics**

Fig 8. Chromaticity Coordinate vs. Junction Temperature



### **Ambient Temperature Characteristics**

Fig 9. Maximum Forward Current vs. Ambient Temperature



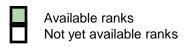
## **Color Bin Structure**

Table 4. Bin Code description

Part Number	Port Number Intensity I <sub>V</sub> (cd) @ I <sub>E</sub> =20mA Chro		Color Chromaticity Coordinate	Typical Forward Voltage (V₁) @ I <sub>F</sub> =20mA			
	Bin Code	Min.	Max.	@ I <sub>F</sub> =20mA	Bin Code	Min.	Max.
	W5	13.5	14.8	Refer to page.14	Α	20.7	21.5
CANNOKCOD	X8	14.8	16.1		В	21.5	22.2
SAW9KG0B	Y1	16.1	17.4		С	22.2	23.0
	Z4	17.4	19.0				

Table 5. Intensity rank distribution

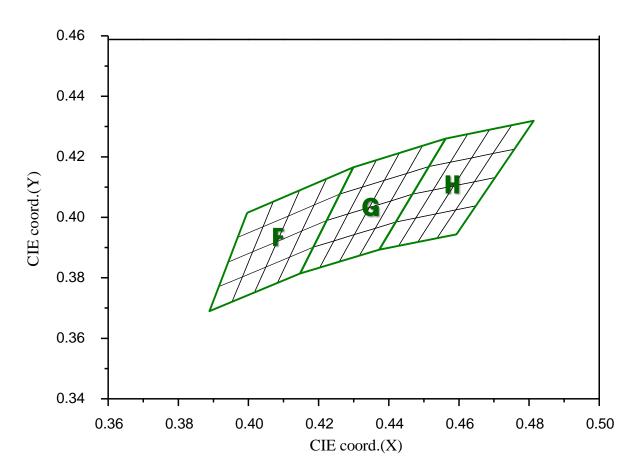
сст	CIE	IV Rank			
3200 ~ 3700K	F	W5	X8	Y1	Z4
2900 ~ 3200K	G	W5	X8	Y1	Z4
2600 ~ 2900K	Н	W5	X8	Y1	Z4



### Notes:

- (1) All measurements were made under the standardized environment of Seoul Semiconductor.
- (2) Seoul Semiconductor sorts the LED package according to the luminous intensity IV.

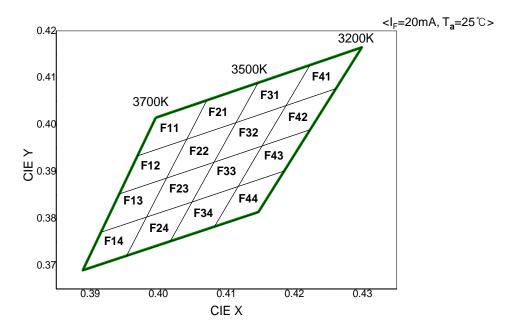
Fig 10. CIE Chromaticity Diagram  $T_a=25 \, ^{\circ}C$ ,  $I_F=20 \, \text{mA}$ 



<sup>(1)</sup> Energy Star binning applied to all 2600~3700K.

<sup>(2)</sup> Measurement Uncertainty of the Color Coordinates :  $\pm$  0.007

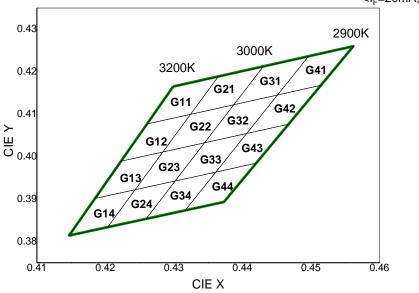




F1	11	F:	21	F3	31	F-	<b>41</b>
CIE X	CIE Y						
0.3996	0.4015	0.4071	0.4052	0.4146	0.4089	0.4223	0.4127
0.3969	0.3934	0.4042	0.3969	0.4114	0.4005	0.4187	0.4041
0.4042	0.3969	0.4114	0.4005	0.4187	0.4041	0.4261	0.4077
0.4071	0.4052	0.4146	0.4089	0.4223	0.4127	0.4299	0.4165
F1	12	F	22	F3	32	F4	<b>42</b>
CIE X	CIE Y						
0.3969	0.3934	0.4042	0.3969	0.4114	0.4005	0.4187	0.4041
0.3943	0.3853	0.4012	0.3886	0.4082	0.3920	0.4152	0.3955
0.4012	0.3886	0.4082	0.3920	0.4152	0.3955	0.4223	0.3990
0.4042	0.3969	0.4114	0.4005	0.4187	0.4041	0.4261	0.4077
F1	13	F	23	F3	33	F4	43
CIE X	CIE Y						
CIE X	CIE Y						
CIE X 0.3943	CIE Y 0.3853	CIE X 0.4012	CIE Y 0.3886	CIE X 0.4082	CIE Y 0.3920	CIE X 0.4152	CIE Y 0.3955
CIE X 0.3943 0.3916	CIE Y 0.3853 0.3771	CIE X 0.4012 0.3983	CIE Y 0.3886 0.3803	CIE X 0.4082 0.4049	CIE Y 0.3920 0.3836	CIE X 0.4152 0.4117	CIE Y 0.3955 0.3869
CIE X 0.3943 0.3916 0.3983	CIE Y 0.3853 0.3771 0.3803 0.3886	CIE X 0.4012 0.3983 0.4049 0.4082	CIE Y 0.3886 0.3803 0.3836	CIE X 0.4082 0.4049 0.4117	CIE Y 0.3920 0.3836 0.3869 0.3955	CIE X 0.4152 0.4117 0.4185	CIE Y 0.3955 0.3869 0.3902 0.3990
CIE X 0.3943 0.3916 0.3983 0.4012	CIE Y 0.3853 0.3771 0.3803 0.3886	CIE X 0.4012 0.3983 0.4049 0.4082	CIE Y 0.3886 0.3803 0.3836 0.3920	CIE X 0.4082 0.4049 0.4117 0.4152	CIE Y 0.3920 0.3836 0.3869 0.3955	CIE X 0.4152 0.4117 0.4185 0.4223	CIE Y 0.3955 0.3869 0.3902 0.3990
CIE X  0.3943  0.3916  0.3983  0.4012	CIE Y 0.3853 0.3771 0.3803 0.3886	CIE X 0.4012 0.3983 0.4049 0.4082	CIE Y 0.3886 0.3803 0.3836 0.3920	CIE X 0.4082 0.4049 0.4117 0.4152	CIE Y 0.3920 0.3836 0.3869 0.3955	CIE X 0.4152 0.4117 0.4185 0.4223	CIE Y 0.3955 0.3869 0.3902 0.3990
CIE X 0.3943 0.3916 0.3983 0.4012 F1	CIE Y 0.3853 0.3771 0.3803 0.3886 14 CIE Y	CIE X 0.4012 0.3983 0.4049 0.4082 F2 CIE X	CIE Y 0.3886 0.3803 0.3836 0.3920 24 CIE Y	CIE X 0.4082 0.4049 0.4117 0.4152  CIE X	CIE Y 0.3920 0.3836 0.3869 0.3955	CIE X 0.4152 0.4117 0.4185 0.4223 FA	CIE Y 0.3955 0.3869 0.3902 0.3990 44 CIE Y
CIE X 0.3943 0.3916 0.3983 0.4012 F1 CIE X 0.3916	CIE Y 0.3853 0.3771 0.3803 0.3886 14 CIE Y 0.3771	CIE X 0.4012 0.3983 0.4049 0.4082 F2 CIE X 0.3983	CIE Y 0.3886 0.3803 0.3836 0.3920 24 CIE Y 0.3803	CIE X 0.4082 0.4049 0.4117 0.4152 F3 CIE X 0.4049	CIE Y 0.3920 0.3836 0.3869 0.3955 34 CIE Y 0.3836	CIE X 0.4152 0.4117 0.4185 0.4223 F2 CIE X 0.4117	CIE Y 0.3955 0.3869 0.3902 0.3990 44 CIE Y 0.3869



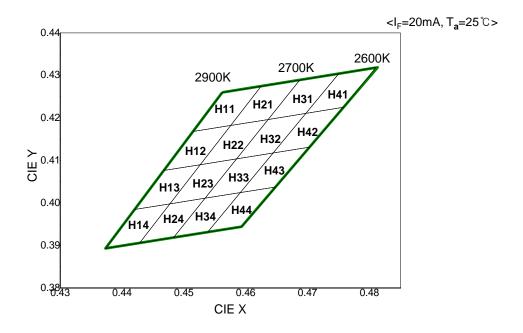




G	11	G	21	G3	31	G.	41
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.4299	0.4165	0.4364	0.4188	0.4430	0.4212	0.4496	0.4236
0.4261	0.4077	0.4324	0.4099	0.4387	0.4122	0.4451	0.4145
0.4324	0.4100	0.4387	0.4122	0.4451	0.4145	0.4514	0.4168
0.4365	0.4189	0.4430	0.4212	0.4496	0.4236	0.4562	0.4260
G	12	G	22	G	32	G.	42
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.4261	0.4077	0.4324	0.4100	0.4387	0.4122	0.4451	0.4145
0.4223	0.3990	0.4284	0.4011	0.4345	0.4033	0.4406	0.4055
0.4284	0.4011	0.4345	0.4033	0.4406	0.4055	0.4468	0.4077
0.4324	0.4100	0.4387	0.4122	0.4451	0.4145	0.4515	0.4168
G	13	G	23	G	33	G.	43
CIE X	13 CIE Y	G CIE X	23 CIE Y	G: CIE X	CIE Y	G/ CIE X	43 CIE Y
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
CIE X 0.4223	CIE Y 0.3990	CIE X 0.4284	CIE Y 0.4011	CIE X 0.4345	CIE Y 0.4033	CIE X 0.4406	CIE Y 0.4055
CIE X 0.4223 0.4185	CIE Y 0.3990 0.3902	CIE X 0.4284 0.4243	CIE Y 0.4011 0.3922	CIE X 0.4345 0.4302	CIE Y 0.4033 0.3943	CIE X 0.4406 0.4361	CIE Y 0.4055 0.3964
CIE X 0.4223 0.4185 0.4243 0.4284	CIE Y 0.3990 0.3902 0.3922	CIE X 0.4284 0.4243 0.4302 0.4345	CIE Y 0.4011 0.3922 0.3943	CIE X 0.4345 0.4302 0.4361	CIE Y 0.4033 0.3943 0.3964 0.4055	CIE X 0.4406 0.4361 0.4420	CIE Y 0.4055 0.3964 0.3985 0.4077
CIE X 0.4223 0.4185 0.4243 0.4284	CIE Y 0.3990 0.3902 0.3922 0.4011	CIE X 0.4284 0.4243 0.4302 0.4345	CIE Y 0.4011 0.3922 0.3943 0.4033	CIE X 0.4345 0.4302 0.4361 0.4406	CIE Y 0.4033 0.3943 0.3964 0.4055	CIE X 0.4406 0.4361 0.4420 0.4468	CIE Y 0.4055 0.3964 0.3985 0.4077
CIE X 0.4223 0.4185 0.4243 0.4284	CIE Y 0.3990 0.3902 0.3922 0.4011	CIE X 0.4284 0.4243 0.4302 0.4345	CIE Y 0.4011 0.3922 0.3943 0.4033	CIE X 0.4345 0.4302 0.4361 0.4406	CIE Y 0.4033 0.3943 0.3964 0.4055	CIE X 0.4406 0.4361 0.4420 0.4468	CIE Y 0.4055 0.3964 0.3985 0.4077
CIE X 0.4223 0.4185 0.4243 0.4284  G CIE X	CIE Y 0.3990 0.3902 0.3922 0.4011 14 CIE Y	CIE X 0.4284 0.4243 0.4302 0.4345 G CIE X	CIE Y 0.4011 0.3922 0.3943 0.4033 24 CIE Y	CIE X 0.4345 0.4302 0.4361 0.4406 G3 CIE X	CIE Y 0.4033 0.3943 0.3964 0.4055 34 CIE Y	CIE X 0.4406 0.4361 0.4420 0.4468 GCIE X	CIE Y 0.4055 0.3964 0.3985 0.4077 44 CIE Y
CIE X 0.4223 0.4185 0.4243 0.4284  G CIE X 0.4243	CIE Y 0.3990 0.3902 0.3922 0.4011 14 CIE Y 0.3922	CIE X 0.4284 0.4243 0.4302 0.4345 G CIE X 0.4302	CIE Y 0.4011 0.3922 0.3943 0.4033 24 CIE Y 0.3943	CIE X 0.4345 0.4302 0.4361 0.4406 CIE X 0.4302	CIE Y 0.4033 0.3943 0.3964 0.4055 34 CIE Y 0.3943	CIE X 0.4406 0.4361 0.4420 0.4468 GCIE X 0.4361	0.4055 0.3964 0.3985 0.4077 44 CIE Y 0.3964

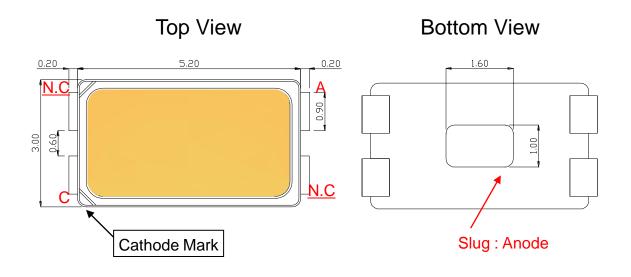
# **SEOUL SEMICONDUCTOR**

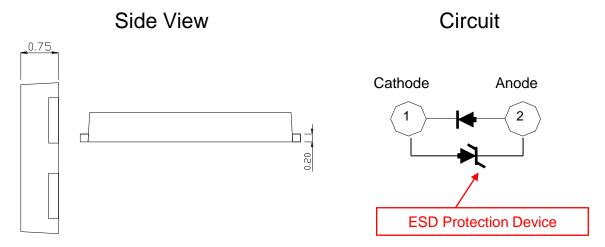
### SAW9KG0B - Acrich MJT 5630



Y
304
211
225
319
Y
211
18
32
225
225
225 Y
ΞΥ
Y 18
18 025
18 025 038
18 025 038
18 025 038
118 025 038 32
18 025 038 32 FY 025
3

### **Mechanical Dimensions**



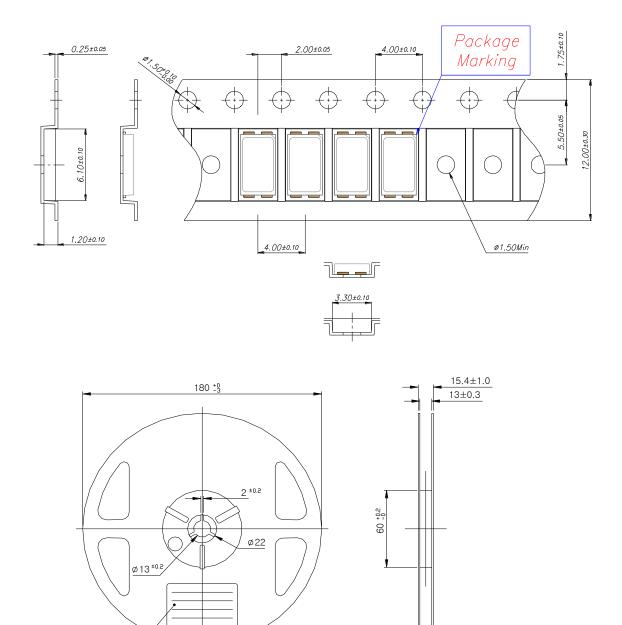


[Note] Package Forward Current is 20mA

### Notes:

- (1) All dimensions are in millimeters.
- (2) Scale: none
- (3) Undefined tolerance is  $\pm 0.2 mm$

# **Reel Packaging**



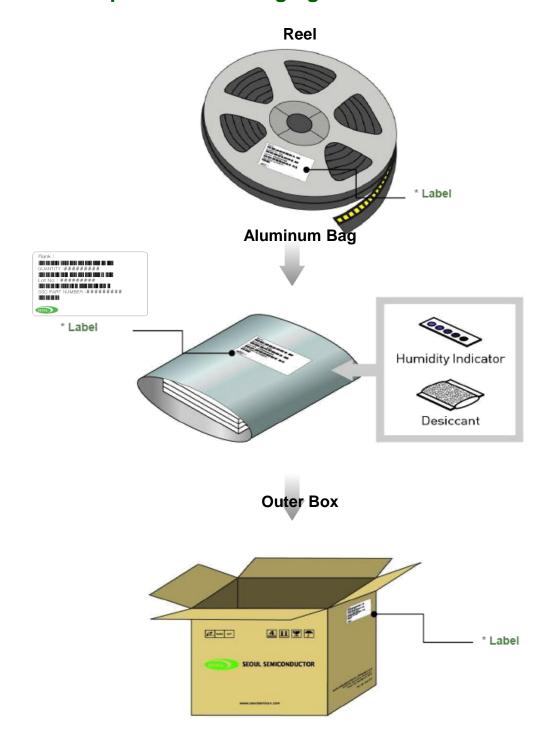
( Tolerance:  $\pm 0.2$ , Unit: mm )

(1) Quantity: Max 3,500pcs/Reel

Label

- (2) Cumulative Tolerance : Cumulative Tolerance/10 pitches to be  $\pm 0.2$ mm
- (3) Adhesion Strength of Cover Tape
  Adhesion strength to be 0.1-0.7N when the cover tape is turned off from the carrier tape at the angle of 10° to the carrier tape.
- (4) Package: P/N, Manufacturing data Code No. and Quantity to be indicated on a damp proof Package.

# **Emitter Tape & Reel Packaging**



# **Emitter Tape & Reel Packaging**

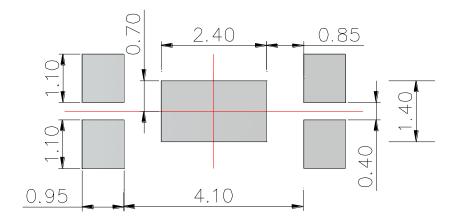
Table 6. Part Numbering System :  $X_1X_2X_3X_4X_5X_6X_7$ 

Part Number Code	Description	Part Number	Value
X <sub>1</sub>	Company	S	SSC
X <sub>2</sub>	Acrich LED series	Α	
X <sub>3</sub> X <sub>4</sub>	Color Specification	W9	CRI 90
X <sub>5</sub>	Package series	К	K series
X <sub>6</sub>	Voltage	G	
X <sub>7</sub>	PCB type	0	Emitter
X <sub>8</sub>	Revision	В	rev1

Table 7. Lot Numbering System  $:Y_1Y_2Y_3Y_4Y_5Y_6Y_7Y_8Y_9Y_{10} - Y_{11}Y_{12}Y_{13}Y_{14}Y_{15}Y_{16}Y_{17}$ 

Lot Number Code	Description	Lot Number	Value
Y <sub>1</sub> Y <sub>2</sub>	Year		
Y <sub>3</sub>	Month		
Y <sub>4</sub> Y <sub>5</sub>	Day		
Y <sub>6</sub>	Top View LED series		
Y <sub>7</sub> Y <sub>8</sub> Y <sub>9</sub> Y <sub>10</sub>	Mass order		
Y <sub>11</sub> Y <sub>12</sub> Y <sub>13</sub> Y <sub>14</sub> Y <sub>15</sub> Y <sub>16</sub> Y <sub>17</sub>	Internal Number		

### **Recommended Solder Pad**



### [Recommended Solder Pattern]

### Notes:

- (1) All dimensions are in millimeters.
- (2) Scale: none
- (3) This drawing without tolerances are for reference only
- (4) Undefined tolerance is  $\pm 0.1 \text{mm}$

### **Reflow Soldering Characteristics**

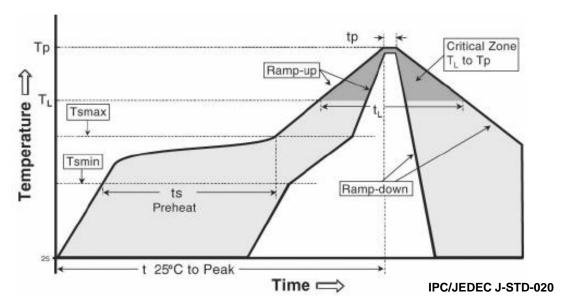


Table 8.

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3° C/second max.	3° C/second max.
Preheat - Temperature Min (T <sub>smin</sub> ) - Temperature Max (T <sub>smax</sub> ) - Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-180 seconds
Time maintained above: - Temperature (T <sub>L</sub> ) - Time (t <sub>L</sub> )	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak Temperature (T <sub>p</sub> )	215℃	260℃
Time within 5°C of actual Peak Temperature (t <sub>p</sub> )2	10-30 seconds	20-40 seconds
Ramp-down Rate	6 °C/second max.	6 °C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

### Caution

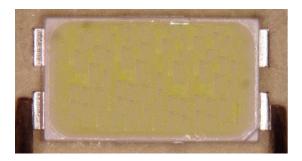
- (1) Reflow soldering is recommended not to be done more than two times. In the case of more than 24 hours passed soldering after first, LEDs will be damaged.
- (2) Repairs should not be done after the LEDs have been soldered. When repair is unavoidable, suitable tools must be used.
- (3) Die slug is to be soldered.
- (4) When soldering, do not put stress on the LEDs during heating.
- (5) After soldering, do not warp the circuit board.

### **Handling of Silicone Resin for LEDs**

(1) During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound.



(2) In general, LEDs should only be handled from the side. By the way, this also applies to LEDs without a silicone sealant, since the surface can also become scratched.



- (3) When populating boards in SMT production, there are basically no restrictions regarding the form of the pick and place nozzle, except that mechanical pressure on the surface of the resin must be prevented. This is assured by choosing a pick and place nozzle which is larger than the LED's reflector area.
- (4) Silicone differs from materials conventionally used for the manufacturing of LEDs. These conditions must be considered during the handling of such devices. Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust.

As mentioned previously, the increased sensitivity to dust requires special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components.

- (5) SSC suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin.

  Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.
- (6) Please do not mold this product into another resin (epoxy, urethane, etc) and do not handle this. product with acid or sulfur material in sealed space.

### **Precaution for Use**

(1) Storage

To avoid the moisture penetration, we recommend store in a dry box with a desiccant.

The recommended storage temperature range is 5  $^{\circ}$ C to 30  $^{\circ}$ C and a maximum humidity of RH50%.

(2) Use Precaution after Opening the Packaging

Use proper SMT techniques when the LED is to be soldered dipped as separation of the lens may affect the light output efficiency.

Pay attention to the following:

- a. Recommend conditions after opening the package
  - Sealing
  - Temperature : 5 ~ 40 °C Humidity : less than RH30%
- b. If the package has been opened more than 4 week(MSL\_2a) or the color of the desiccant changes, components should be dried for 10-12hr at  $60\pm5\,^{\circ}$ C
- (3) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering.
- (4) Do not rapidly cool device after soldering.
- (5) Components should not be mounted on warped (non coplanar) portion of PCB.
- (6) Radioactive exposure is not considered for the products listed here in.
- (7) Gallium arsenide is used in some of the products listed in this publication.

These products are dangerous if they are burned or shredded in the process of disposal.

- It is also dangerous to drink the liquid or inhale the gas generated by such products when chemically disposed of.
- (8) This device should not be used in any type of fluid such as water, oil, organic solvent and etc. When washing is required, IPA (Isopropyl Alcohol) should be used.
- (9) When the LEDs are in operation the maximum current should be decided after measuring the package temperature.
- (10) LEDs must be stored properly to maintain the device. If the LEDs are stored for 3 months or more after being shipped from SSC, a sealed container with a nitrogen atmosphere should be used for storage.
- (11) The appearance and specifications of the product may be modified for improvement without notice.
- (12) Long time exposure of sunlight or occasional UV exposure will cause lens discoloration.
- (13) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy.

The result can be a significant loss of light output from the fixture.

Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues.

- (14) Attaching LEDs, do not use adhesives that outgas organic vapor.
- (15) The driving circuit must be designed to allow forward voltage only when it is ON or OFF.

  If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.

### **Company Information**

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#### **Company Information**

Seoul Semiconductor (www.SeoulSemicon.com) manufacturers and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, Home appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs.

The company's broad product portfolio includes a wide array of package and device choices such as Acrich and Acirch2, high-brightness LEDs, mid-power LEDs, side-view LEDs, and through-hole type LEDs as well as custom modules, displays, and sensors.

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