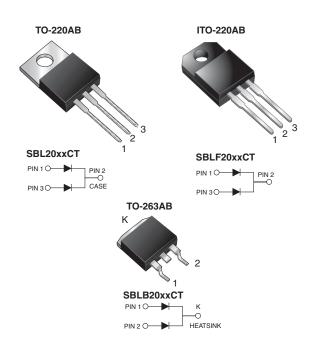
## SBL(F,B)2030CT, SBL(F,B)2040CT

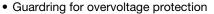
Vishay General Semiconductor

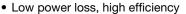
## **Dual Common Cathode Schottky Rectifier**



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	10 A x 2				
V <sub>RRM</sub>	30 V, 40 V				
I <sub>FSM</sub>	250 A				
V <sub>F</sub>	0.60 V				
T <sub>J</sub> max.	150 °C				

#### **FEATURES**





· Low forward voltage drop

· High forward surge capability

High frequency operation



 Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB and ITO-220AB package)

AEC-Q101 qualified

 Material categorization: For definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

For use in low voltage, high frequency rectifier of switching mode power supplies, freewheeling diodes, DC/DC converters and polarity protection application.

### **MECHANICAL DATA**

Case: TO-220AB, ITO-220AB, TO-263AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade Base P/NHE3 - RoHS-compliant, AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix

meets JESD 201 class 2 whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	SBL2030CT	SBL2040CT	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	30	40		
Working peak reverse voltage		V <sub>RWM</sub>	21	28	V	
Maximum DC blocking voltage		V <sub>DC</sub>	30	40		
Maximum average forward rectified current at $T_C = 105\ ^{\circ}C$	total device	I <sub>F(AV)</sub>	20			
	per diode		10			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	250		А	
Peak repetitive reverse surge current per diode at $t_p$ = 2.0 $\mu$ s, 1 kHz		I <sub>RRM</sub>	1.0			
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 150		°C	
Isolation voltage (ITO-220AB only) from terminal to heatsink t = 1 min		V <sub>AC</sub>	1500		V	



# SBL(F,B)2030CT, SBL(F,B)2040CT

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>C</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUE	UNIT
Maximum instantaneous forward voltage per diode	V <sub>F</sub> <sup>(1)</sup>	10 A		0.6	V
Maximum instantaneous reverse current at DC blocking voltage per diode (1)	I <sub>R</sub> <sup>(2)</sup>	Rated V <sub>R</sub>	T <sub>C</sub> = 25 °C	1.0	- mA
			T <sub>C</sub> = 100 °C	50	

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>C</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	SBL	SBLF	SBLB	UNIT
Typical thermal resistance from junction to case per diode	$R_{ heta JC}$	2.0	4.0	2.0	°C/W

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-220AB	SBL2030CT-E3/45	1.85	45	50/tube	Tube		
ITO-220AB	SBLF2030CT-E3/45	1.99	45	50/tube	Tube		
TO-263AB	SBLB2030CT-E3/45	1.35	45	50/tube	Tube		
TO-263AB	SBLB2030CT-E3/81	1.33	81	800/reel	Tape and reel		
TO-220AB	SBL2030CTHE3/45 (1)	1.85	45	50/tube	Tube		
ITO-220AB	SBLF2030CTHE3/45 <sup>1)</sup>	1.99	45	50/tube	Tube		
TO-263AB	SBLB2030CTHE3/45 (1)	1.35	45	50/tube	Tube		
TO-263AB	SBLB2030CTHE3/81 (1)	1.33	81	800/reel	Tape and reel		

#### Note

(1) AEC-Q101 qualified

# Vishay General Semiconductor

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

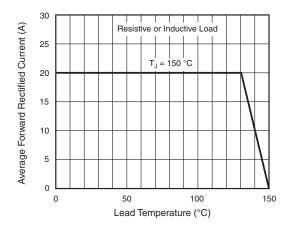


Fig. 1 - Forward Current Derating Curve

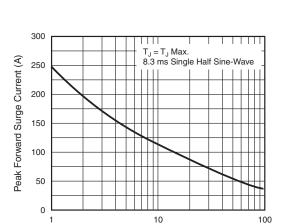


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

Number of Cycles at 60 Hz

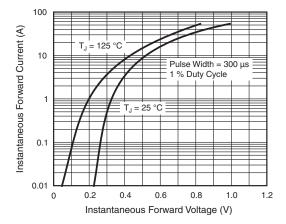


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

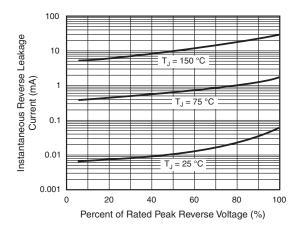


Fig. 4 - Typical Reverse Characteristics Per Diode

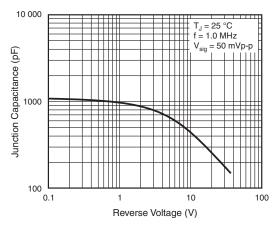


Fig. 5 - Typical Junction Capacitance Per Diode

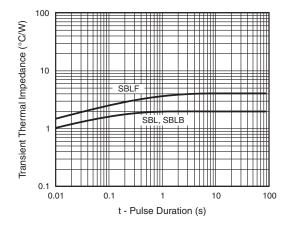
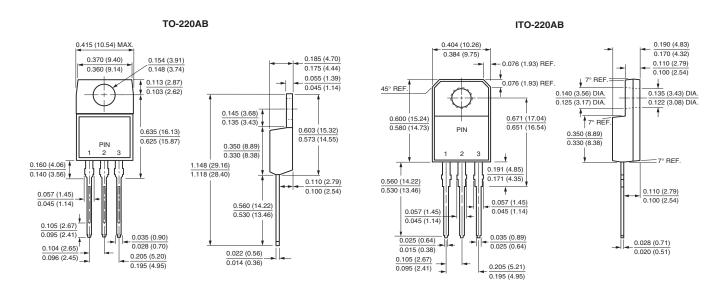


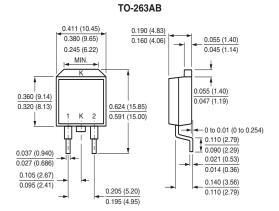
Fig. 6 - Typical Transient Thermal Impedance Per Diode

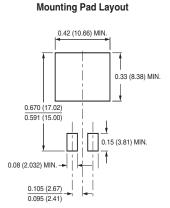
# SBL(F,B)2030CT, SBL(F,B)2040CT

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### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)









### **Legal Disclaimer Notice**

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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