

## **N-channel SiC power MOSFET**

$V_{\mathrm{DSS}}$	1200V
R <sub>DS(on)</sub> (Typ.)	40m $Ω$
I <sub>D</sub>	55A
$P_D$	262W

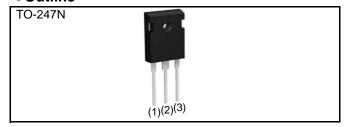
# ● Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

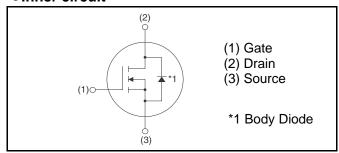
### Application

- · Solar inverters
- DC/DC converters
- Switch mode power supplies
- · Induction heating
- Motor drives

#### Outline



### •Inner circuit



### Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Typo	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT3040KL

### ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		$V_{DSS}$	1200	V
Continuous drain current	T <sub>c</sub> = 25°C	I <sub>D</sub> *1	55	А
	T <sub>c</sub> = 100°C	I <sub>D</sub> *1	39	А
Pulsed drain current		I <sub>D,pulse</sub> *2	137	А
Gate - Source voltage		$V_{GSS}$	-4 to 22	V
Junction temperature	T <sub>j</sub>	175	°C	
Range of storage temperature		T <sub>stg</sub>	-55 to +175	°C

### ●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	
Thermal resistance, junction - case	$R_{thJC}$	-	0.44	0.57	°C/W

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$ , $I_D = 1mA$	1200	-	-	V
		$V_{DS} = 1200V, V_{GS} = 0V$				
Zero gate voltage drain current	$I_{ m DSS}$	$T_j = 25^{\circ}C$	-	1	10	μΑ
		T <sub>j</sub> = 150°C	-	2	-	
Gate - Source leakage current	I <sub>GSS+</sub>	$V_{GS} = +22V, V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current	I <sub>GSS-</sub>	$V_{GS} = -4V$ , $V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	V <sub>GS (th)</sub>	$V_{DS} = 10V, I_{D} = 10mA$	2.7	-	5.6	V
		$V_{GS} = 18V, I_D = 20A$				
Static drain - source on - state resistance	R <sub>DS(on)</sub> *3	T <sub>j</sub> = 25°C	-	40	52	mΩ
2		T <sub>j</sub> = 125°C	-	60	-	
Gate input resistance	R <sub>G</sub>	f = 1MHz, open drain	-	7	-	Ω

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatar	Cymahal	ymbol Conditions		Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	g <sub>fs</sub> *3	$V_{DS} = 10V, I_D = 20A$	-	8.3	-	S
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1337	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 800V	-	76	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	27	-	
Effective output capacitance, energy related	C <sub>o(er)</sub>	$V_{GS} = 0V$ $V_{DS} = 0V$ to 600V	-	122	-	pF
Turn - on delay time	t <sub>d(on)</sub> *3	$V_{DD} = 400V, I_{D} = 18A$	-	21	1	
Rise time	t <sub>r</sub> *3	$V_{GS} = 18V/0V$	-	39	-	no
Turn - off delay time	t <sub>d(off)</sub> *3	$R_L = 22\Omega$	-	49	ı	ns
Fall time	t <sub>f</sub> *3	$R_G = 0\Omega$	-	24	ı	
Turn - on switching loss	E <sub>on</sub> *3	$V_{DD} = 600V, I_{D} = 20A$ $V_{GS} = 18V/0V$	-	283		1
Turn - off switching loss	E <sub>off</sub> *3	$R_G = 0\Omega L=250\mu H$ * $E_{on}$ includes diode reverse recovery	-	118	-	μJ

## •Gate Charge characteristics $(T_a = 25^{\circ}C)$

Parameter	Symbol	Conditions	Values			Unit
	Symbol		Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*3}$	V <sub>DD</sub> = 600V	-	107	-	
Gate - Source charge	$Q_{gs}^{*3}$	I <sub>D</sub> = 20A	-	22	-	nC
Gate - Drain charge	Q <sub>gd</sub> *3	V <sub>GS</sub> = 18V	-	41	-	
Gate plateau voltage	V <sub>(plateau)</sub>	$V_{DD} = 600V, I_D = 20A$	-	9.6	-	V

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> PW  $\leq$  10  $\mu s,$  Duty cycle  $\leq$  1%

<sup>\*3</sup> Pulsed

## ullet Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit
		Min.	Тур.	Max.	Offic	
Inverse diode continuous, forward current	l <sub>S</sub> *1	-T <sub>c</sub> = 25°C	-	1	55	А
Inverse diode direct current, pulsed	I <sub>SM</sub> *2		-	-	137	А
Forward voltage	V <sub>SD</sub> *3	$V_{GS} = 0V, I_{S} = 20A$	-	3.2	ı	V
Reverse recovery time	t <sub>rr</sub> *3	I <sub>F</sub> = 20A, V <sub>R</sub> = 600V di/dt = 1100A/μs	-	25	ı	ns
Reverse recovery charge	Q <sub>rr</sub> *3		-	115	-	nC
Peak reverse recovery current	I <sub>rrm</sub> *3		-	9	-	Α

Fig.1 Power Dissipation Derating Curve

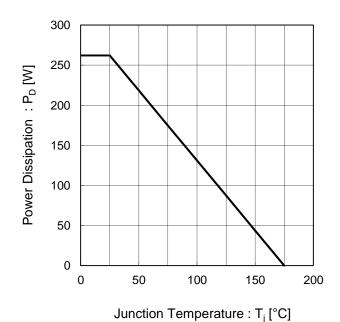
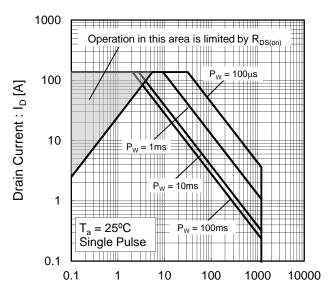
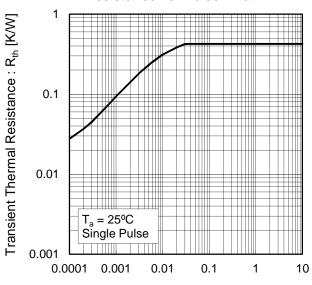


Fig.2 Maximum Safe Operating Area



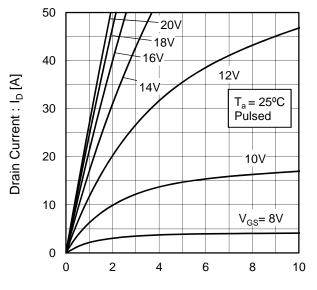
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.3 Typical Transient Thermal Resistance vs. Pulse Width



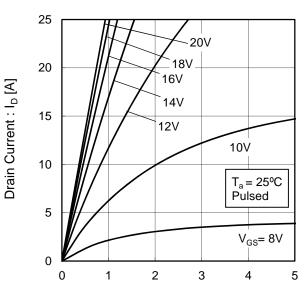
Pulse Width: Pw [s]

Fig.4 Typical Output Characteristics(I)

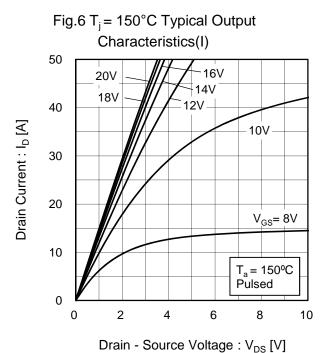


Drain - Source Voltage :  $V_{DS}$  [V]

Fig.5 Typical Output Characteristics(II)



Drain - Source Voltage : V<sub>DS</sub> [V]



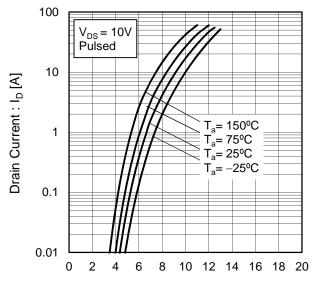
Drain Current : I<sub>D</sub> [A]

Characteristics(II) 25 20V 10V 20 14V 16V 15 18V  $V_{GS} = 8V$ 10 5 T<sub>a</sub> = 150°C Pulsed 0 1 2 3 0 5

Fig.7 T<sub>i</sub> = 150°C Typical Output

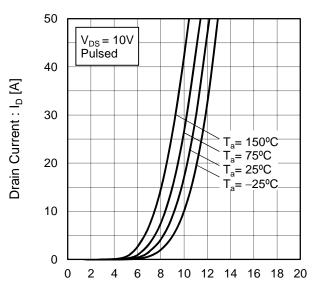
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.8 Typical Transfer Characteristics (I)



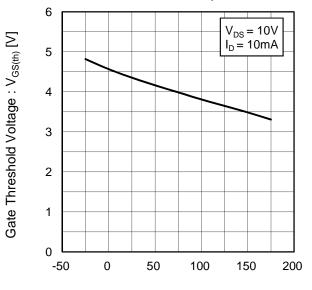
Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.9 Typical Transfer Characteristics (II)



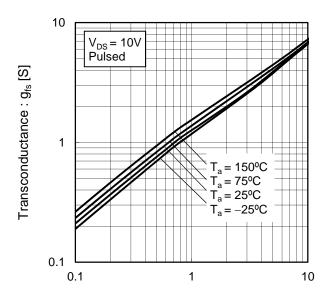
Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.10 Gate Threshold Voltage vs. Junction Temperature



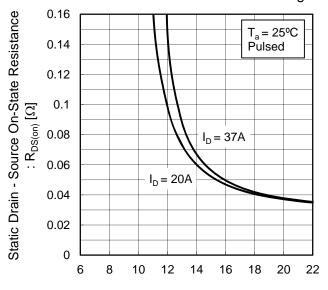
Junction Temperature :  $T_j$  [°C]

Fig.11 Transconductance vs. Drain Current



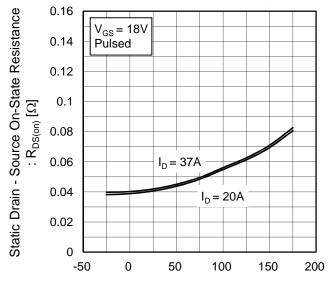
Drain Current : I<sub>D</sub> [A]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



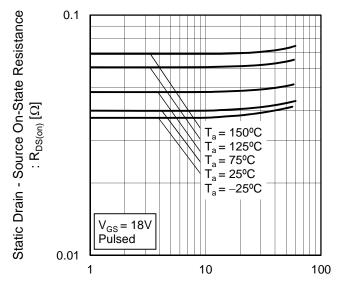
Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



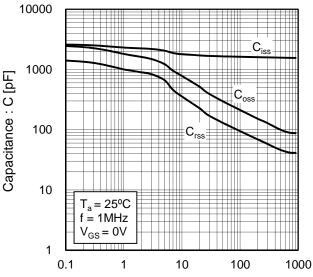
Junction Temperature : T<sub>i</sub> [°C]

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current



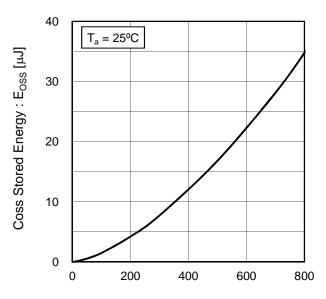
Drain Current : I<sub>D</sub> [A]

Fig.15 Typical Capacitance
vs. Drain - Source Voltage



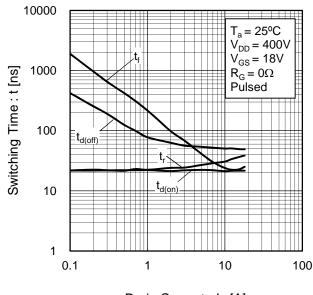
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.16 Coss Stored Energy



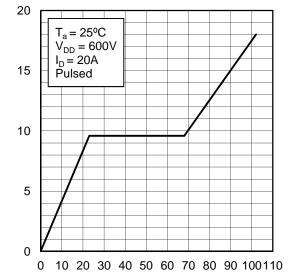
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.17 Switching Characteristics



Drain Current : I<sub>D</sub> [A]

Fig.18 Dynamic Input Characteristics



Total Gate Charge : Q<sub>q</sub> [nC]

Gate - Source Voltage : V<sub>GS</sub> [V]

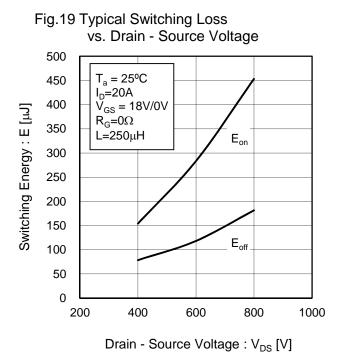
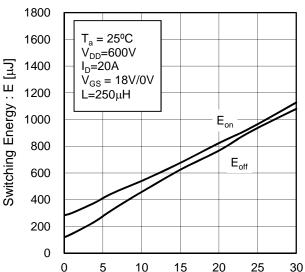


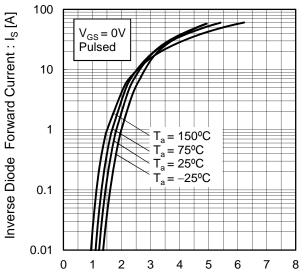
Fig.20 Typical Switching Loss vs. Drain Current 1800  $T_a = 25^{\circ}C$ 1600 V<sub>DD</sub>=600V  $V_{GS} = 18V/0V$  $R_G = 0\Omega$ Switching Energy : E [µJ] 1400 L=250μH 1200  $\mathsf{E}_{\mathsf{on}}$ 1000 800 600 400 200 0 10 30 20 40 50 60 0 Drain Current : I<sub>D</sub> [A]

Fig.21 Typical Switching Loss vs. External Gate Resistance



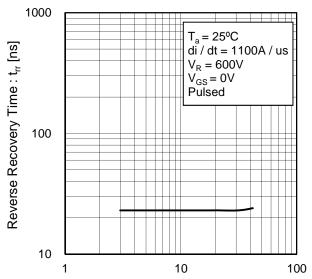
External Gate Resistance :  $R_G[\Omega]$ 

Fig.22 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V<sub>SD</sub> [V]

Fig.23 Reverse Recovery Time vs.Inverse Diode Forward Current



Inverse Diode Forward Current : I<sub>S</sub> [A]

#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

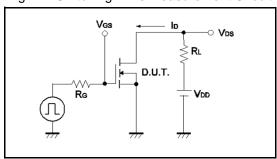


Fig.2-1 Gate Charge Measurement Circuit

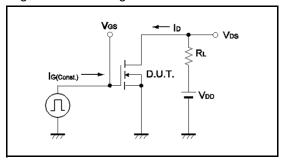


Fig.3-1 Switching Energy Measurement Circuit

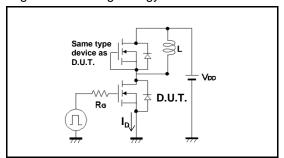


Fig.4-1 Reverse Recovery Time Measurement Circuit Fig.4-2 Reverse Recovery Waveform

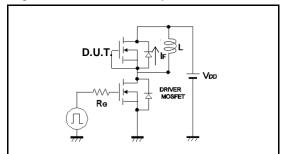


Fig.1-2 Switching Waveforms

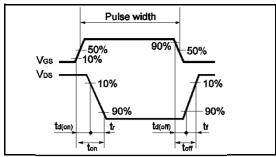


Fig.2-2 Gate Charge Waveform

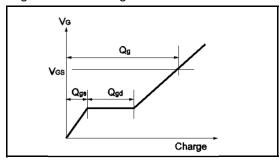
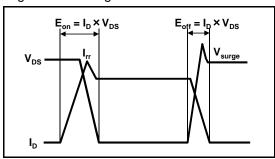
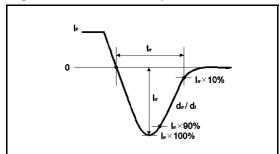


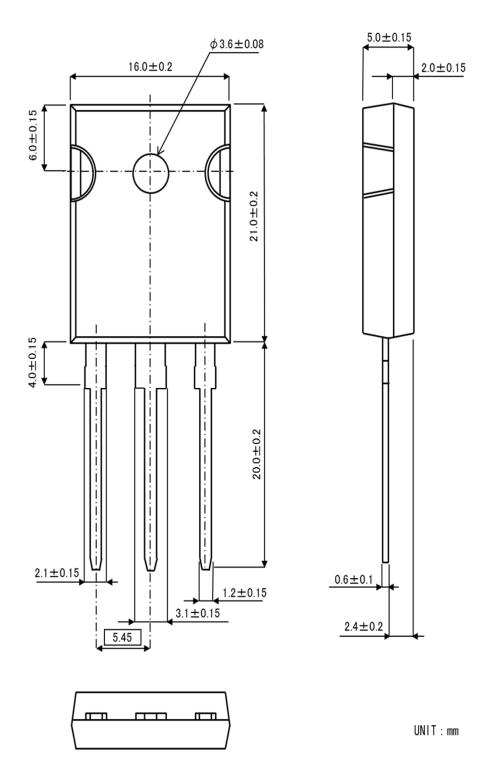
Fig.3-2 Switching Waveforms





### Dimensions

TO-247



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