

RoHS

COMPLIANT

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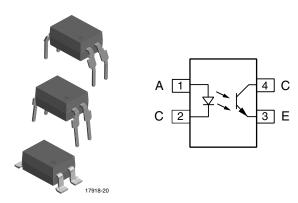
(5-2008)



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Vishay Semiconductors

Optocoupler, Phototransistor Output, High Reliability, 5000 V_{RMS}, 110 °C Rated



DESCRIPTION

The 110 °C rated SFH1617A (DIP) feature a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 package.

The coupling devices are designed for signal transmission between two electrically separated circuits.

The couplers are end-stackable with 2.54 mm spacing.

Creepage and clearance distances of > 8.0 mm are achieved with option 6. This version complies with IEC 60950 (DIN VDE 0805) for reinforced insulation up to an operation voltage of 400 V_{RMS} or DC. Specifications subject to change.

FEATURES

- Operating temperature from 55 °C to + 110 °C
- · Good CTR linearity depending on forward
- Isolation test voltage, 5000 V_{RMS}
- High collector emitter voltage, V_{CFO} = 70 V
- Low saturation voltage
- · Fast switching times
- Low CTR degradation
- Temperature stableIE
- · Low coupling capacitance
- End stackable, 0.100" (2.54 mm) spacing
- High common mode interference immunity

• Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- AC adapter
- SMPS
- PLC
- · Factory automation
- Game consoles

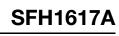
AGENCY APPROVALS

- UL1577, file no. E52744
- cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending) available with option 1
- BSI EN 60950; EN 60065
- FIMKO
- CQC (pending)

ORDERING INFORMATION									
S F H 1 6 1 7 A - # X 0 # # T PART NUMBER CTR PACKAGE OPTION TAPE AND REEL Option 7 > 8 mm									
AGENCY CERTIFIED/PACKAGE		CTF	R (%)						
UL, cUL, BSI, FIMKO	40 to 80	63 to 125	100 to 200	160 to 320					
DIP-4	-	SFH1617A-2	SFH1617A-3	-					
VDE, UL, cUL, BSI, FIMKO	40 to 80	63 to 125	100 to 200	160 to 320					
DIP-4, 400 mil, option 6	-	-	SFH1617A-3X016	-					
SMD-4, option 7	-	SFH617A-2X017T	-	-					

Note

Additional options may be possible, please contact sales office.





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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Reverse voltage		V _R	6.0	V				
DC forward current		I _F	60	mA				
Surge forward current	t ≤ 10 µs	I _{FSM}	2.5	Α				
LED power dissipation		P _{diss}	70	mW				
OUTPUT								
Collector emitter voltage		V _{CEO}	70	V				
Emitter collector voltage		V _{ECO}	7.0	V				
Collector current		I _C	50	mA				
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA				
Output power dissipation		P _{diss}	150	mW				
COUPLER								
Isolation test voltage between emitter and detector, refer to climate DIN 40046, part 2, Nov. 74		V _{ISO}	5000	V_{RMS}				
Isolation resistance	$V_{IO} = 500 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹²	Ω				
ISOIATION PESISTANCE	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω				
Storage temperature range		T _{stg}	- 55 to + 150	°C				
Ambient temperature range		T _{amb}	- 55 to + 110	°C				
Soldering temperature (1)	2 mm from case, ≤ 10 s	T _{sld}	260	°C				

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT								
Forward voltage	$I_F = 60 \text{ mA}$		V_{F}		1.35	1.65	V	
Reverse current	V _R = 6.0 V		I_{R}		0.01	10	μA	
Capacitance	$V_R = 0 V, f = 1.0 MHz$		Co		13		pF	
OUTPUT								
Collector emitter capacitance	$V_{CE} = 5.0 \text{ V}, f = 1.0 \text{ MHz}$		C _{CE}		5.2		pF	
	V _{CF} = 10 V	SFH1617A-1	I _{CEO}		2.0	50	nA	
Collector emitter leakage		SFH1617A-2	I _{CEO}		2.0	50	nA	
current	V _{CE} = 10 V	SFH1617A-3	I _{CEO}		5.0	100	nA	
		SFH1617A-4	I _{CEO}		5.0	100	nA	
COUPLER								
Collector emitter saturation voltage	I _F = 10 mA, f = 1.0 MHz		V _{CEsat}		0.25	0.4	V	
Coupling capacitance			C _C		0.4		pF	

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.



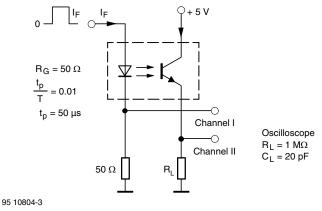


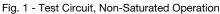
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CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
		SFH1617A-1	CTR	40		80	%	
	$I_{\rm F}$ = 10 mA, $V_{\rm CF}$ = 5.0 V	SFH1617A-2	CTR	63		125	%	
	I _F = 10 IIIA, V _{CE} = 5.0 V	SFH1617A-3	CTR	100		200	%	
1 //		SFH1617A-4	CTR	160		320	%	
l _O ∕l _F		SFH1617A-1	CTR	13	30		%	
		SFH1617A-2	CTR	22	45		%	
	$I_F = 1.0 \text{ mA}, V_{CE} = 5.0 \text{ V}$	SFH1617A-3	CTR	34	70		%	
		SFH1617A-4	CTR	56	90		%	

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED	·						
Turn-on time	$I_F = 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 75 \Omega$		t _{on}		3.0		μs
Rise time	$I_F = 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 75 \Omega$		t _r		2.0		μs
Turn-off time	$I_F = 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 75 \Omega$		t _{off}		2.3		μs
Fall time	$I_F = 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 75 \Omega$		t _f		2.0		μs
Cut-off frequency	I _F = 10 mA, V _{CC} = 5.0 V		f _{ctr}		100		kHz
SATURATED	·						
	I _F = 20 mA	SFH1617A-1	t _{on}		3.0		μs
T time.	I _F = 10 mA	SFH1617A-2	t _{on}		4.2		μs
Turn-on time		SFH1617A-3	t _{on}		4.2		μs
	I _F = 5.0 mA	SFH1617A-4	t _{on}		6.0		μs
	I _F = 20 mA	SFH1617A-1	t _r		2.0		μs
Diag times	I _F = 10 mA	SFH1617A-2	t _r		3.0		μs
Rise time		SFH1617A-3	t _r		3.0		μs
	I _F = 5.0 mA	SFH1617A-4	t _r		4.6		μs
	I _F = 20 mA	SFH1617A-1	t _{off}		18		μs
T # +:		SFH1617A-2	t _{off}		23		μs
Turn-off time	I _F = 10 mA	SFH1617A-3	t _{off}		23		μs
	I _F = 5.0 mA	SFH1617A-4	t _{off}		25		μs
F. W	I _F = 20 mA	SFH1617A-1	t _f		11		μs
	1. 10 1	SFH1617A-2	t _f		14		μs
Fall time	I _F = 10 mA	SFH1617A-3	t _f		14		μs
	I _F = 5.0 mA	SFH1617A-4	t _f		15		μs





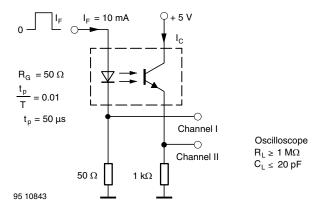
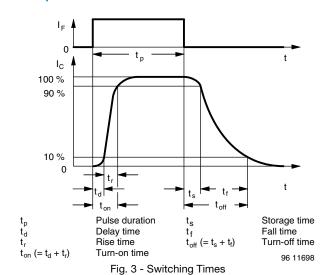


Fig. 2 - Test Circuit, Saturated Operation



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SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC 68 part 1)				55/110/21				
Comparative tracking index		CTI	175		399			
Rated impulse voltage		V _{IOTM}			8	kV		
Maximum working voltages	Recurring peak voltage	V _{IORM}			890	V		
Forward current		I _{SI}			275	mA		
Power dissipation		P _{SO}			400	mW		
Safety temperature		T _{SI}			175	°C		
Creepage distance			7.0			mm		
Clearance distance			7.0			mm		
Isolation distance	per IEC 60950 2.10.5.1		0.4			mm		

Note

According to DIN EN 60747-5-2 (VDE 0884). These optocouplers are suitable for "safe electrical insulation" only within the safety ratings.
 Compliance with the safety ratings shall be ensured by means of protective circuits.



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TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

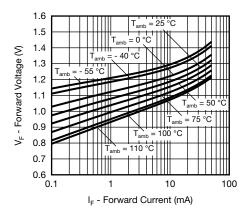


Fig. 4 - Forward Voltage vs. Forward Current

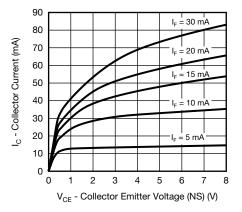


Fig. 5 - Collector Current vs. Collector Emitter Voltage (NS)

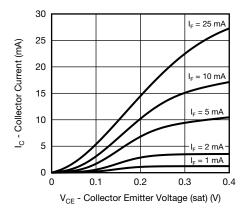


Fig. 6 - Collector Current vs. Collector Emitter Voltage (sat)

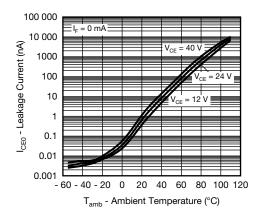


Fig. 7 - Leakage Current vs. Ambient Temperature

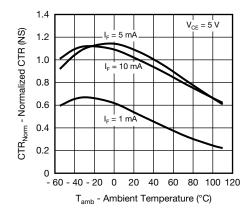


Fig. 8 - Normalized CTR (NS) vs. Ambient Temperature

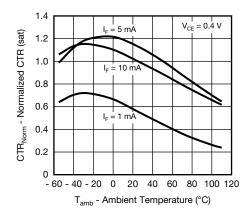


Fig. 9 - Normalized CTR (sat) vs. Ambient Temperature



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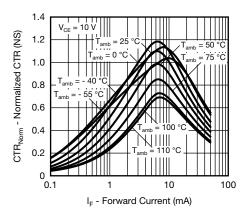


Fig. 10 - Normalized CTR (NS) vs. Forward Current

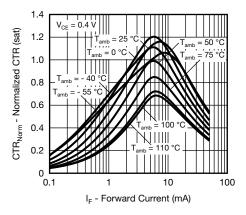


Fig. 11 - Normalized CTR (sat) vs. Forward Current

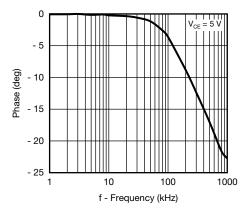


Fig. 12 - CTR Frequency vs. Phase Angle

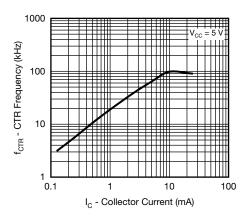


Fig. 13 - CTR Frequency vs. Collector Current

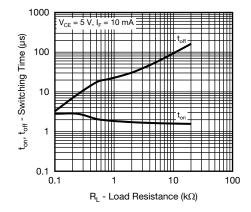
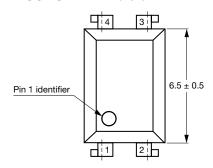


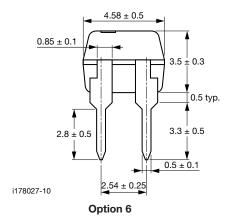
Fig. 14 - Switching Time vs. Load Resistance

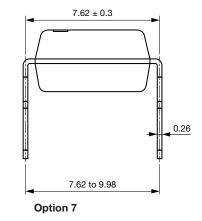


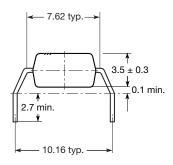
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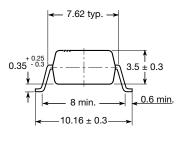
PACKAGE DIMENISONS in millimeters

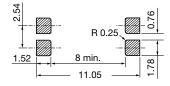












20802-30

PACKAGE MARKING



Notes

- VDE logo is only marked on option 1 parts. Option information is not marked on the part.
- Tape and reel suffix (T) is not part of the package marking.



Legal Disclaimer Notice

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