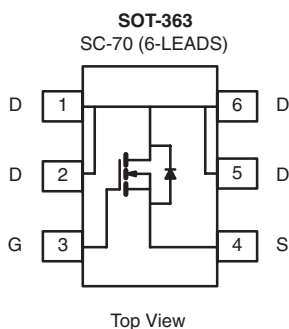


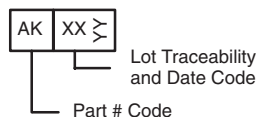
N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ.)
30	0.066 at $V_{GS} = 4.5$ V	4.0 ^a	4.85
	0.095 at $V_{GS} = 2.5$ V	4.0	



Marking Code



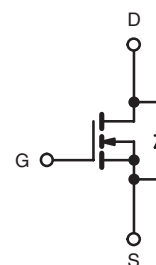
Ordering Information: Si1470DH-T1-E3 (Lead (Pb)-free)
Si1470DH-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE
Available



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	
Continuous Drain Current ($T_J = 150$ °C) ^a	$T_C = 25$ °C	5.1	A
	$T_C = 70$ °C	4.0	
	$T_A = 25$ °C	3.8 ^{b, c}	
	$T_A = 70$ °C	3.1 ^{b, c}	
Pulsed Drain Current	I_{DM}	12	mJ
Avalanche Current	I_{AS}	10	
Repetitive Avalanche Energy	E_{AS}	5	
Continuous Source-Drain Diode Current	$T_C = 25$ °C	2.3	A
	$T_A = 25$ °C	1.3 ^{b, c}	
Maximum Power Dissipation ^a	$T_C = 25$ °C	2.8	W
	$T_C = 70$ °C	1.8	
	$T_A = 25$ °C	1.5 ^{b, c}	
	$T_A = 70$ °C	1.0 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R_{thJA}	60	80	°C/W
Maximum Junction-to-Foot (Drain)	R_{thJF}	34	45	

Notes:

- Based on $T_C = 25$ °C.
- Surface mounted on 1" x 1" FR4 board.
- $t = 5$ s.
- Maximum under steady state conditions is 125 °C/W.

SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		27.41		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /			- 3.83		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.6		1.6	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 12 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	nA
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 85 °C			10	μA
On-State Drain Current ^a	I _{D(on)}	V _{DS} = ≥ 5 V, V _{GS} = 4.5 V	12			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 3.8 A		0.055	0.066	Ω
		V _{GS} = 2.5 V, I _D = 3.1 A		0.079	0.095	
Forward Transconductance	g _{fs}	V _{DS} = 15 V, I _D = 3.8 A		11.2		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		510		pF
Output Capacitance	C _{oss}			66		
Reverse Transfer Capacitance	C _{rss}			39		
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 5 V, I _D = 3.8 A		5	7.5	nC
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 3.8 A		4.85	7.3	
Gate-Drain Charge	Q _{gd}			1.35		
				1.26		
Gate Resistance	R _g	f = 1 MHz		7.3	10.95	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 5.0 Ω I _D ≅ 3.0 A, V _{GEN} = 4.5 V, R _g = 1 Ω		9.0	15	ns
Rise Time	t _r			51	77	
Turn-Off DelayTime	t _{d(off)}			18	27	
Fall Time	t _f			7.1	10.65	
Drain-Source Body Diode Characteristics						
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			2.3	A
Pulse Diode Forward Current ^a	I _{SM}				12	
Body Diode Voltage	V _{SD}	I _S = 1.8 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 2.3 A, dI/dt = 100 A/μs		11.5	17.25	nC
Body Diode Reverse Recovery Charge	Q _{rr}			5.2	7.8	ns
Reverse Recovery Fall Time	t _a			7.7		
Reverse Recovery Rise Time	t _b			3.8		

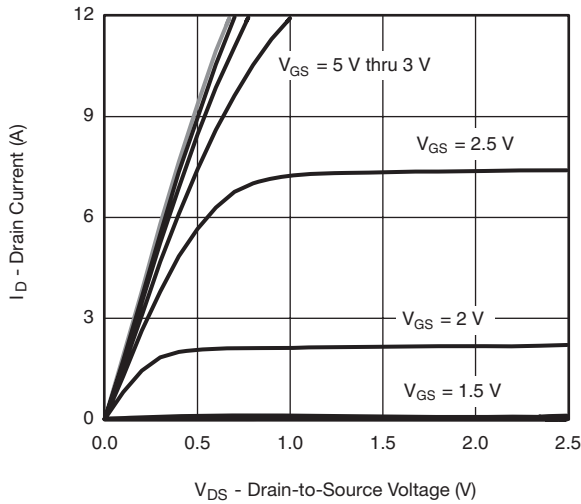
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

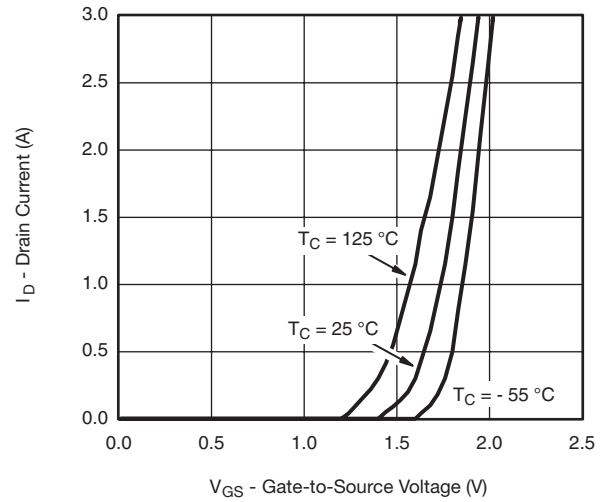
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

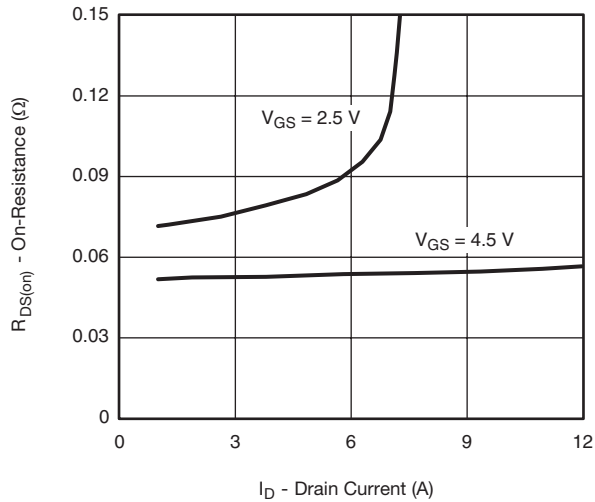
TYPICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$, unless otherwise noted



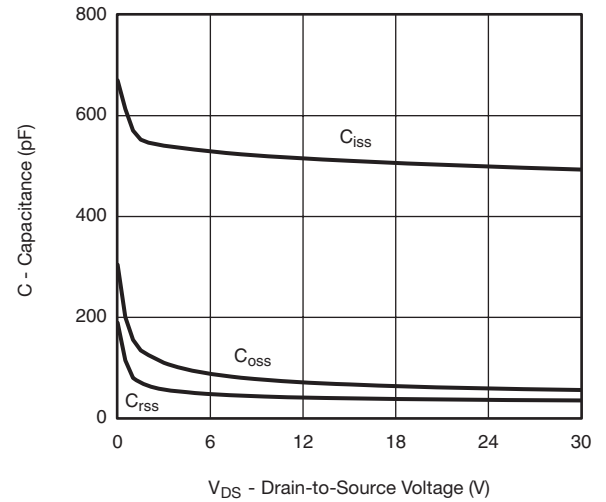
Output Characteristics



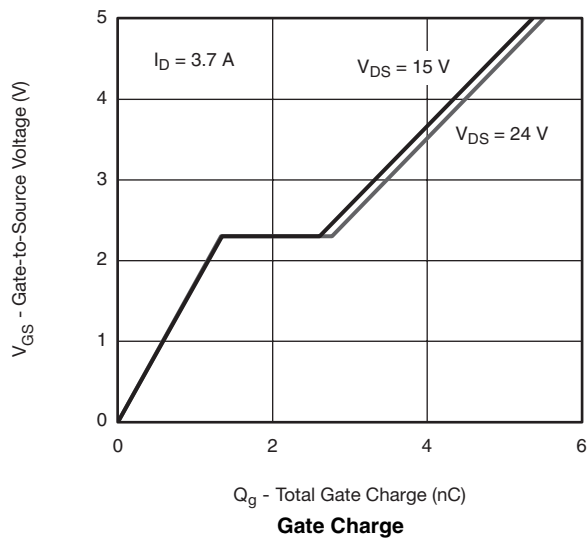
Transfer Characteristics Curves vs. Temperature



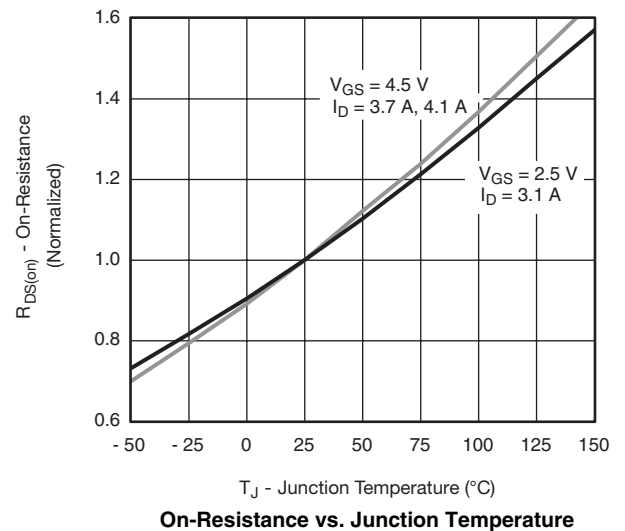
On-Resistance vs. Drain Current



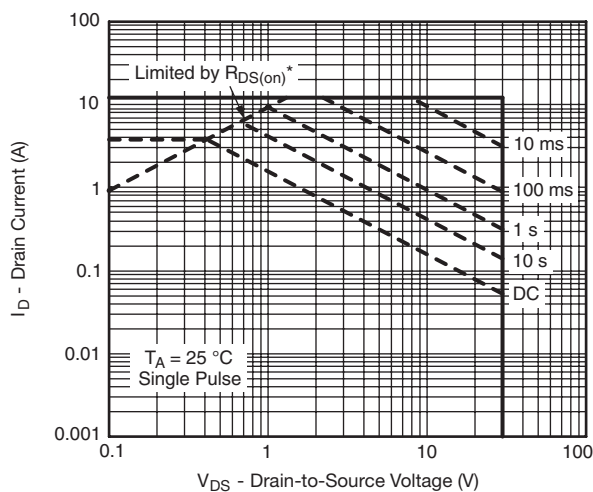
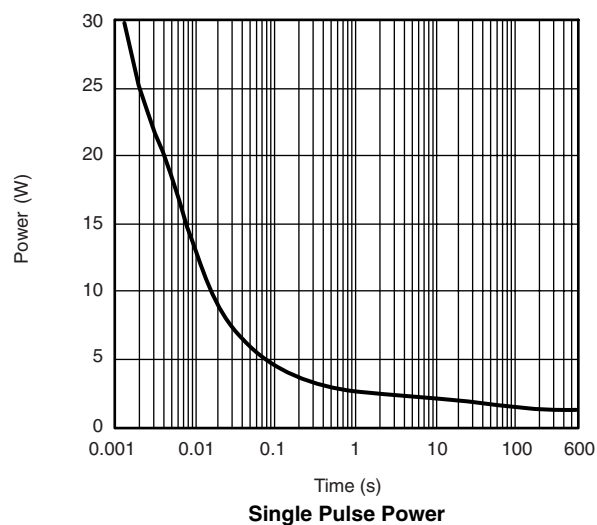
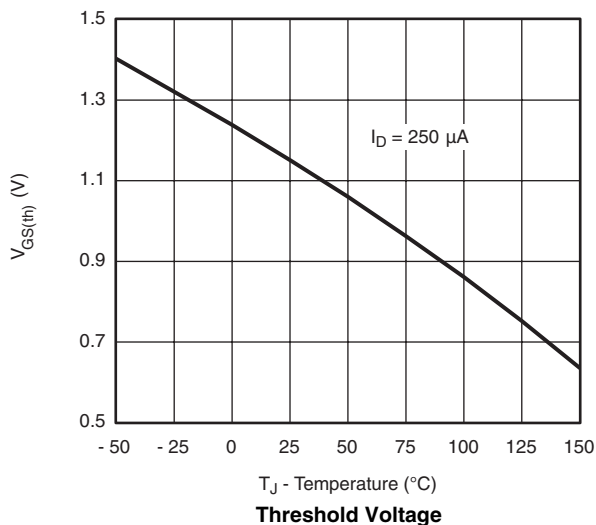
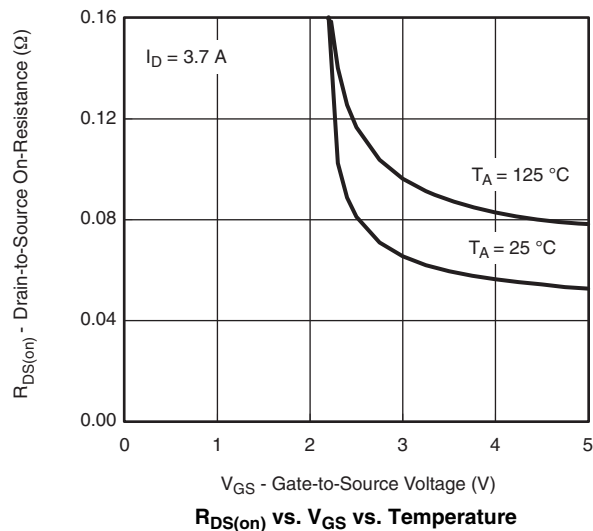
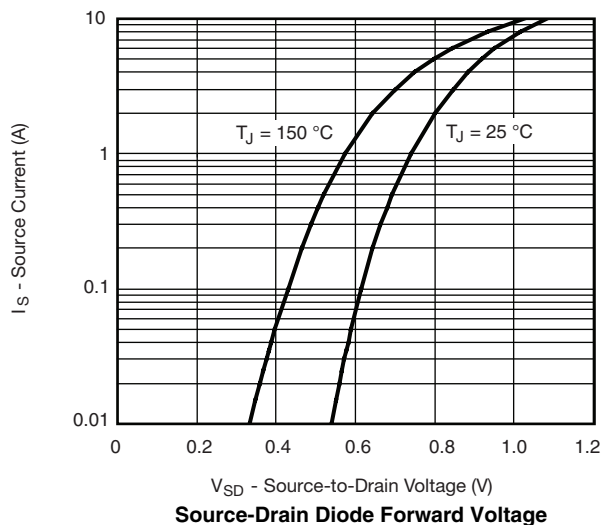
Capacitance



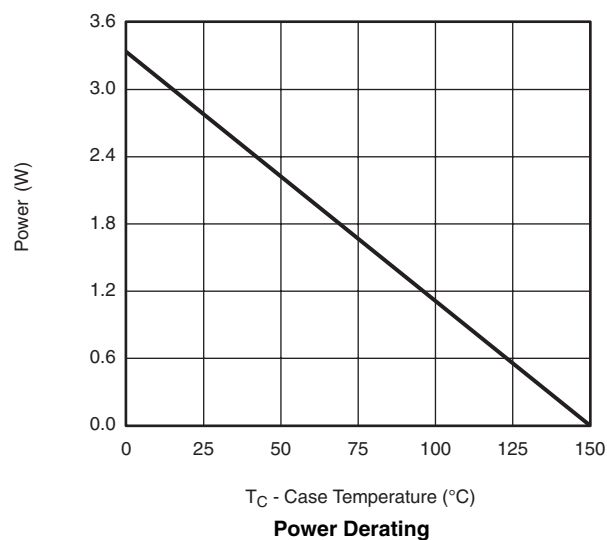
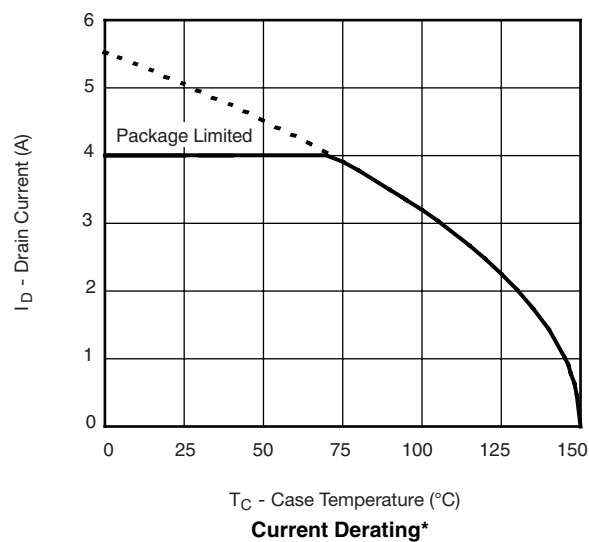
Gate Charge



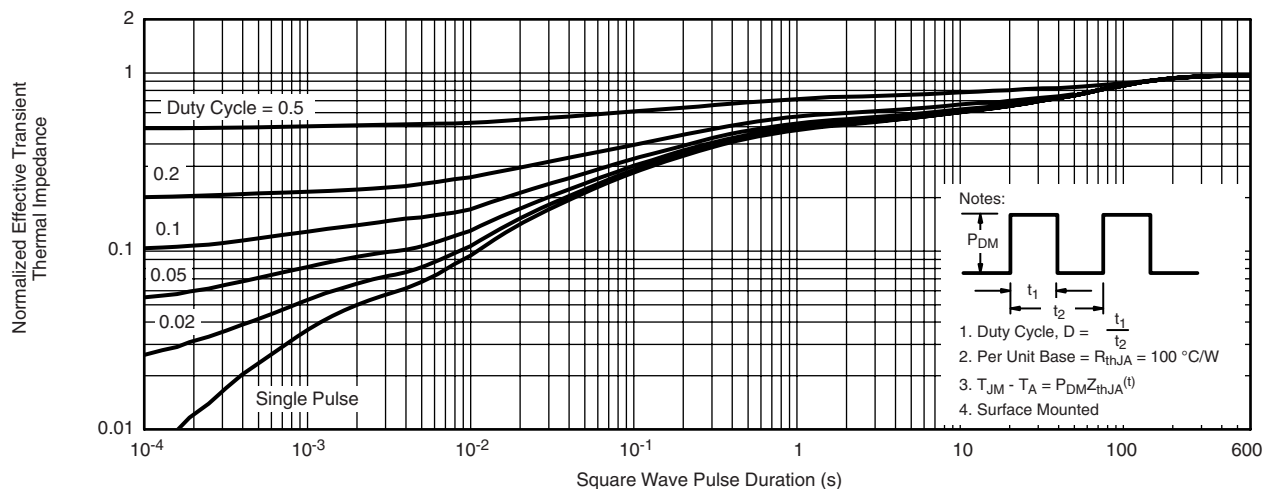
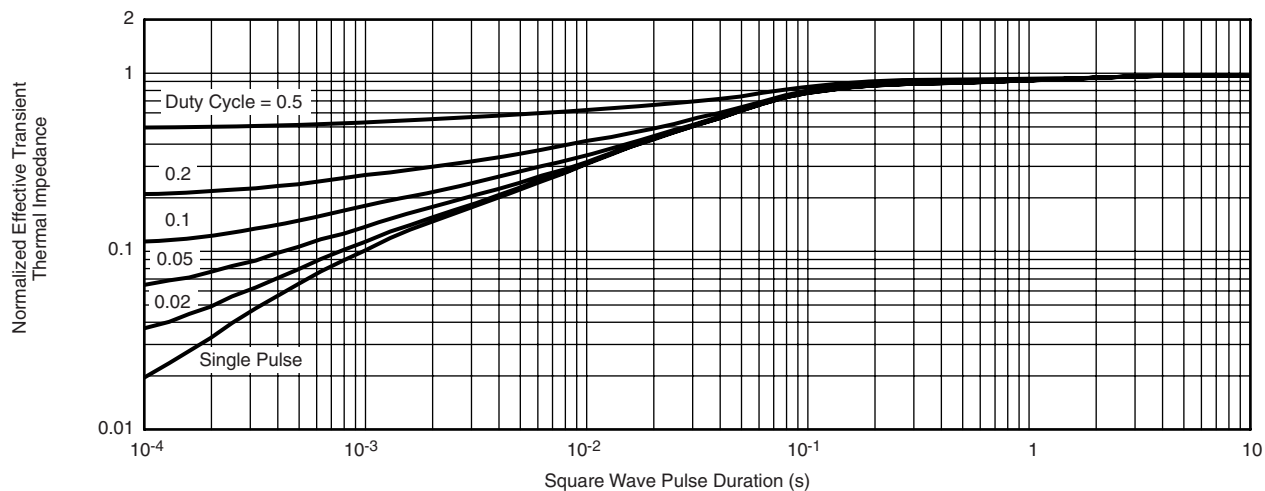
On-Resistance vs. Junction Temperature

TYPICAL CHARACTERISTICS $T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified**Safe Operating Area, Junction-to-Ambient**

TYPICAL CHARACTERISTICS $T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max)} = 150\text{ }^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS $T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted

Normalized Thermal Transient Impedance, Junction-to-Ambient

Normalized Thermal Transient Impedance, Junction-to-Foot

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