

P-Channel 20-V (D-S) MOSFET with Schottky Diode

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
- 20	0.200 at $V_{GS} = - 4.5$ V	± 1.8
	0.340 at $V_{GS} = - 2.5$ V	± 1.3

SCHOTTKY PRODUCT SUMMARY

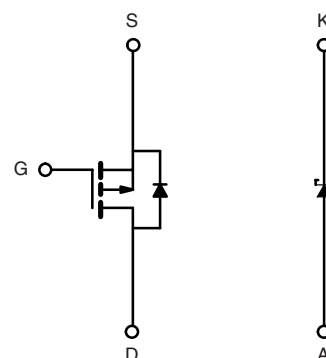
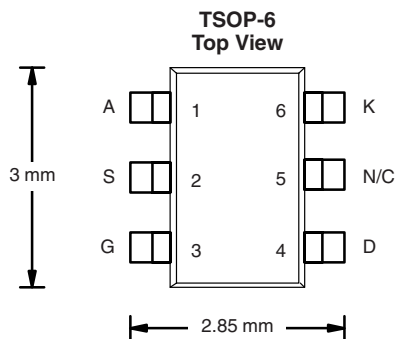
V_{KA} (V)	V_F (V) Diode Forward Voltage	I_F (A)
20	0.48 V at 0.5 A	0.5

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- LITTLE FOOT® Plus
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE
Available



P-Channel MOSFET

Ordering Information: Si3853DV-T1-E3 (Lead (Pb)-free)
Si3853DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted

Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage (MOSFET and Schottky)		V _{DS}	- 20		V
Reverse Voltage (Schottky)		V _{KA}	20		
Gate-Source Voltage (MOSFET)		V _{GS}	± 12	± 12	
Continuous Drain Current (T _J = 150 °C) (MOSFET) ^a	T _A = 25 °C	I _D	± 1.8	± 1.6	A
	T _A = 70 °C		± 1.5	± 1.2	
Pulsed Drain Current (MOSFET)		I _{DM}	± 7		
Continuous Source Current (MOSFET Diode Conduction) ^a		I _S	- 1.05	- 0.75	
Average Forward Current (Schottky)		I _F	0.5		
Pulsed Foward Current (Schottky)		I _{FM}	7		
Maximum Power Dissipation (MOSFET) ^a	T _A = 25 °C	P _D	1.15	0.83	W
	T _A = 70 °C		0.73	0.53	
Maximum Power Dissipation (Schottky) ^a	T _A = 25 °C		1.0	0.76	
	T _A = 70 °C		0.64	0.48	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

Notes:

a. Surface mounted on 1" x 1" FR4 board.

THERMAL RESISTANCE RATINGS

Parameter		Device	Symbol	Typical	Maximum	Unit
Junction-to-Ambient ^a	$t \leq 5$ s	MOSFET	R_{thJA}	93	110	°C/W
		Schottky		103	125	
	Steady State	MOSFET		130	150	
		Schottky		140	165	
Junction-to-Foot	Steady State	MOSFET	R_{thJF}	75	90	
		MOSFET		80	95	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

MOSFET SPECIFICATIONS $T_J = 25$ °C, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -250$ μ A	- 0.5			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0$ V, $V_{GS} = \pm 12$ V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -16$ V, $V_{GS} = 0$ V			- 1	μ A
		$V_{DS} = -16$ V, $V_{GS} = 0$ V, $T_J = 75$ °C			- 10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq -5$ V, $V_{GS} = -4.5$ V	- 5			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -4.5$ V, $I_D = -1.8$ A		0.160	0.200	Ω
		$V_{GS} = -2.5$ V, $I_D = -1.0$ A		0.280	0.340	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -5$ V, $I_D = -1.8$ A		3.6		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.05$ A, $V_{GS} = 0$ V		- 0.83	- 1.10	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -10$ V, $V_{GS} = -4.5$ V, $I_D = -1.8$ A		2.7	4.0	nC
Gate-Source Charge	Q_{gs}			0.4		
Gate-Drain Charge	Q_{gd}			0.6		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10$ V, $R_L = 10$ Ω $I_D \approx -1$ A, $V_{GEN} = -4.5$ V, $R_g = 6$ Ω		11	17	ns
Rise Time	t_r			34	50	
Turn-Off Delay Time	$t_{d(off)}$			19	30	
Fall Time	t_f			24	36	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -1.05$ A, $dI/dt = 100$ A/ μ s		20	40	

Notes:

a. Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.

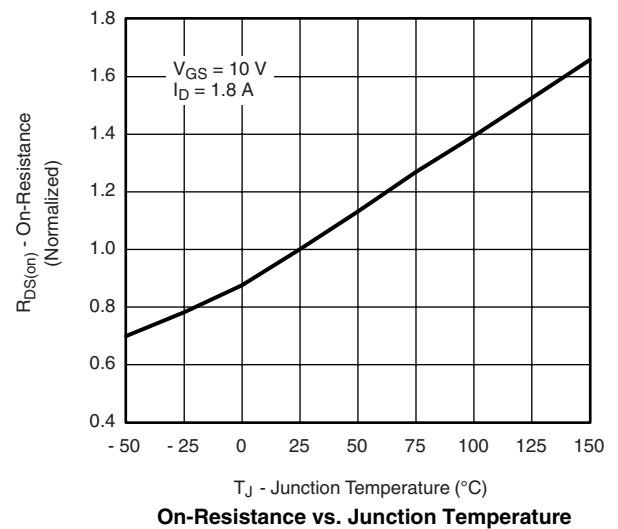
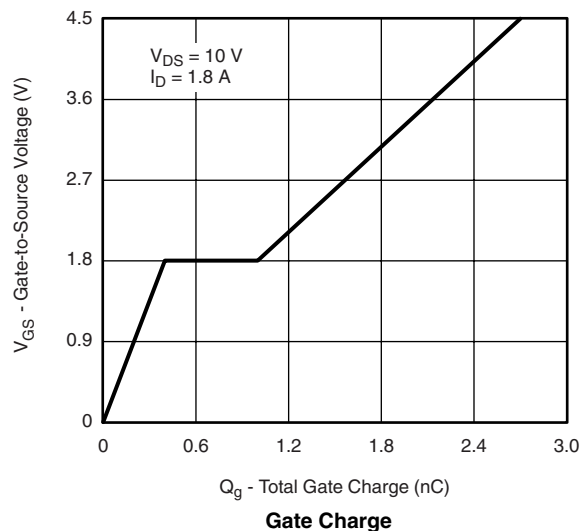
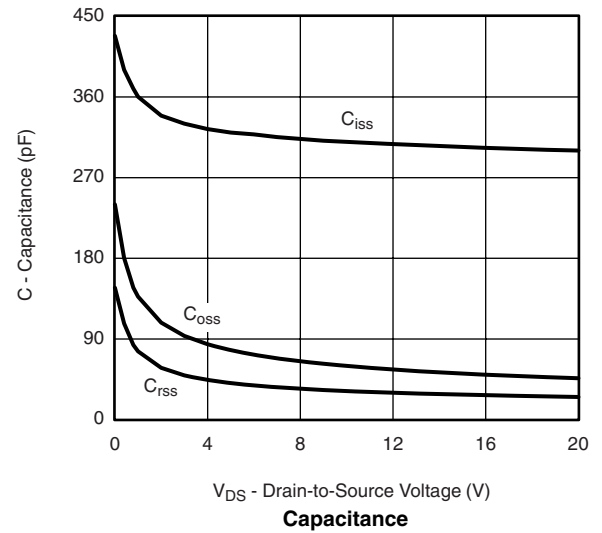
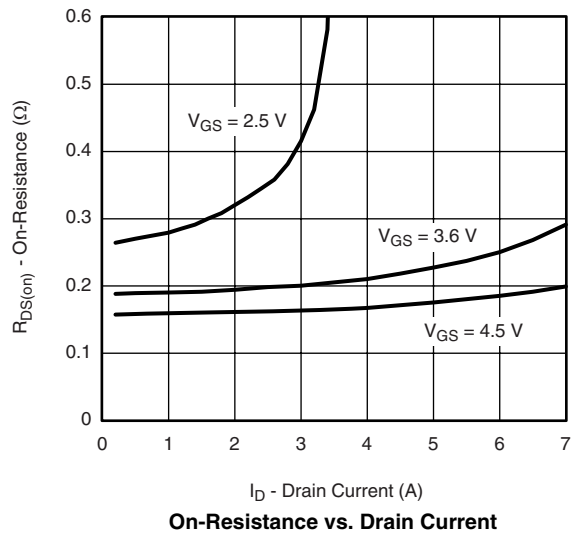
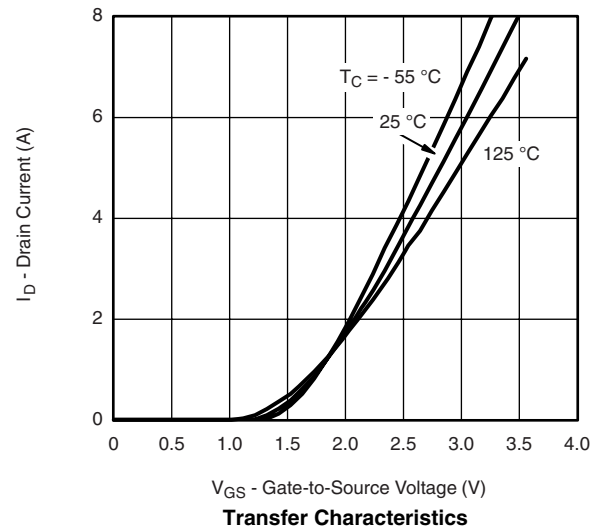
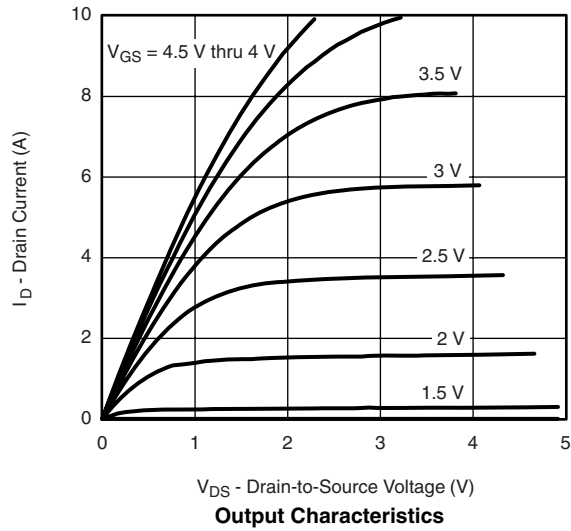
b. Guaranteed by design, not subject to production testing.

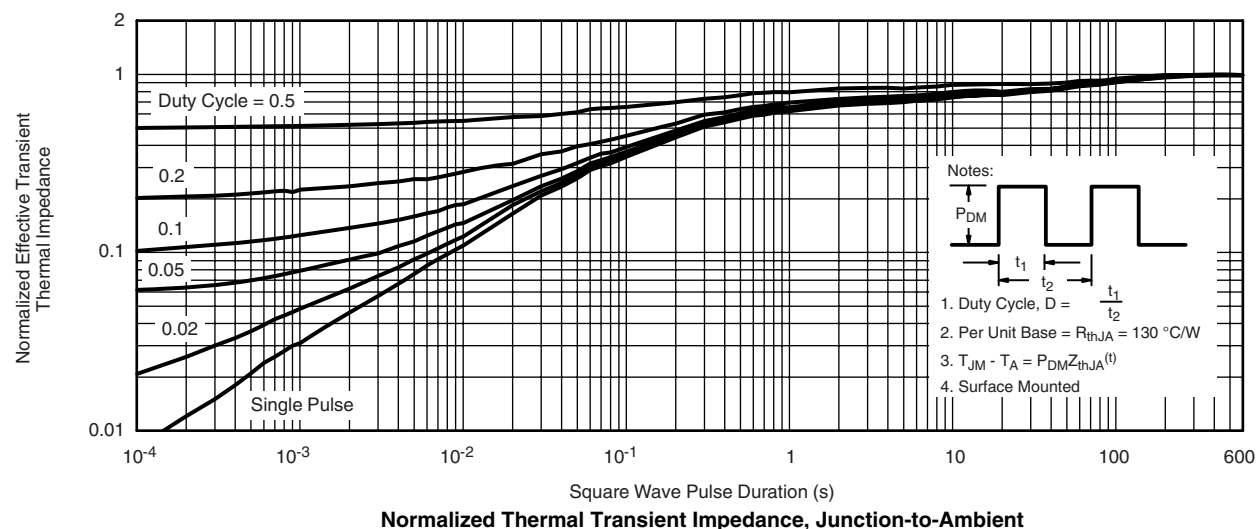
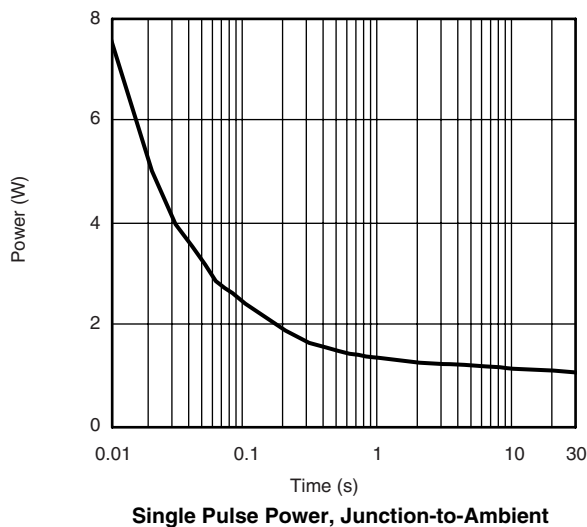
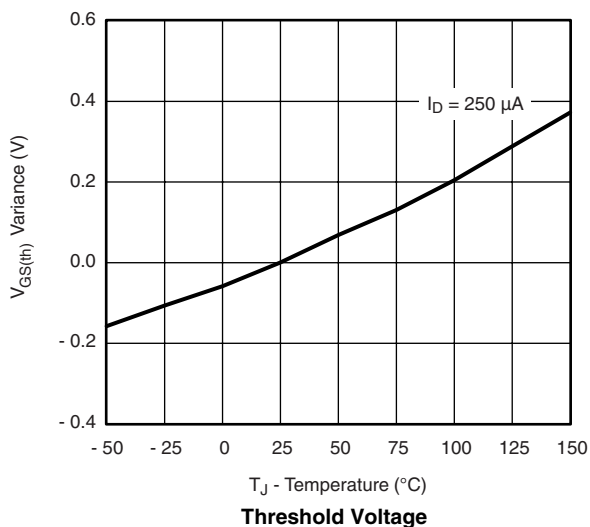
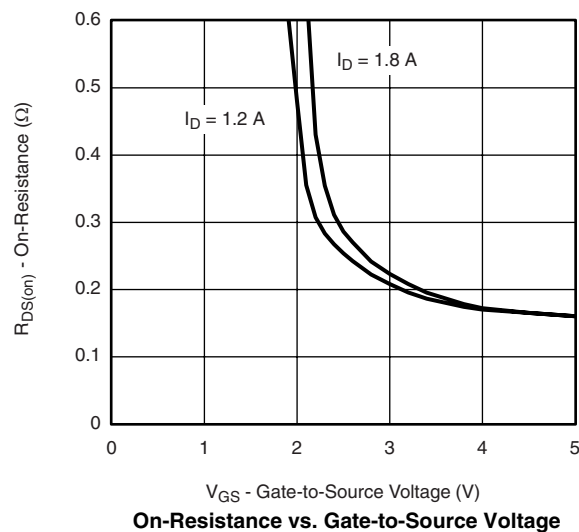
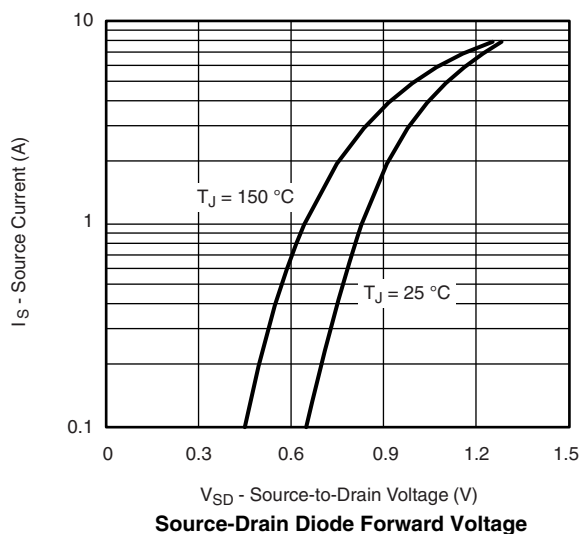
SCHOTTKY SPECIFICATIONS $T_J = 25$ °C, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Forward Voltage Drop	V_F	$I_F = 0.5$ A		0.42	0.48	V
		$I_F = 0.5$ A, $T_J = 125$ °C		0.33	0.4	
Maximum Reverse Leakage Current	I_{rm}	$V_R = 20$ V		0.002	0.100	mA
		$V_R = 20$ V, $T_J = 75$ °C		0.06	1	
		$V_R = 20$ V, $T_J = 125$ °C		1.5	10	
Junction Capacitance	C_T	$V_R = 10$ V		31		pF

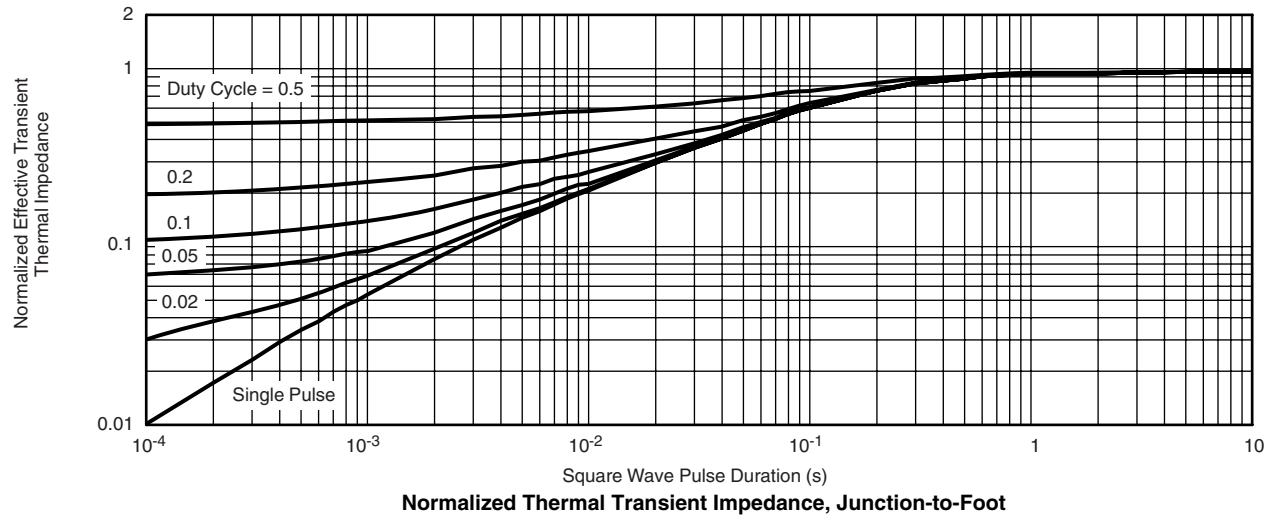
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

MOSFET TYPICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$, unless otherwise noted

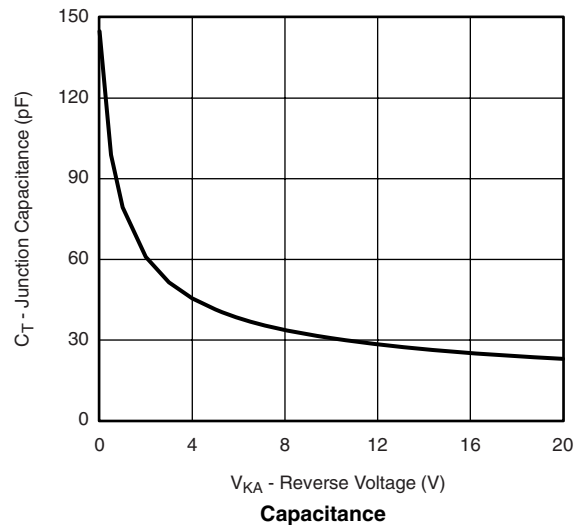
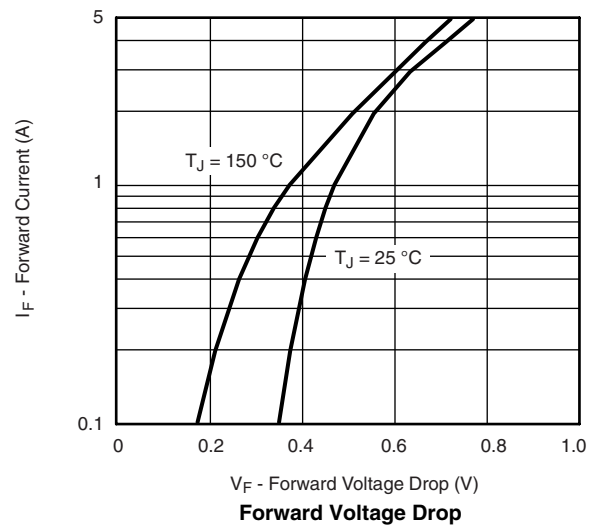
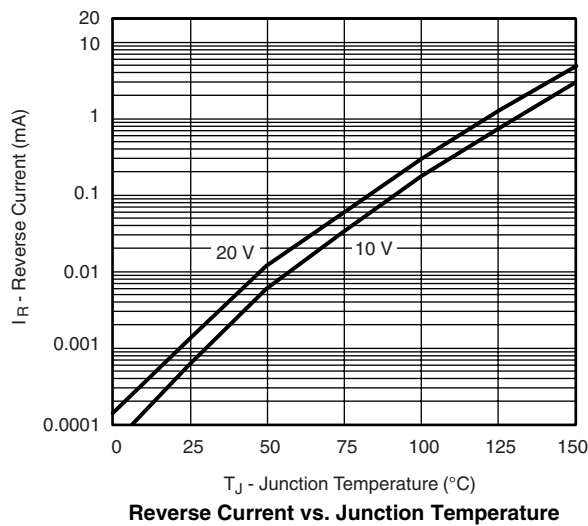


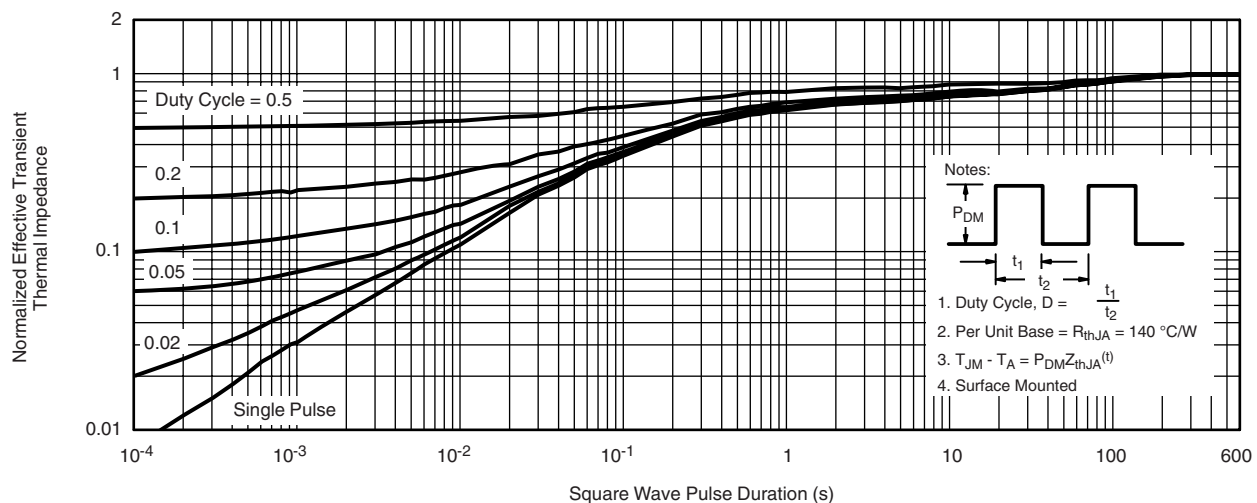
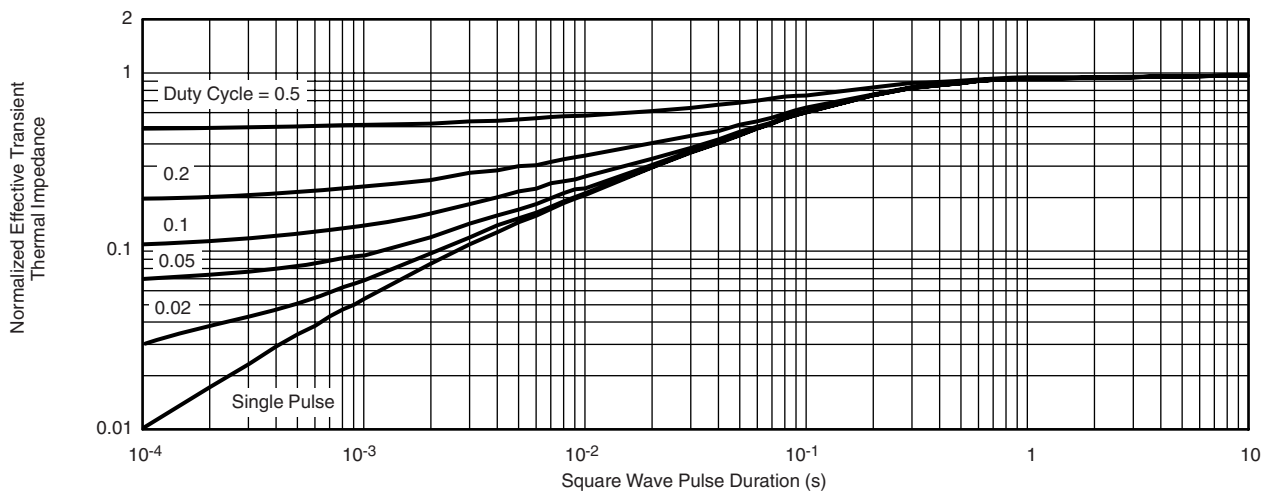
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Normalized Thermal Transient Impedance, Junction-to-Ambient

Normalized Thermal Transient Impedance, Junction-to-Foot

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