



# **Dual P-Channel 20-V (D-S) MOSFET**

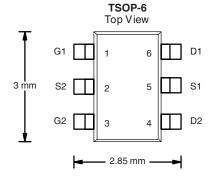
PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	
- 20	0.200 at $V_{GS} = -4.5 \text{ V}$	± 1.8	
	0.235 at V <sub>GS</sub> = - 3.6 V	± 1.6	
	0.340 at V <sub>GS</sub> = - 2.5 V	± 1.3	

### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs: 2.5 V Rated
- Compliant to RoHS Directive 2002/95/EC

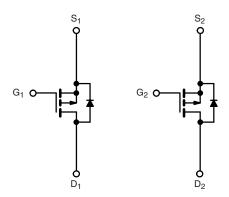


**FREE** 



Ordering Information: Si3909DV-T1-E3 (Lead (Pb)-free)

Si3909DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	$\Gamma_A = 25  ^{\circ}\text{C}$ , unles	ss otherwise not	ed		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 20	V	
Gate-Source Voltage		V <sub>GS</sub> ± 12		v	
Continuous Drain Comment /T 150 °C\A h	T <sub>A</sub> = 25 °C	ı	± 1.8		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a, b</sup>	T <sub>A</sub> = 70 °C	I <sub>D</sub>	± 1.2		
Pulsed Drain Current		I <sub>DM</sub>	± 7	A	
Continuous Diode Current (Diode Conduction) <sup>a, b</sup>		I <sub>S</sub>	- 1.05		
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.15	w	
Maximum Power Dissipation <sup>a, b</sup>	T <sub>A</sub> = 70 °C	' D	0.73	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian una lumation ta Anabianta	t ≤ 5 s	R <sub>thJA</sub>	93	110	°C/W	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		130	150		
Maximum Junction-to-Lead	Steady State		75	90		

Notes:

a. Surface Mounted on FR4 board.

 $b.\ t \leq 5\ s.$ 

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Parameter	Symbol	Test Conditions Mi		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.5			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 16 V, V <sub>GS</sub> = 0 V	' <sub>DS</sub> = - 16 V, V <sub>GS</sub> = 0 V		- 1	μΑ	
		V <sub>DS</sub> = - 16 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le$ - 5 V, $V_{GS}$ = - 4.5 V	- 5			Α	
		$V_{GS} = -4.5 \text{ V}, I_D = -1.8 \text{ A}$		0.160	0.200	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -3.6 \text{ V}, I_D = -1.6 \text{ A}$		0.190	0.235		
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 1 A		0.280	0.340		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 1.8 A		3.6		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.05 A, V <sub>GS</sub> = 0 V		- 0.83	- 1.1	V	
Dynamic <sup>b</sup>				•			
Total Gate Charge	$Q_g$			2.7	4.0		
Gate-Source Charge	$Q_{gs}$	$Q_{gs}$ $V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -1.8 \text{ A}$		0.4		nC	
Gate-Drain Charge	$Q_{gd}$			0.6		1	
Turn-On Delay Time	t <sub>d(on)</sub>			11	17		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 10 $\Omega$		34	50		
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong$ - 1 A, $\text{V}_\text{GEN}$ = - 4.5 V, $\text{R}_\text{g}$ = 6 $\Omega$		19	30	ns	
Fall Time	t <sub>f</sub>			24	36		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.05 A, dI/dt = 100 A/μs		20	40		

#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

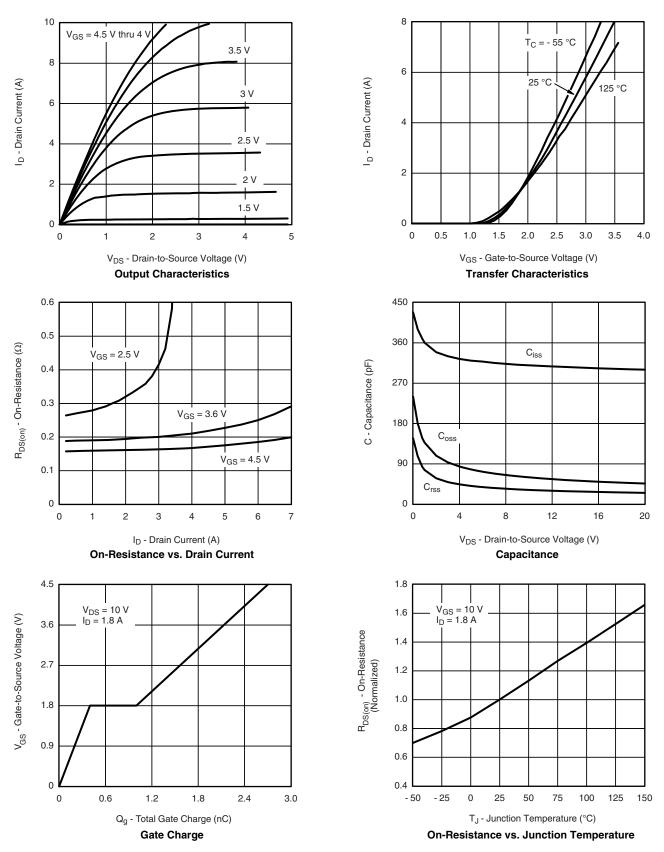
b. Guaranteed by design, not subject to production testing.







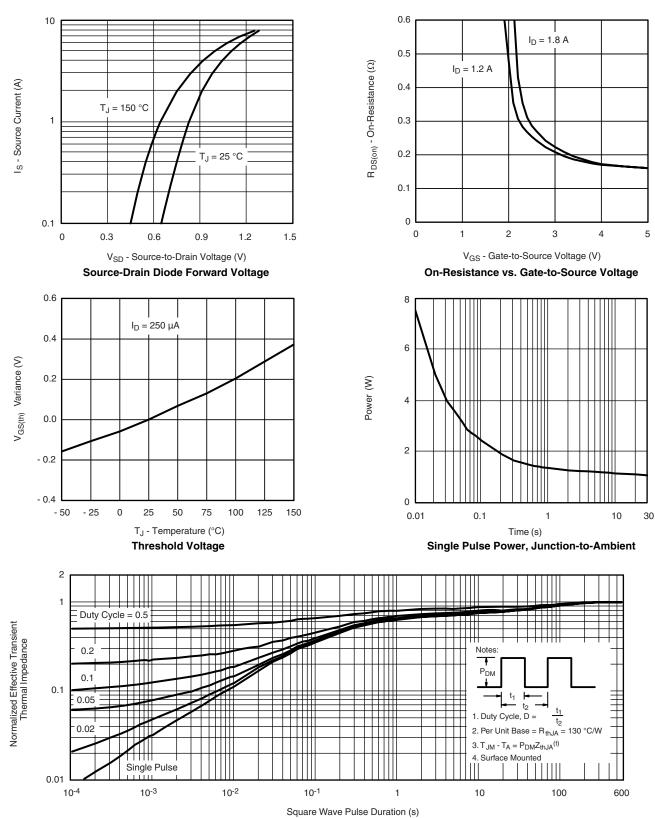
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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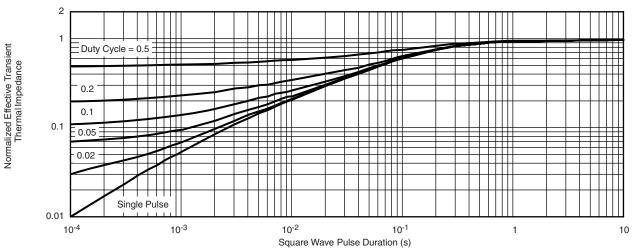
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppq?70968">www.vishay.com/ppq?70968</a>.



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Revision: 02-Oct-12 Document Number: 91000