

## N-Channel 30-V (D-S) MOSFET

### PRODUCT SUMMARY

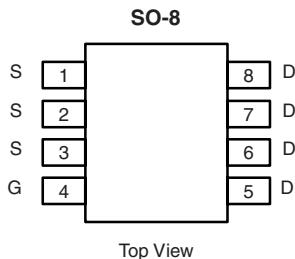
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
30	0.0135 at $V_{GS} = 10$ V	10
	0.020 at $V_{GS} = 4.5$ V	8

### FEATURES

- Halogen-free According to IEC 61249-2-21
- Definition
- TrenchFET® Power MOSFET
- 100 %  $R_g$  Tested
- Compliant to RoHS Directive 2002/95/EC

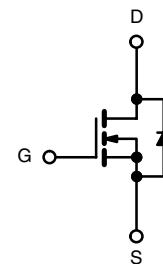


**RoHS**  
COMPLIANT  
**HALOGEN**  
**FREE**  
Available



Top View

**Ordering Information:** Si4410BDY-T1-E3 (Lead (Pb)-free)  
Si4410BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

### APPLICATIONS

- Battery Switch
- Load Switch

### ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	10 s	Steady State	Unit
Drain-Source Voltage	$V_{DS}$	30	$\pm 20$	V
Gate-Source Voltage	$V_{GS}$			
Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup>	$I_D$ ( $T_A = 25$ °C)	10	7.5	A
	$I_D$ ( $T_A = 70$ °C)	8	6	
Pulsed Drain Current (10 $\mu$ s Pulse Width)	$I_{DM}$	50		
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	2.3	1.26	
Maximum Power Dissipation <sup>a</sup>	$P_D$ ( $T_A = 25$ °C)	2.5	1.4	W
	$P_D$ ( $T_A = 70$ °C)	1.6	0.9	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		°C

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10$ s	$R_{thJA}$	40	50
	Steady State		70	90
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	25	30	°C/W

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

**SPECIFICATIONS**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

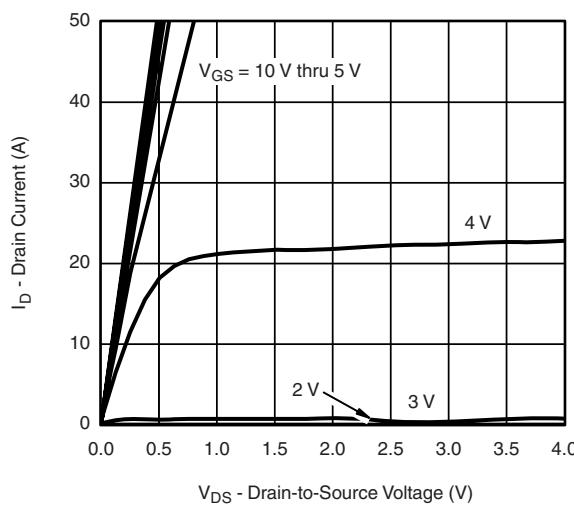
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$ , $I_D = 250 \mu\text{A}$	1.0		3.0	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}$ , $V_{GS} = \pm 20 \text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30 \text{ V}$ , $V_{GS} = 0 \text{ V}$		1		$\mu\text{A}$
		$V_{DS} = 30 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $T_J = 55^\circ\text{C}$		5		
		$V_{DS} \geq 5 \text{ V}$ , $V_{GS} = 10 \text{ V}$	20			
On-State Drain Current <sup>a</sup>	$I_{D(\text{on})}$	$V_{GS} = 10 \text{ V}$ , $I_D = 10 \text{ A}$		0.011	0.0135	$\Omega$
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(\text{on})}$	$V_{GS} = 4.5 \text{ V}$ , $I_D = 5 \text{ A}$		0.0165	0.020	
		$V_{DS} = 15 \text{ V}$ , $I_D = 10 \text{ A}$		25		S
Forward Transconductance <sup>a</sup>	$g_{fs}$	$I_S = 2.3 \text{ A}$ , $V_{GS} = 0 \text{ V}$		0.76	1.1	V
<b>Dynamic<sup>b</sup></b>						
Gate Charge	$Q_g$	$V_{DS} = 15 \text{ V}$ , $V_{GS} = 5 \text{ V}$ , $I_D = 10 \text{ A}$		13	20	nC
Total Gate Charge	$Q_{gt}$	$V_{DS} = 15 \text{ V}$ , $V_{GS} = 10 \text{ V}$ , $I_D = 10 \text{ A}$		25	40	
Gate-Source Charge	$Q_{gs}$			5.5		
Gate-Drain Charge	$Q_{gd}$			3.7		
Gate Resistance	$R_g$	$f = 1 \text{ MHz}$	0.5	1.6	2.7	$\Omega$
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = 25 \text{ V}$ , $R_L = 25 \Omega$ $I_D \geq 1 \text{ A}$ , $V_{GEN} = 10 \text{ V}$ , $R_g = 6 \Omega$		10	15	ns
Rise Time	$t_r$			10	15	
Turn-Off Delay Time	$t_{d(\text{off})}$			40	60	
Fall Time	$t_f$			15	25	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = 2.3 \text{ A}$ , $di/dt = 100 \text{ A}/\mu\text{s}$		35	70	

Notes:

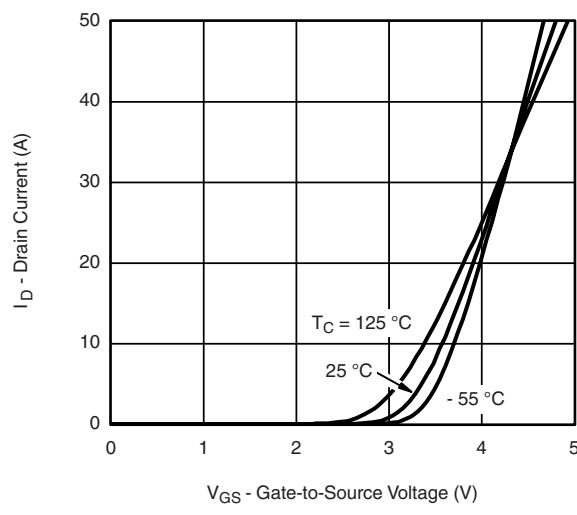
a. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .

b. Guaranteed by design, not subject to production testing.

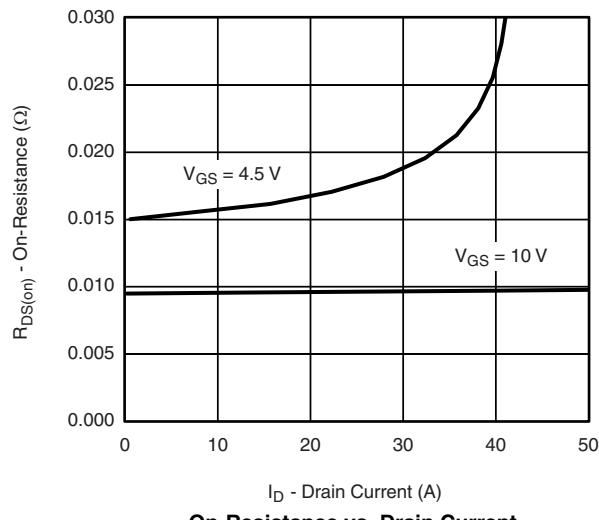
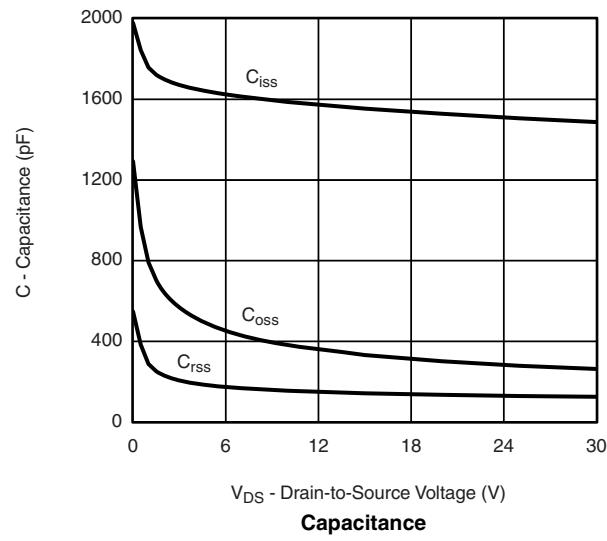
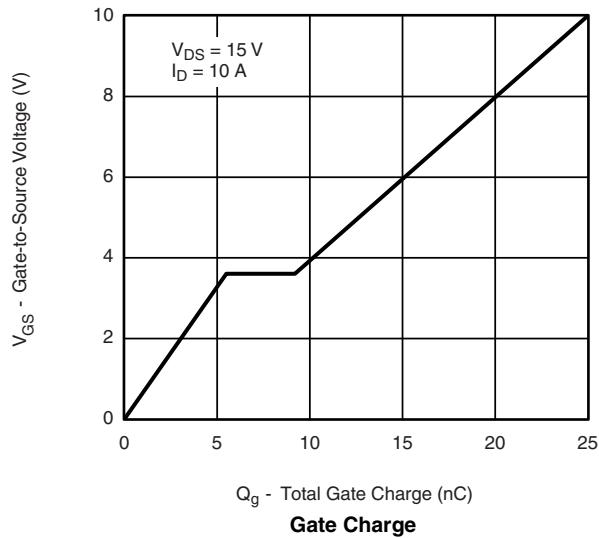
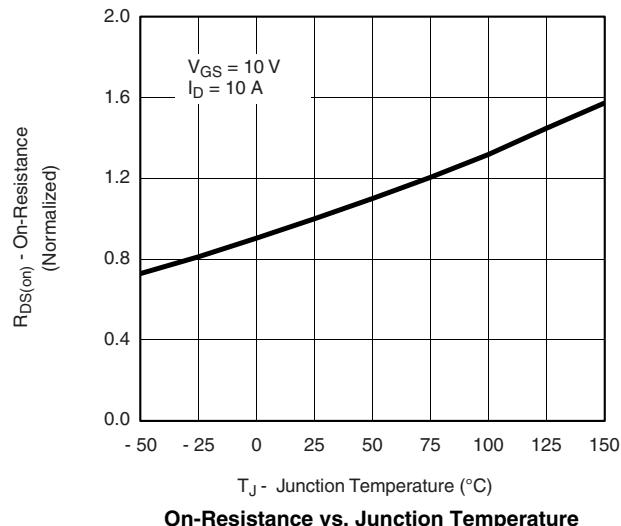
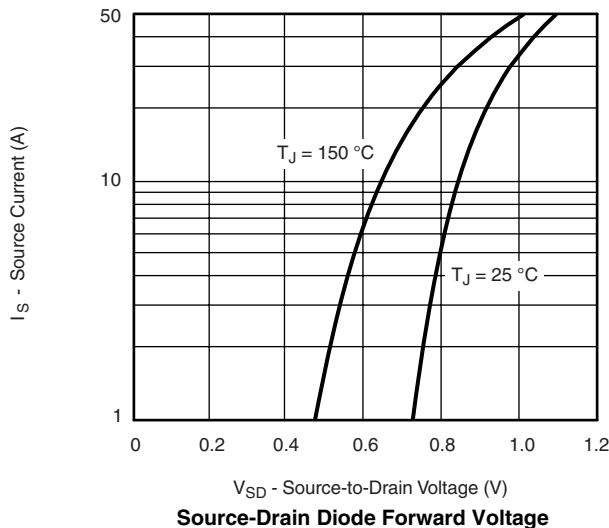
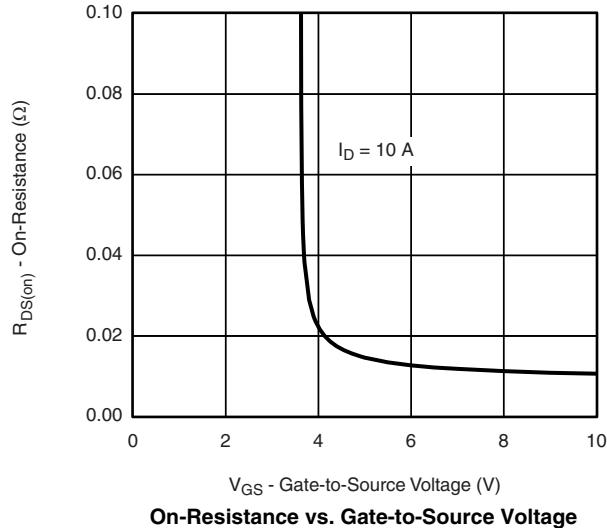
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

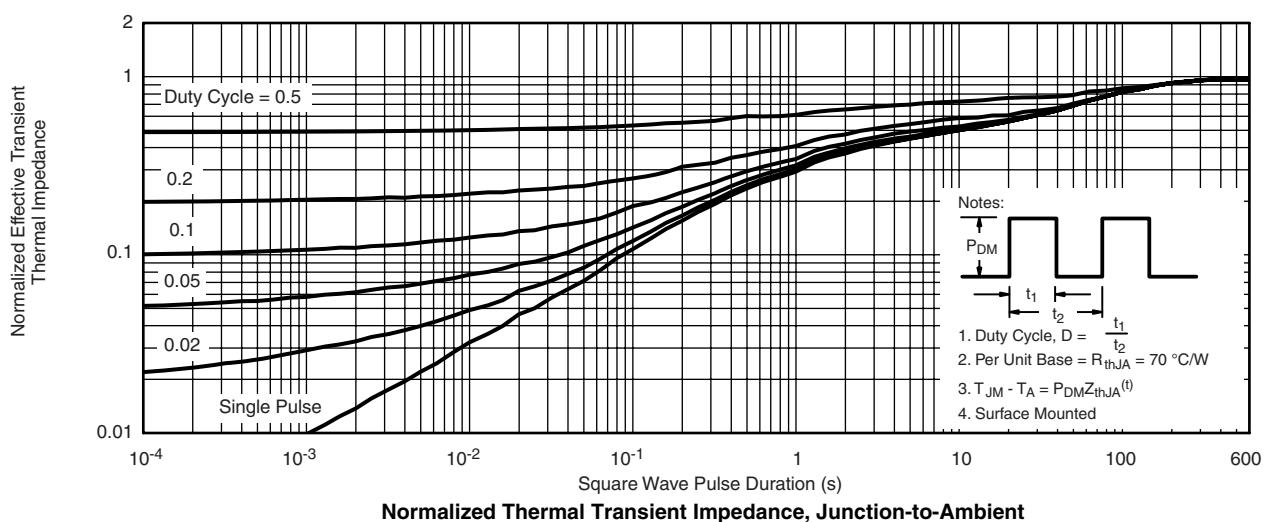
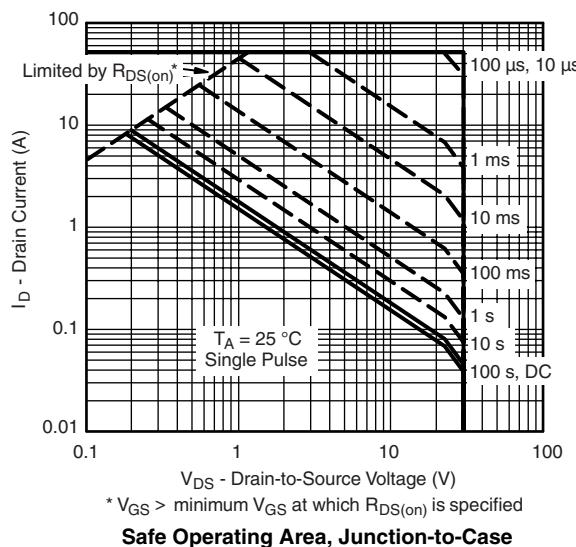
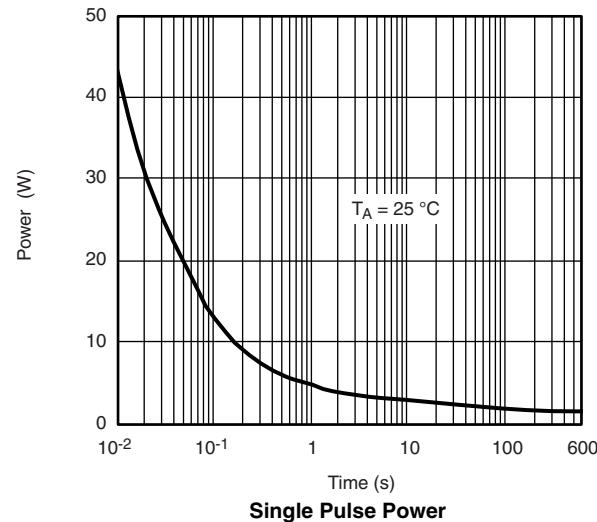
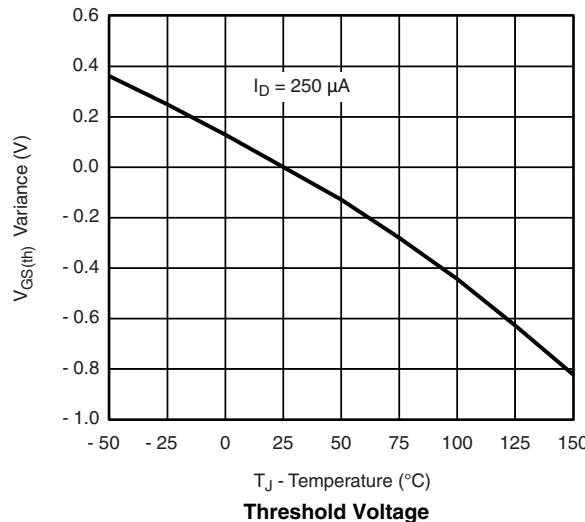
**TYPICAL CHARACTERISTICS**  $25^\circ\text{C}$ , unless otherwise noted

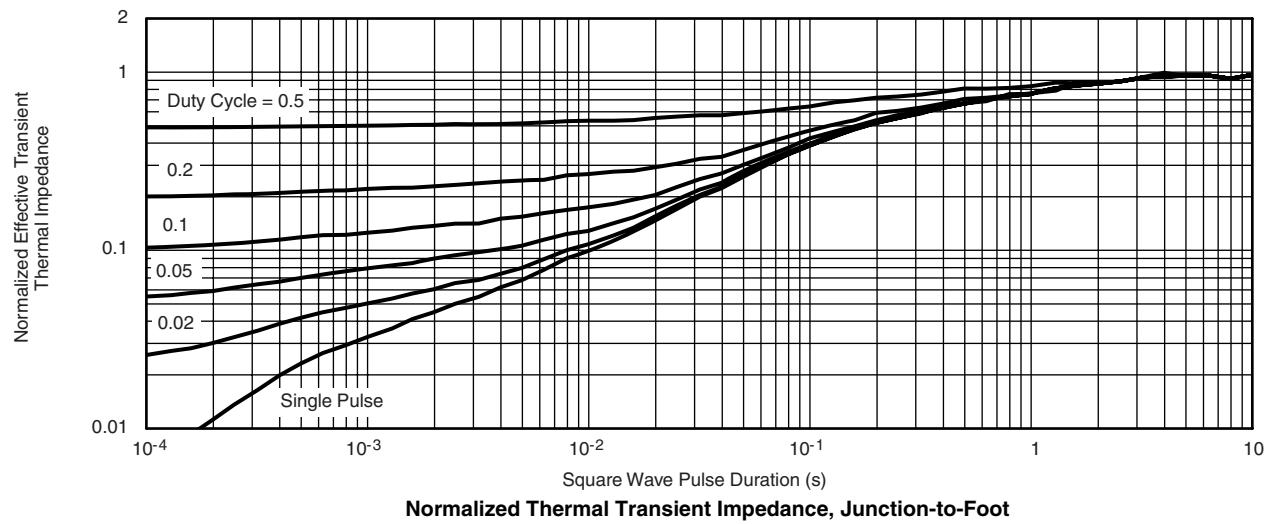
Output Characteristics



Transfer Characteristics

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

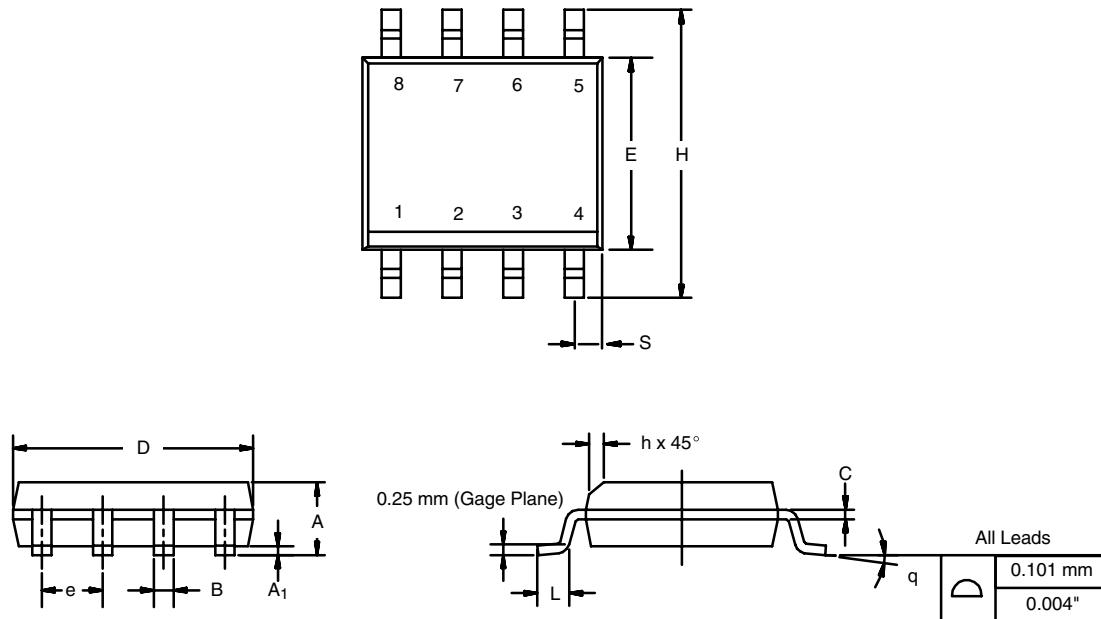
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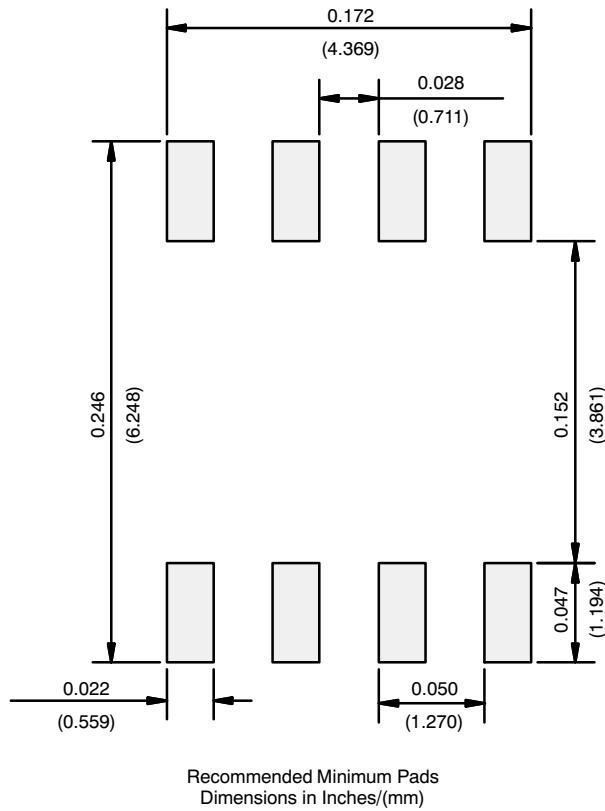
### SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A <sub>1</sub>	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

## RECOMMENDED MINIMUM PADS FOR SO-8



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