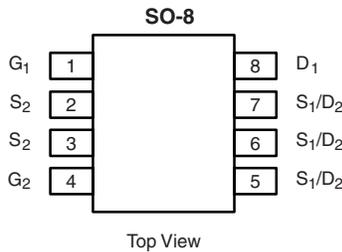


## Dual N-Channel 30-V (D-S) MOSFET with Schottky Diode

PRODUCT SUMMARY				
	V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
Channel-1	30	0.017 at V <sub>GS</sub> = 10 V	8.0	12.5
		0.0195 at V <sub>GS</sub> = 4.5 V	7.5	
Channel-2	30	0.010 at V <sub>GS</sub> = 10 V	15.2	17
		0.0115 at V <sub>GS</sub> = 4.5 V	14.1	

SCHOTTKY PRODUCT SUMMARY		
V <sub>DS</sub> (V)	V <sub>SD</sub> (V) Diode Forward Voltage	I <sub>F</sub> (A) <sup>a</sup>
30	0.43 V at 1.0 A	3.8



Ordering Information: Si4618DY-T1-E3 (Lead (Pb)-free)  
Si4618DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

### FEATURES

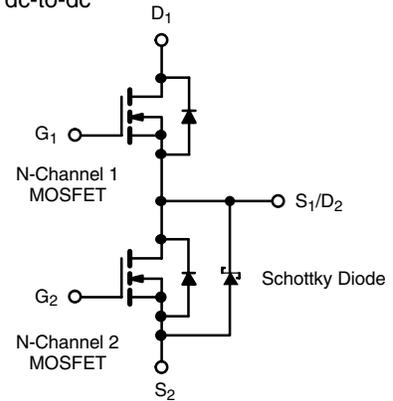
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
Available

### APPLICATIONS

- Notebook Logic dc-to-dc
- Low Current dc-to-dc



ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted				
Parameter	Symbol	Channel-1	Channel-2	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	30	V
Gate-Source Voltage	V <sub>GS</sub>	± 16	± 16	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	I <sub>D</sub>	T <sub>C</sub> = 25 °C	8.0	15.2
		T <sub>C</sub> = 70 °C	6.4	12.1
		T <sub>A</sub> = 25 °C	6.7 <sup>b, c</sup>	11.4 <sup>b, c</sup>
		T <sub>A</sub> = 70 °C	5.4 <sup>b, c</sup>	9.1 <sup>b, c</sup>
Pulsed Drain Current (10 μs Pulse Width)	I <sub>DM</sub>	35	60	A
Source-Drain Current Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	1.8	3.8
		T <sub>A</sub> = 25 °C	1.25 <sup>b, c</sup>	2.4 <sup>b, c</sup>
Pulsed Source-Drain Current	I <sub>SM</sub>	35	35	
Single Pulse Avalanche Current	I <sub>AS</sub>	15	15	
Single Pulse Avalanche Energy	E <sub>AS</sub>	11.2	11.2	mJ
Maximum Power Dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C	1.98	4.16
		T <sub>C</sub> = 70 °C	1.26	2.66
		T <sub>A</sub> = 25 °C	1.38 <sup>b, c</sup>	2.35 <sup>b, c</sup>
		T <sub>A</sub> = 70 °C	0.88 <sup>b, c</sup>	1.5 <sup>b, c</sup>
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Channel-1		Channel-2		Unit	
		Typ.	Max.	Typ.	Max.		
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	72	90	43	53	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	51	63	25	30	

Notes:

- Based on T<sub>C</sub> = 25 °C.
- Surface Mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under Steady State conditions is 125 °C/W (Channel-1) and 100 °C/W (Channel-2).

SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted							
Parameter	Symbol	Test Conditions		Min.	Typ. <sup>a</sup>	Max.	Unit
<b>Static</b>							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$	Ch-1	30			V
		$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$	Ch-2	30			
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$	Ch-1		35		
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250\text{ }\mu\text{A}$	Ch-1		-6		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 1\text{ mA}$	Ch-1	1		2.5	
		$V_{DS} = V_{GS}, I_D = 1\text{ mA}$	Ch-2	1		2.5	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 16\text{ V}$	Ch-1			100	$\mu\text{A}$
		$V_{DS} = 0\text{ V}, V_{GS} = \pm 16\text{ V}$	Ch-2			100	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$	Ch-1			0.001	mA
		$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$	Ch-2		0.05	0.5	
		$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, T_J = 100\text{ }^\circ\text{C}$	Ch-1			0.025	
		$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, T_J = 100\text{ }^\circ\text{C}$	Ch-2		3	15	
On-State Drain Current <sup>b</sup>	$I_{D(on)}$	$V_{DS} = 5\text{ V}, V_{GS} = 10\text{ V}$	Ch-1	20			A
		$V_{DS} = 5\text{ V}, V_{GS} = 10\text{ V}$	Ch-2	20			
Drain-Source On-State Resistance <sup>b</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 8\text{ A}$	Ch-1		0.014	0.017	$\Omega$
		$V_{GS} = 10\text{ V}, I_D = 8\text{ A}$	Ch-2		0.0083	0.010	
		$V_{GS} = 4.5\text{ V}, I_D = 5\text{ A}$	Ch-1		0.016	0.0195	
		$V_{GS} = 4.5\text{ V}, I_D = 5\text{ A}$	Ch-2		0.0095	0.0115	
Forward Transconductance <sup>b</sup>	$g_{fs}$	$V_{DS} = 15\text{ V}, I_D = 8\text{ A}$	Ch-1		40		S
		$V_{DS} = 15\text{ V}, I_D = 8\text{ A}$	Ch-2		47		
<b>Dynamic<sup>a</sup></b>							
Input Capacitance	$C_{iss}$	Channel-1 $V_{DS} = 15\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	Ch-1		1535		pF
Output Capacitance	$C_{oss}$		Ch-2		2290		
Reverse Transfer Capacitance	$C_{rss}$	Channel-2 $V_{DS} = 15\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	Ch-1		205		
			Ch-2		360		
Total Gate Charge	$Q_g$	$V_{DS} = 15\text{ V}, V_{GS} = 10\text{ V}, I_D = 8\text{ A}$	Ch-1		29	44	nC
		$V_{DS} = 15\text{ V}, V_{GS} = 10\text{ V}, I_D = 8\text{ A}$	Ch-2		39	59	
		Channel-1 $V_{DS} = 15\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 8\text{ A}$	Ch-1		12.5	19	
			Ch-2		17	26	
Gate-Source Charge	$Q_{gs}$	Channel-2 $V_{DS} = 15\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 8\text{ A}$	Ch-1		4.1		
Gate-Drain Charge	$Q_{gd}$		Ch-2		5.6		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$	Ch-1		1.8	3.0	$\Omega$
			Ch-2		1.9	3.0	



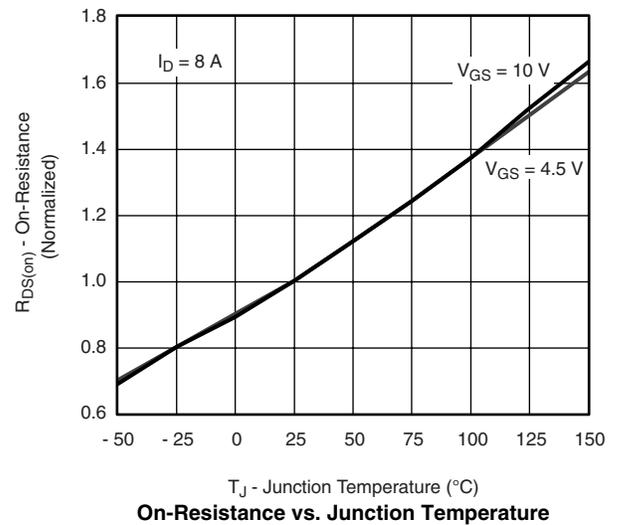
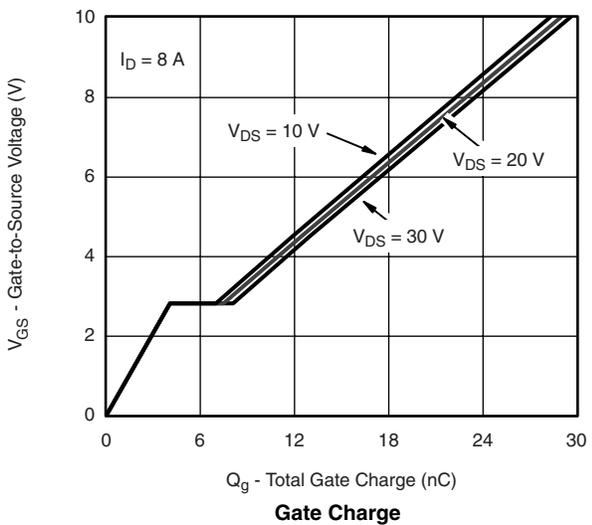
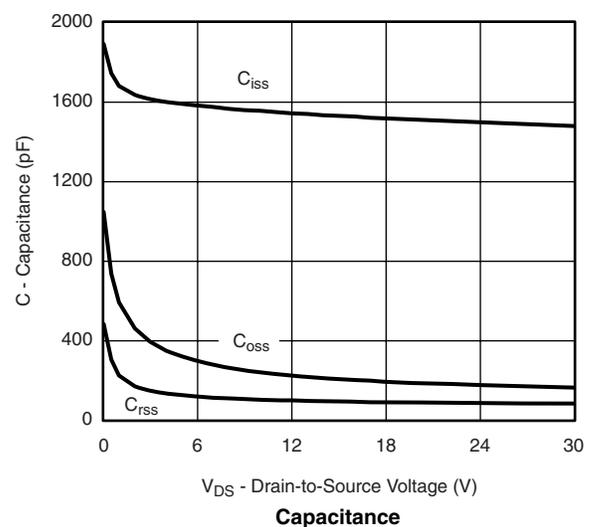
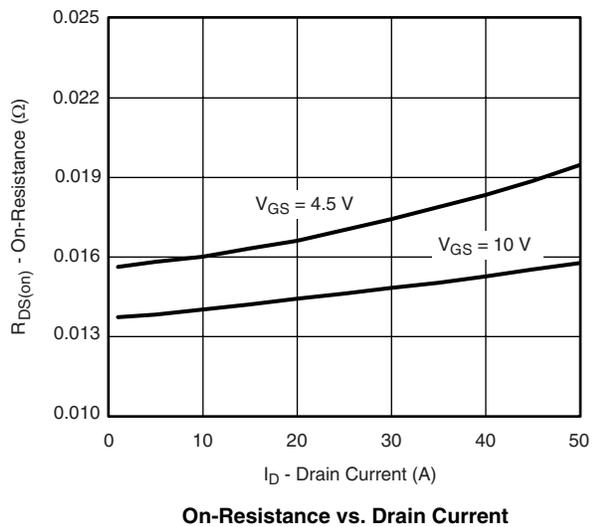
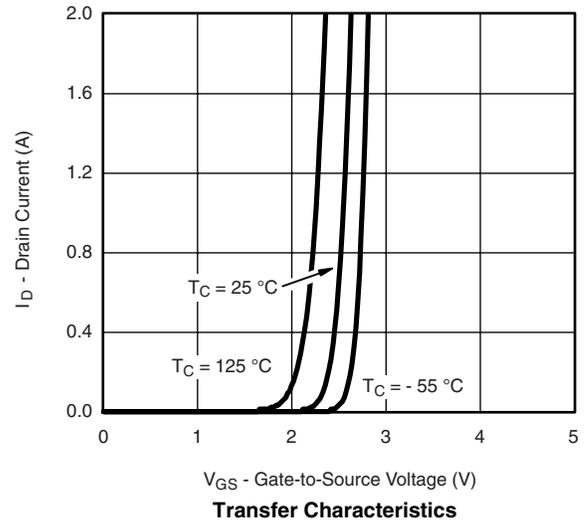
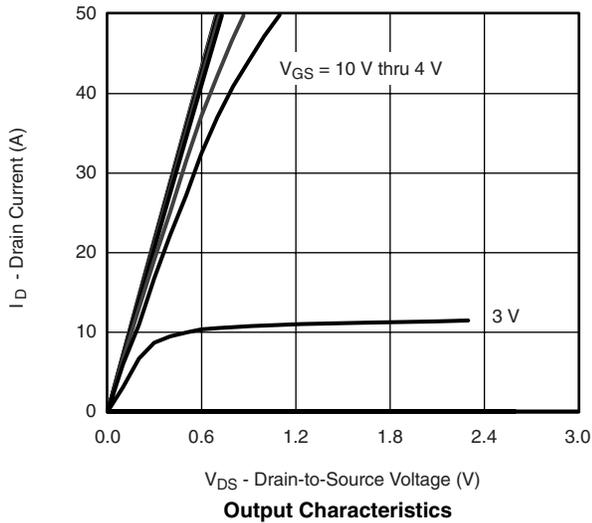
<b>SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit	
<b>Dynamic<sup>a</sup></b>							
Turn-On Delay Time	$t_{d(on)}$	Channel-1 $V_{DD} = 15\text{ V}$ , $R_L = 3\ \Omega$ $I_D \cong 5\text{ A}$ , $V_{GEN} = 10\text{ V}$ , $R_g = 1\ \Omega$	Ch-1		8	15	ns
Rise Time	$t_r$		Ch-2		9	16	
Turn-Off Delay Time	$t_{d(off)}$	Channel-2 $V_{DD} = 15\text{ V}$ , $R_L = 3\ \Omega$ $I_D \cong 5\text{ A}$ , $V_{GEN} = 10\text{ V}$ , $R_g = 1\ \Omega$	Ch-1		20	30	
Fall Time	$t_f$		Ch-2		26	39	
Turn-On Delay Time	$t_{d(on)}$	Channel-1 $V_{DD} = 15\text{ V}$ , $R_L = 3\ \Omega$ $I_D \cong 5\text{ A}$ , $V_{GEN} = 4.5\text{ V}$ , $R_g = 1\ \Omega$	Ch-1		24	36	
Rise Time	$t_r$		Ch-2		24	36	
Turn-Off Delay Time	$t_{d(off)}$	Channel-2 $V_{DD} = 15\text{ V}$ , $R_L = 3\ \Omega$ $I_D \cong 5\text{ A}$ , $V_{GEN} = 4.5\text{ V}$ , $R_g = 1\ \Omega$	Ch-1		87	130	
Fall Time	$t_f$		Ch-2		97	145	
Turn-On Delay Time	$t_{d(on)}$	Channel-1 $V_{DD} = 15\text{ V}$ , $R_L = 3\ \Omega$ $I_D \cong 5\text{ A}$ , $V_{GEN} = 4.5\text{ V}$ , $R_g = 1\ \Omega$	Ch-1		30	45	
Rise Time	$t_r$		Ch-2		35	53	
Turn-Off Delay Time	$t_{d(off)}$	Channel-2 $V_{DD} = 15\text{ V}$ , $R_L = 3\ \Omega$ $I_D \cong 5\text{ A}$ , $V_{GEN} = 4.5\text{ V}$ , $R_g = 1\ \Omega$	Ch-1		34	51	
Fall Time	$t_f$		Ch-2		45	68	
<b>Drain-Source Body Diode Characteristics</b>							
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$	Ch-1			1.8	A
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$		Ch-2			3.8	
Body Diode Voltage	$V_{SD}$	$I_S = 2\text{ A}$	Ch-1		0.77	1.1	V
		$I_S = 1\text{ A}$	Ch-2		0.37	0.43	
Body Diode Reverse Recovery Time	$t_{rr}$	Channel-1 $I_F = 4\text{ A}$ , $dI/dt = 100\text{ A}/\mu\text{s}$ , $T_J = 25\text{ }^\circ\text{C}$	Ch-1		22	33	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$		Ch-2		26	39	
Reverse Recovery Fall Time	$t_a$	Channel-2 $I_F = 4\text{ A}$ , $dI/dt = 100\text{ A}/\mu\text{s}$ , $T_J = 25\text{ }^\circ\text{C}$	Ch-1		15	23	nC
Reverse Recovery Rise Time	$t_b$		Ch-2		15	23	
			Ch-1		13		ns
			Ch-2		13		

Notes:

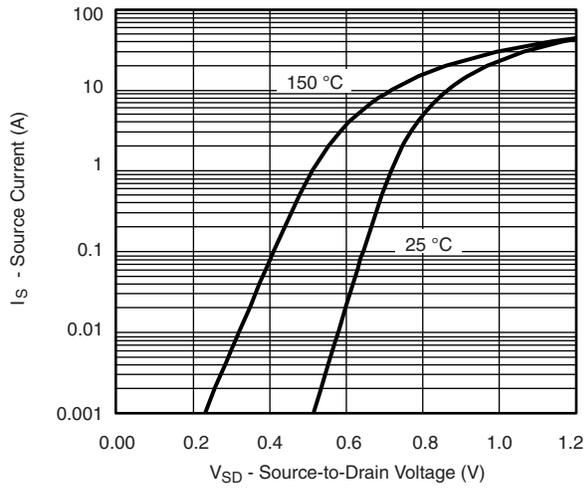
- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width  $\leq 300\ \mu\text{s}$ , duty cycle  $\leq 2\%$ .

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

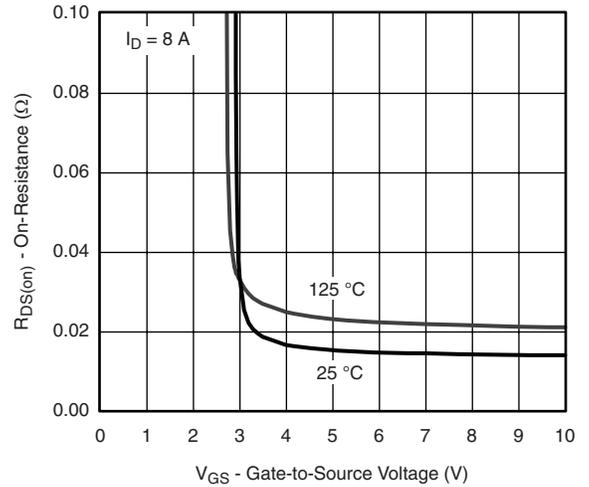
## CHANNEL-1 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



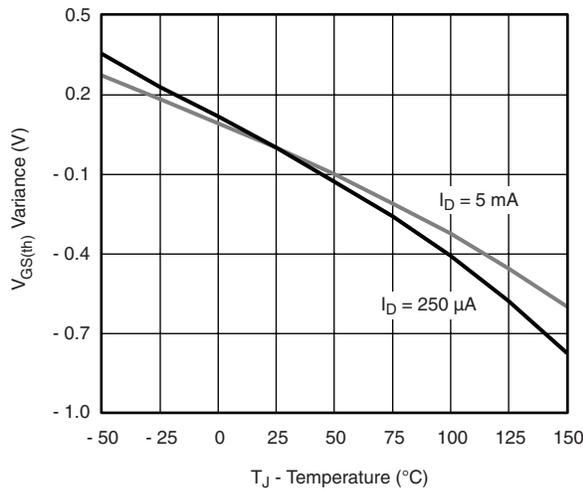
**CHANNEL-1 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



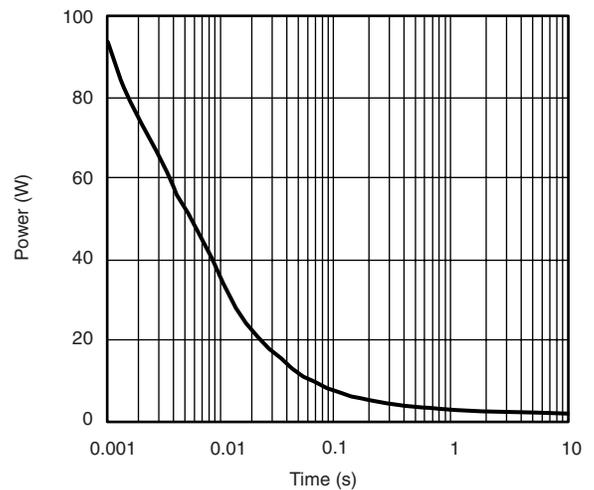
**Source-Drain Diode Forward Voltage**



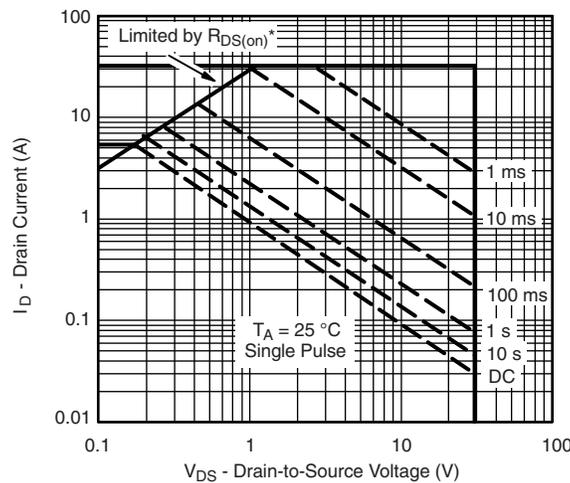
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**



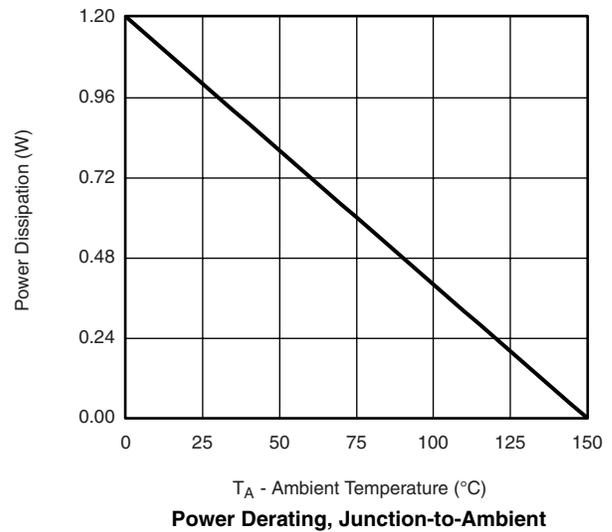
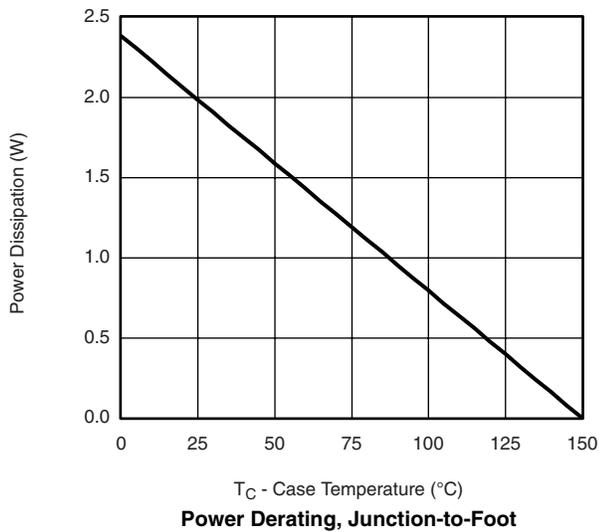
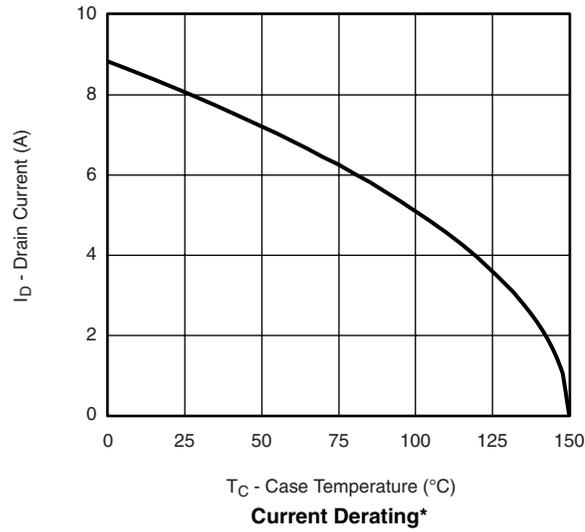
**Single Pulse Power, Junction-to-Ambient**



\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

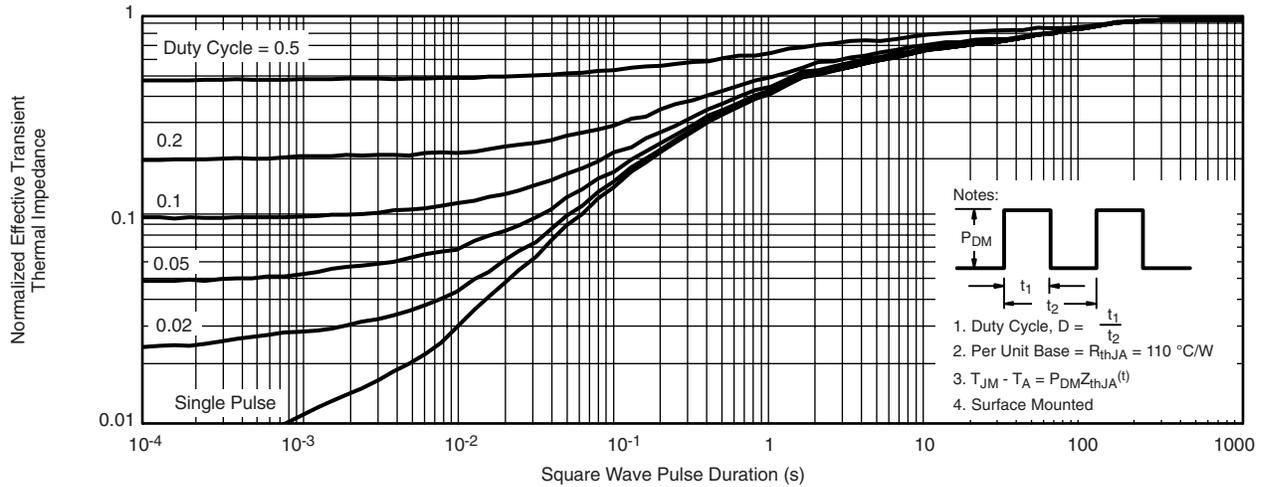
**Safe Operating Area, Junction-to-Ambient**

**CHANNEL-1 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

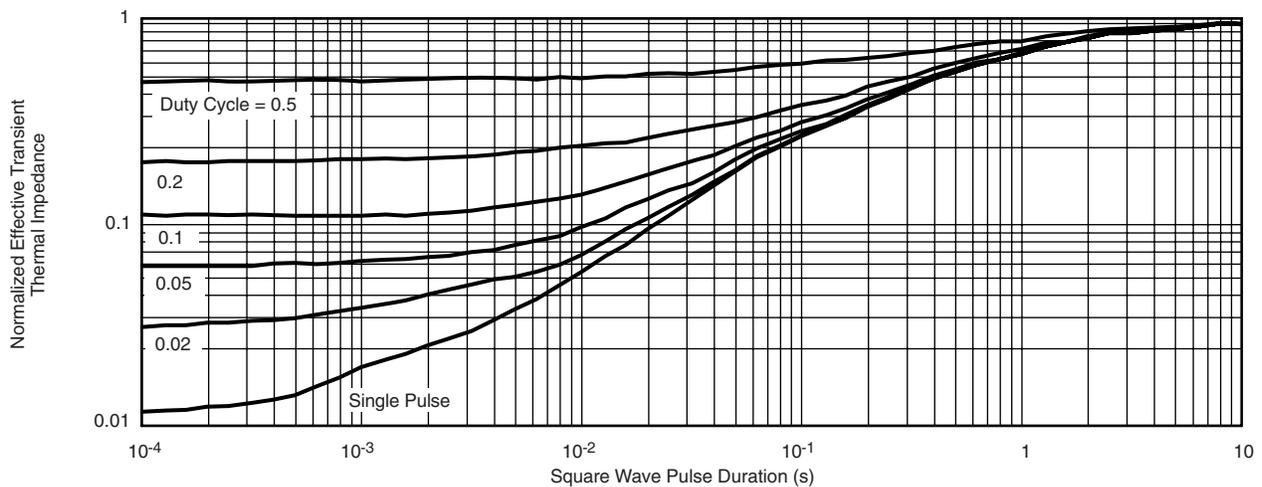


\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**CHANNEL-1 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

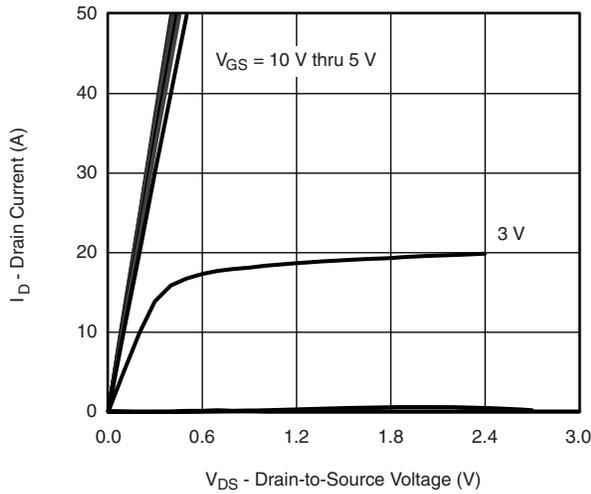


**Normalized Thermal Transient Impedance, Junction-to-Ambient**

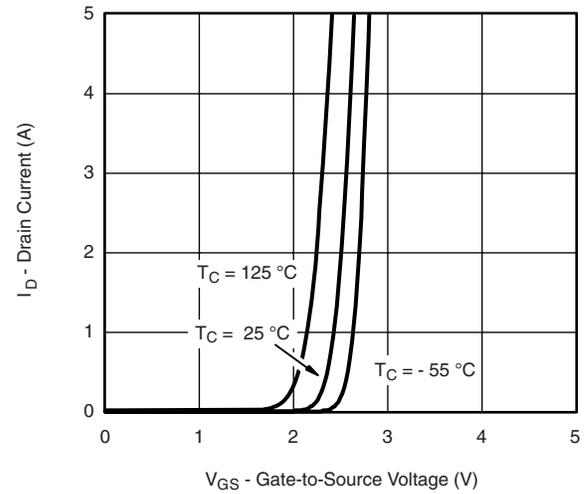


**Normalized Thermal Transient Impedance, Junction-to-Foot**

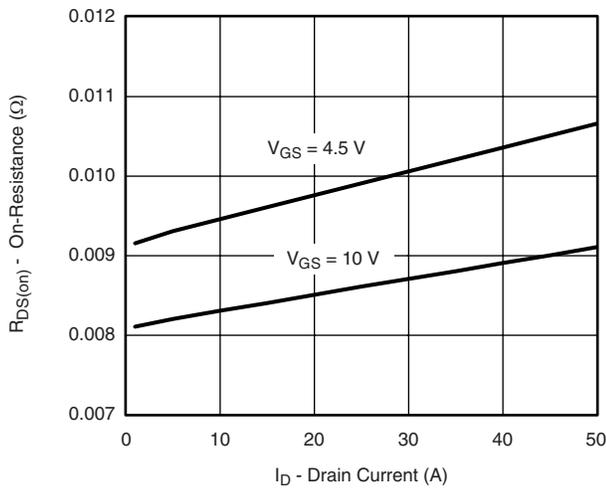
## CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



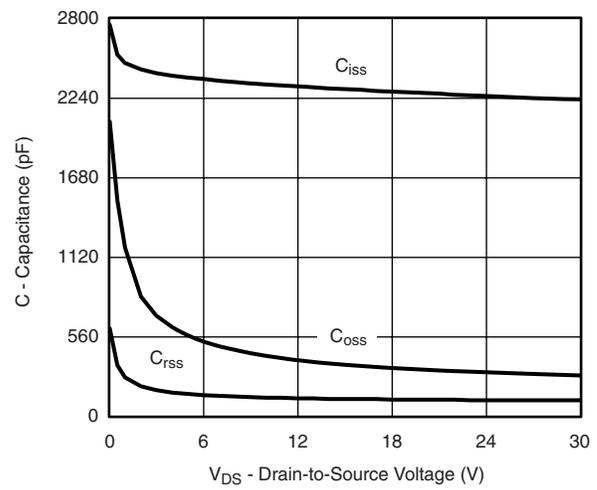
**Output Characteristics**



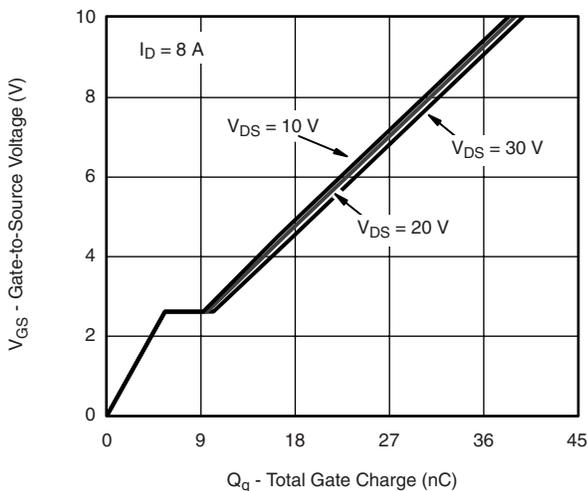
**Transfer Characteristics**



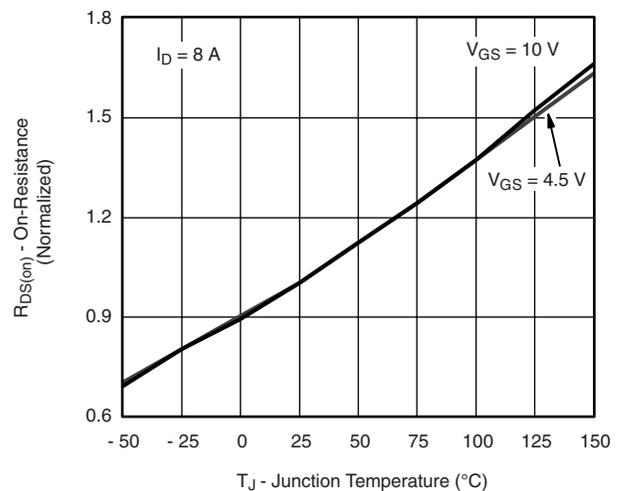
**On-Resistance vs. Drain Current**



**Capacitance**

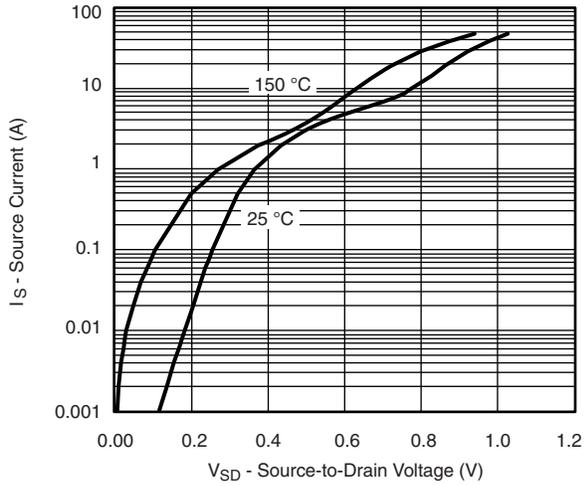


**Gate Charge**

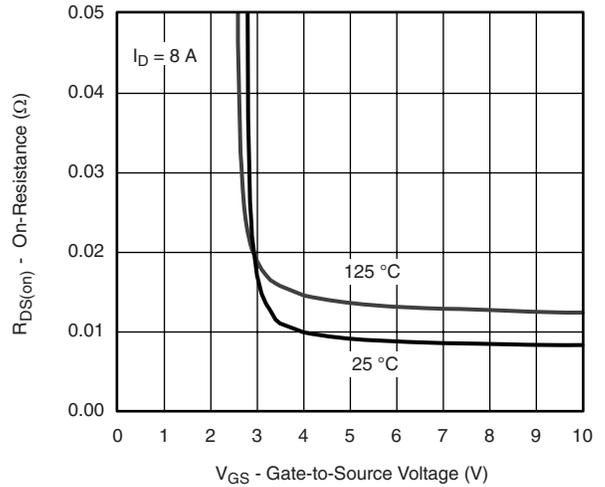


**On-Resistance vs. Junction Temperature**

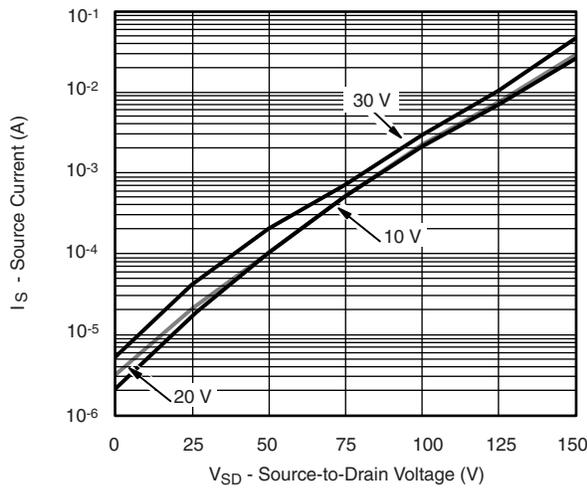
## CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



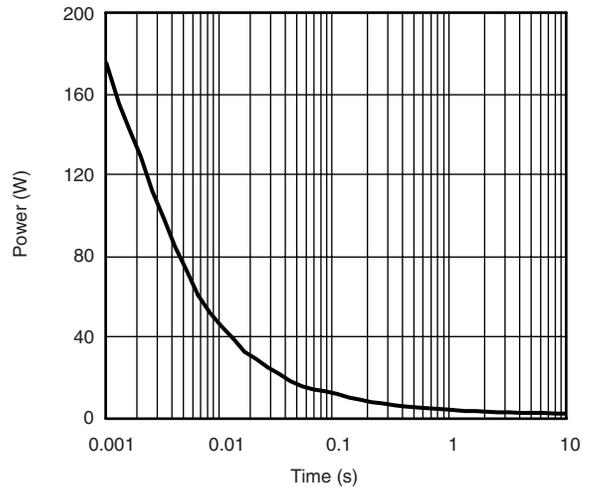
**Source-Drain Diode Forward Voltage**



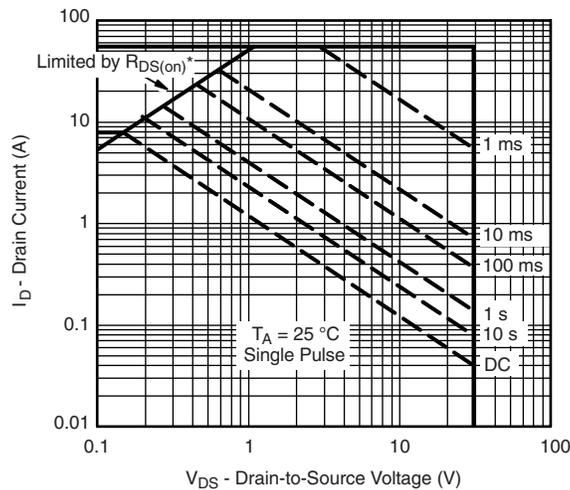
**On-Resistance vs. Gate-to-Source Voltage**



**Reverse Current (Schottky)**



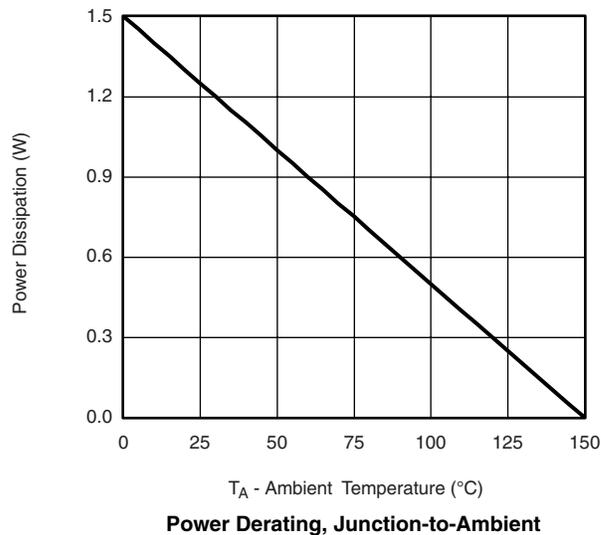
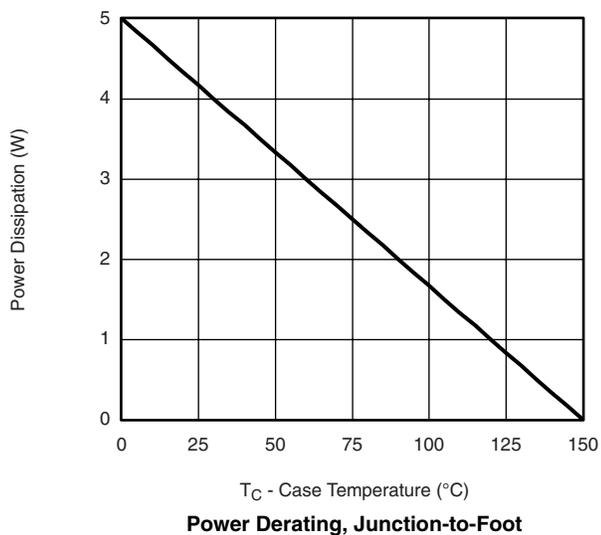
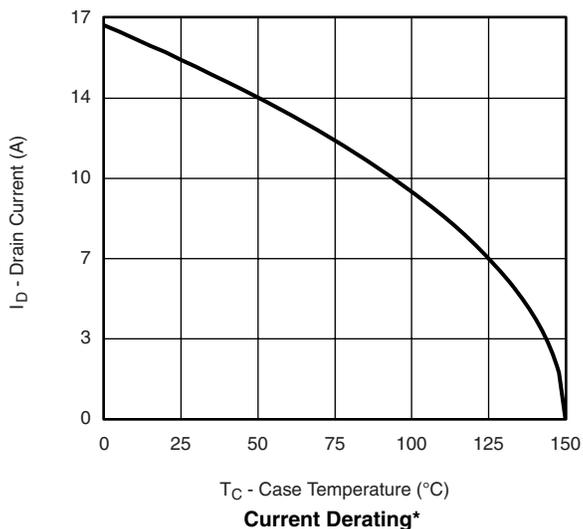
**Single Pulse Power, Junction-to-Ambient**



\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

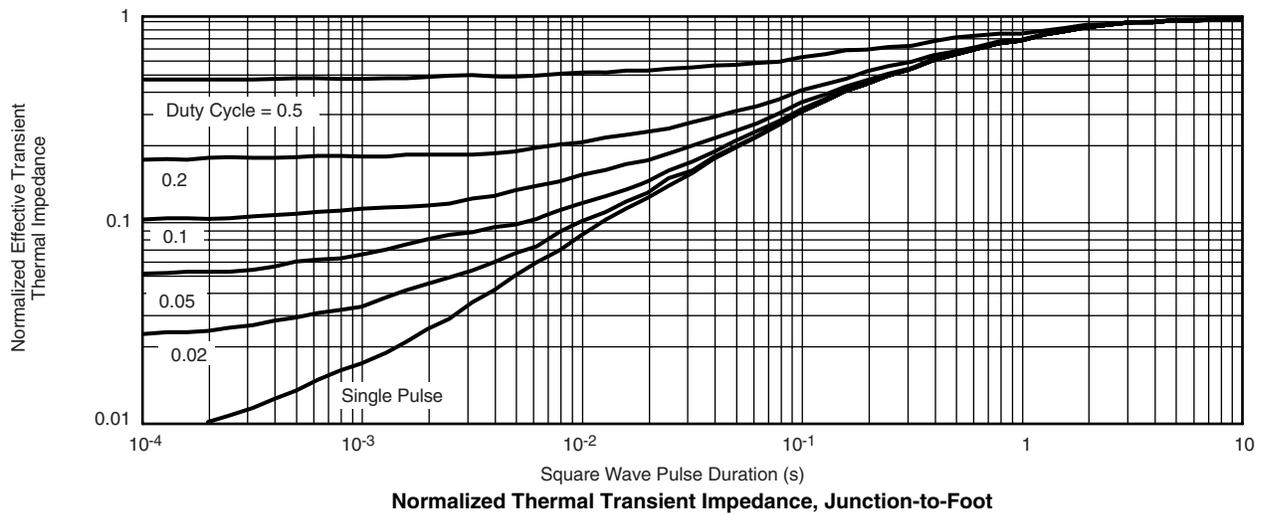
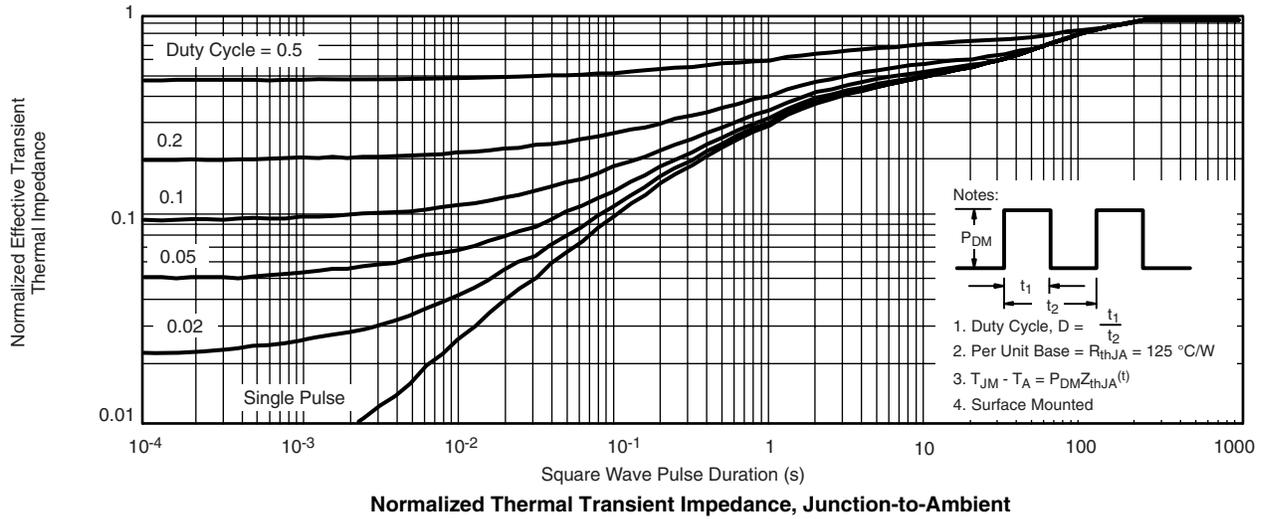
**Safe Operating Area, Junction-to-Ambient**

## CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

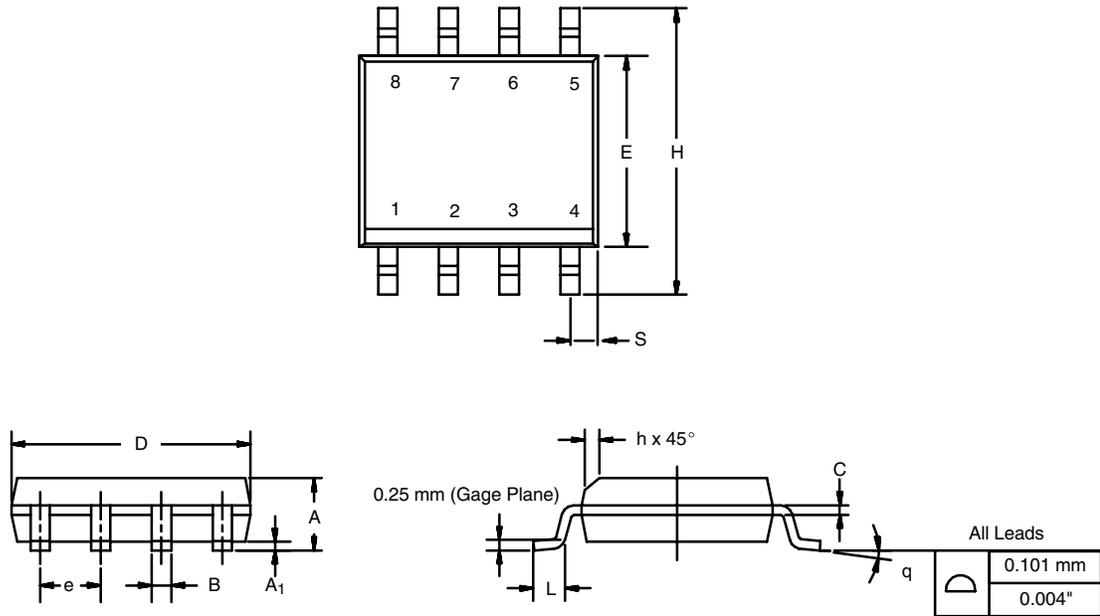
**CHANNEL-2 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see [www.vishay.com/ppg?74450](http://www.vishay.com/ppg?74450).

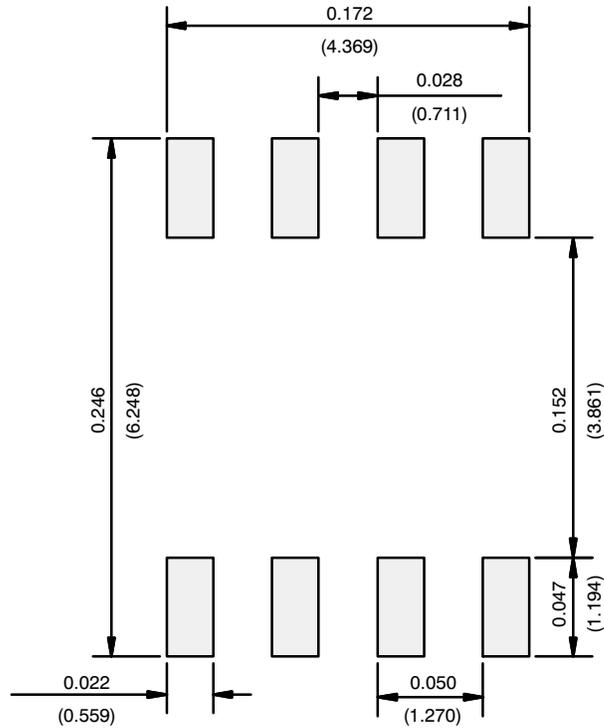
## SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A <sub>1</sub>	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

## RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads  
Dimensions in Inches/(mm)

[Return to Index](#)



## Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

## Material Category Policy

**Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.**

**Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.**

**Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.**