

N-Channel 30-V (D-S) MOSFET

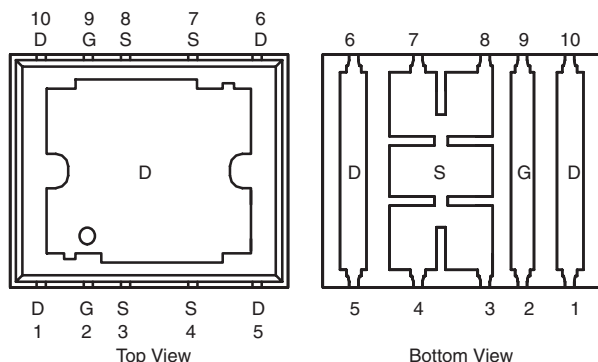
PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^e	Q_g (Typ.)
30	0.007 at $V_{GS} = 10$ V	44.5	13.1 nC
	0.010 at $V_{GS} = 4.5$ V	37.3	

Package Drawing

<http://www.vishay.com/doc?72945>

PolarPAK



Top surface is connected to pins 1, 5, 6, and 10

Ordering Information: SiE844DF-T1-E3 (Lead (Pb)-free)

FEATURES

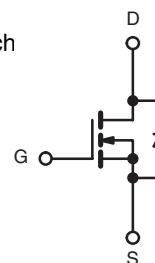
- TrenchFET[®] Gen II Power MOSFET
- Ultra Low Thermal Resistance Using Top-Exposed PolarPAK[®] Package for Double-Sided Cooling
- Leadframe-Based New Encapsulated Package
 - Die Not Exposed
 - Same Layout Regardless of Die Size
- Low Q_{gd}/Q_{gs} Ratio Helps Prevent Shoot-Through
- 100 % R_g and UIS Tested



RoHS
COMPLIANT

APPLICATIONS

- VRM, POL
- DC/DC Conversion
- Server
- High-Side Switch



N-Channel MOSFET

For Related Documents

<http://www.vishay.com/ppg?69988>

ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_J = 150^\circ\text{C}$)	I_D	44.5	A
		35.6	
		20.3 ^{a, b}	
		16.3 ^{a, b}	
Pulsed Drain Current	I_{DM}	60	A
Continuous Source-Drain Diode Current	I_S	20.8	A
		4.3 ^{a, b}	
Single Pulse Avalanche Current	I_{AS}	25	A
Avalanche Energy	E_{AS}	31	mJ
Maximum Power Dissipation	P_D	25	W
		16	
		5.2 ^{a, b}	
		3.3 ^{a, b}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 50 to 150	$^\circ\text{C}$
Soldering Recommendations (Peak Temperature) ^{c, d}		260	$^\circ\text{C}$

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. $t = 10$ s.

c. See Solder Profile (<http://www.vishay.com/ppg?73257>). The PolarPAK is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

d. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

e. Based on $T_C = 25^\circ\text{C}$.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, b}	R_{thJA}	20	24	°C/W
Maximum Junction-to-Case (Drain Top)	R_{thJC} (Drain)	4	5	
Maximum Junction-to-Case (Source) ^{a, c}	R_{thJC} (Source)	5.5	7	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. Maximum under Steady State conditions is 68 °C/W.

c. Measured at source pin (on the side of the package).

SPECIFICATIONS $T_J = 25\text{ °C}$, unless otherwise noted

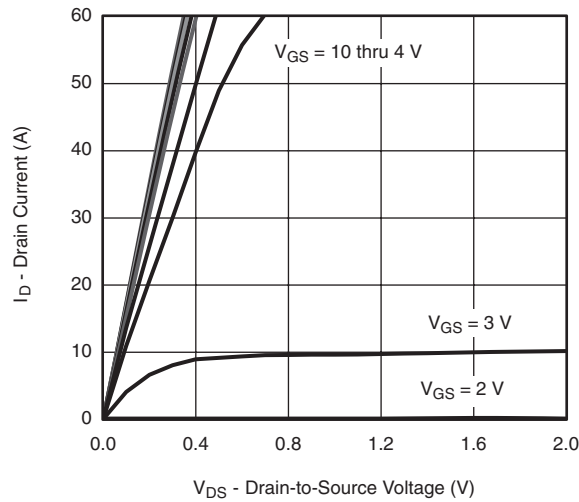
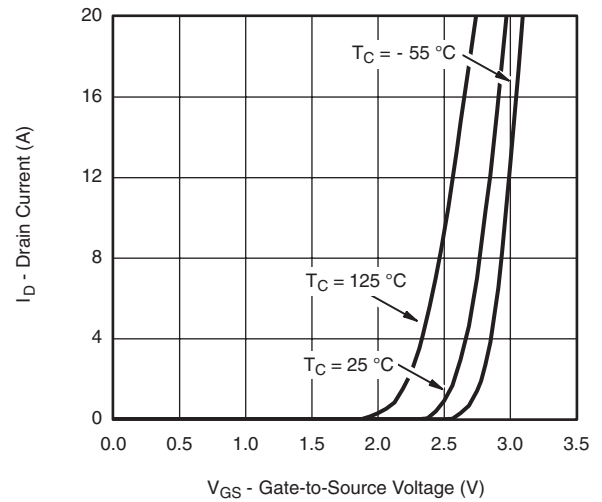
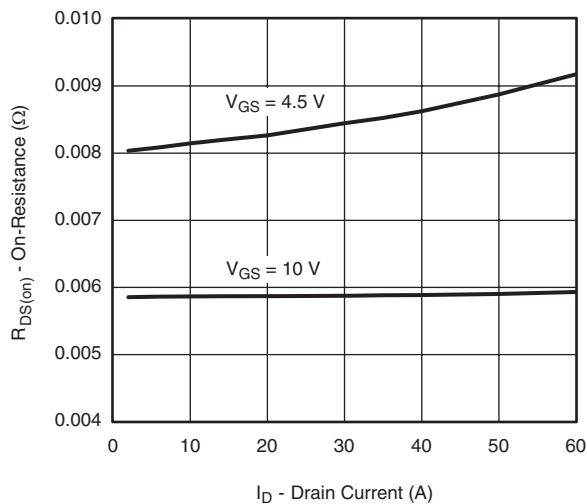
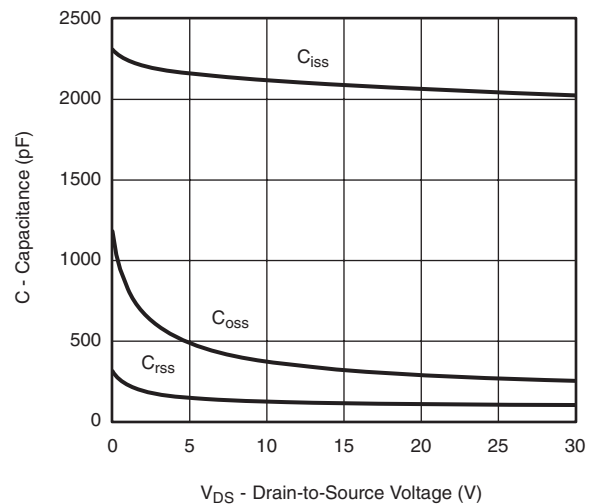
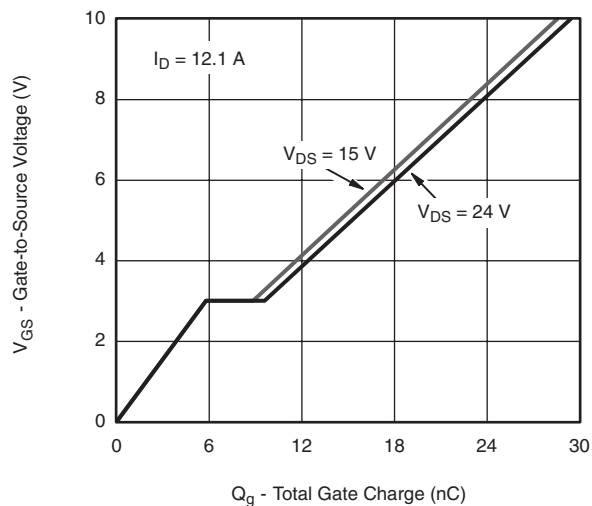
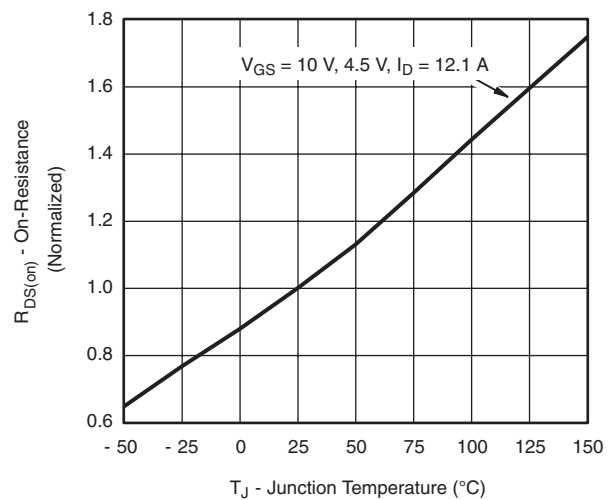
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		30		mV/°C	
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 6			
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1		3	V	
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	μA	
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10		
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	25			A	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 12.1 A		0.0058	0.007	Ω	
		V _{GS} = 4.5 V, I _D = 9.7 A		0.0081	0.010		
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 12.1 A		65		S	
Dynamic ^b							
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		2150		pF	
Output Capacitance	C _{oss}			320			
Reverse Transfer Capacitance	C _{rss}			120			
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 20 A		29	44	nC	
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 20 A		13.1	20		
Gate-Drain Charge	Q _{gd}			6			
Gate Resistance	R _g			3.1			
Turn-On Delay Time	t _{d(on)}	f = 1 MHz		1.2	1.8	Ω	
Rise Time	t _r		V _{DD} = 15 V, R _L = 1.5 Ω I _D ≅ 10 A, V _{GEN} = 4.5 V, R _g = 1 Ω		25	40	ns
Turn-Off Delay Time	t _{d(off)}				10	15	
Fall Time	t _f				25	40	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 1.5 Ω I _D ≅ 10 A, V _{GEN} = 10 V, R _g = 1 Ω			10	15	
Rise Time	t _r			10	15		
Turn-Off Delay Time	t _{d(off)}			25	40		
Fall Time	t _f			10	15		
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			20.8	A	
Pulse Diode Forward Current ^a	I _{SM}				60		
Body Diode Voltage	V _{SD}	I _S = 10 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}	I _F = 10 A, di/dt = 100 A/μs, T _J = 25 °C		30	45	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			24	36	nC	
Reverse Recovery Fall Time	t _a			16		ns	
Reverse Recovery Rise Time	t _b			14			

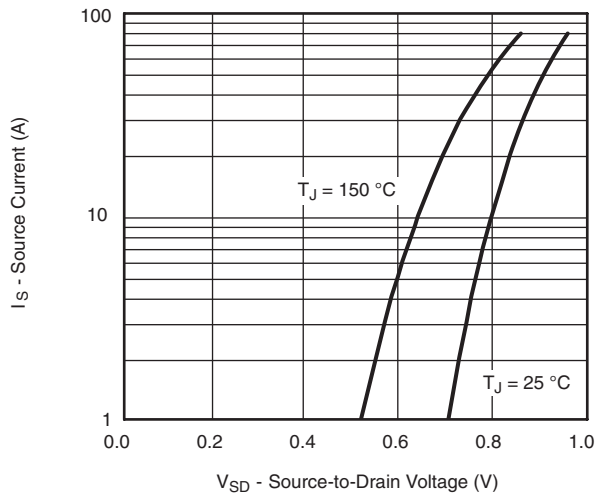
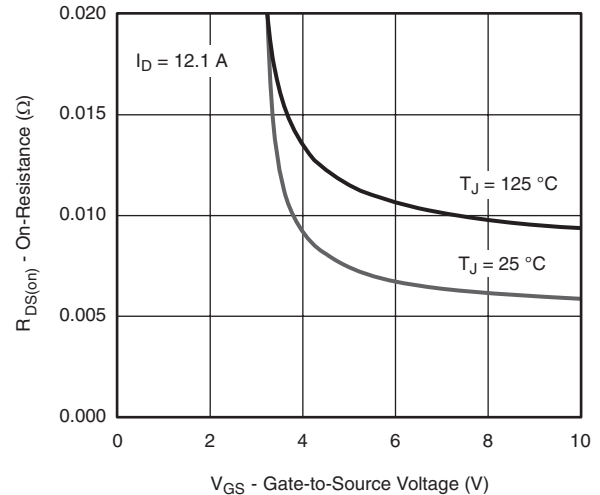
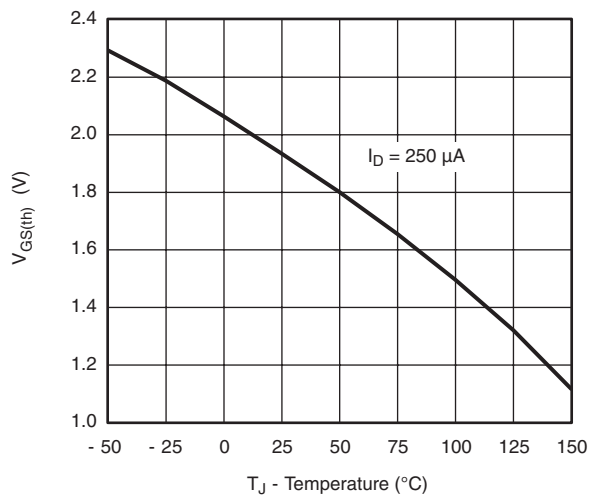
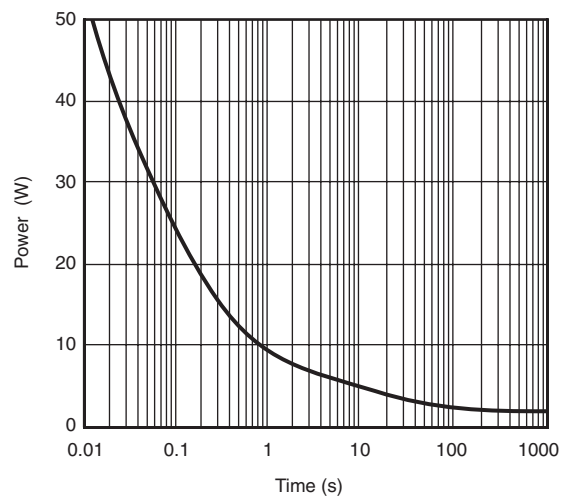
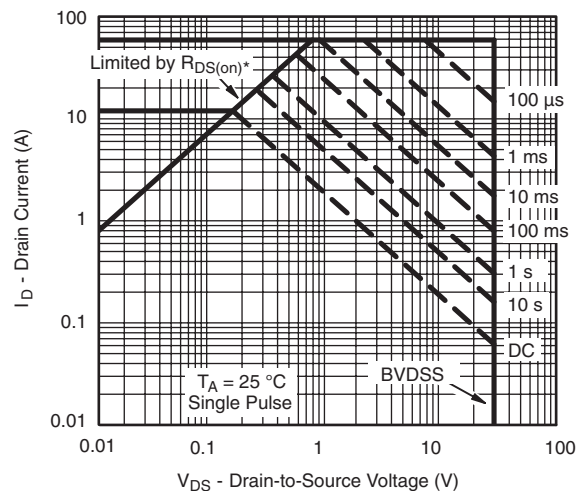
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

b. Guaranteed by design, not subject to production testing.

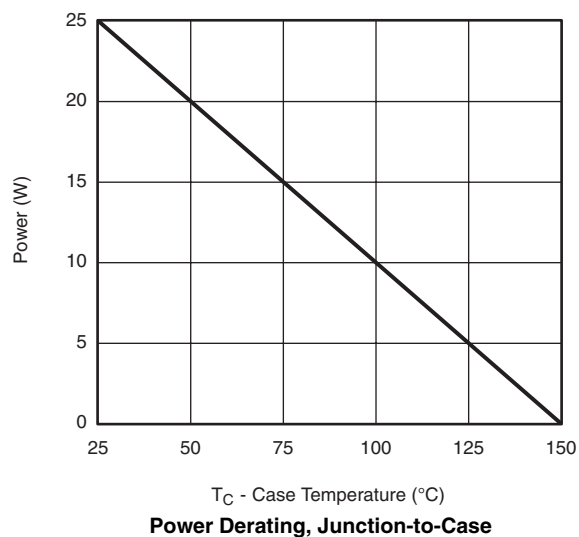
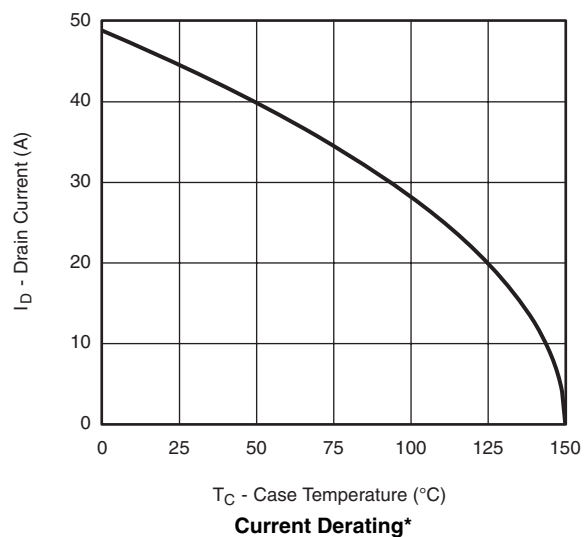
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Output Characteristics****Transfer Characteristics****On-Resistance vs. Drain Current****Capacitance****Gate Charge****On-Resistance vs. Junction Temperature**

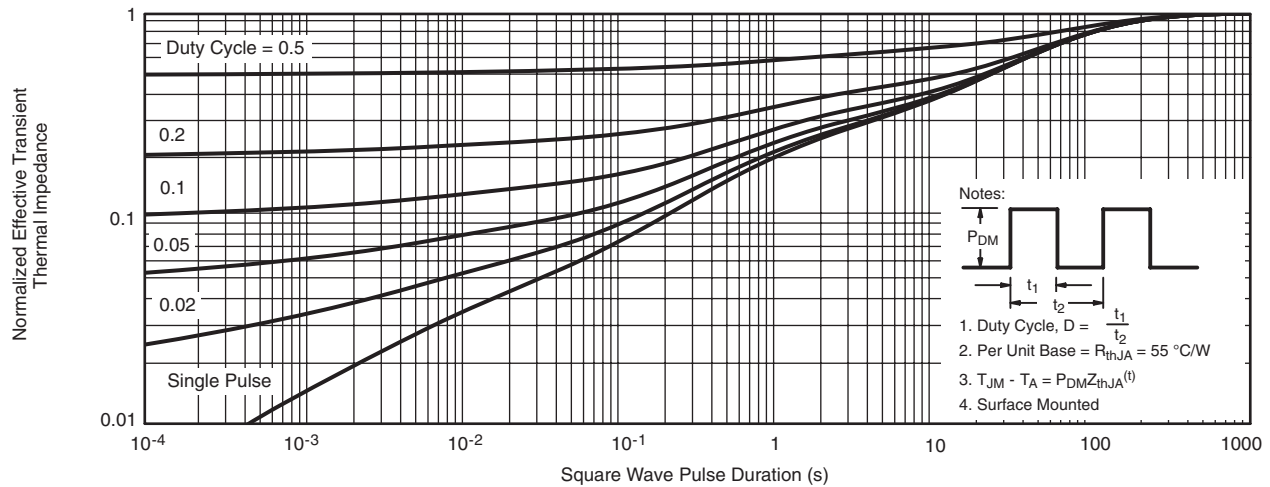
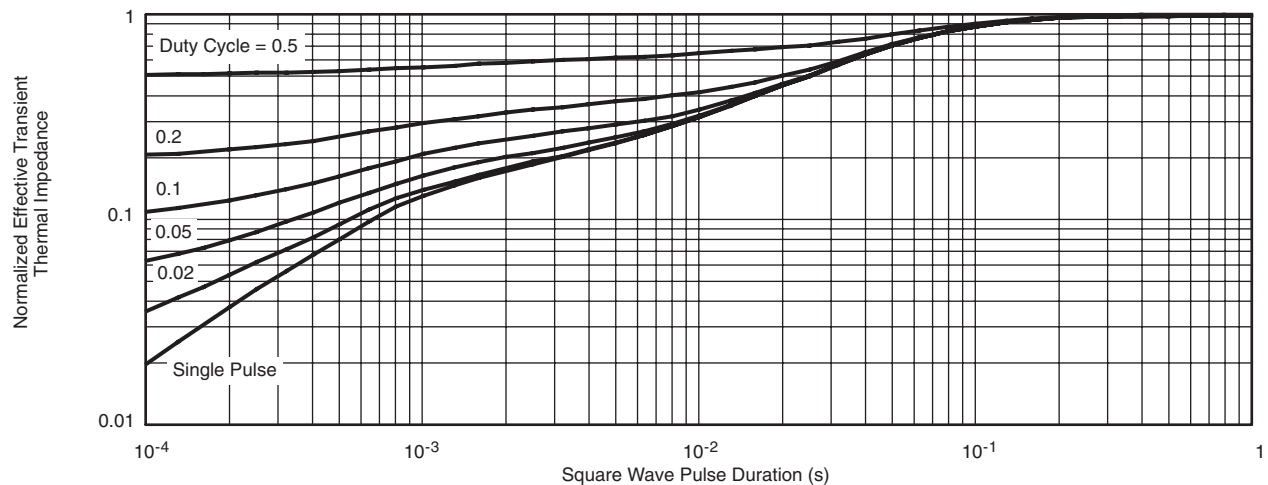
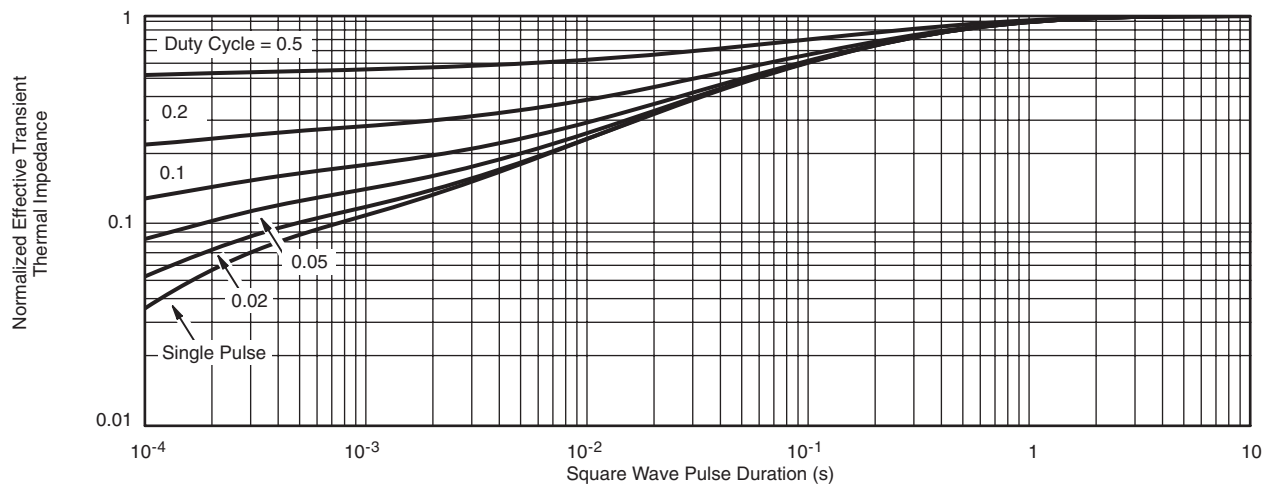
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power, Junction-to-Ambient*** $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified**Safe Operating Area, Junction-to-Ambient**



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted**Normalized Thermal Transient Impedance, Junction-to-Ambient****Normalized Thermal Transient Impedance, Junction-to-Case (Drain Top)****Normalized Thermal Transient Impedance, Junction-to-Source**

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?69988>.



Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.