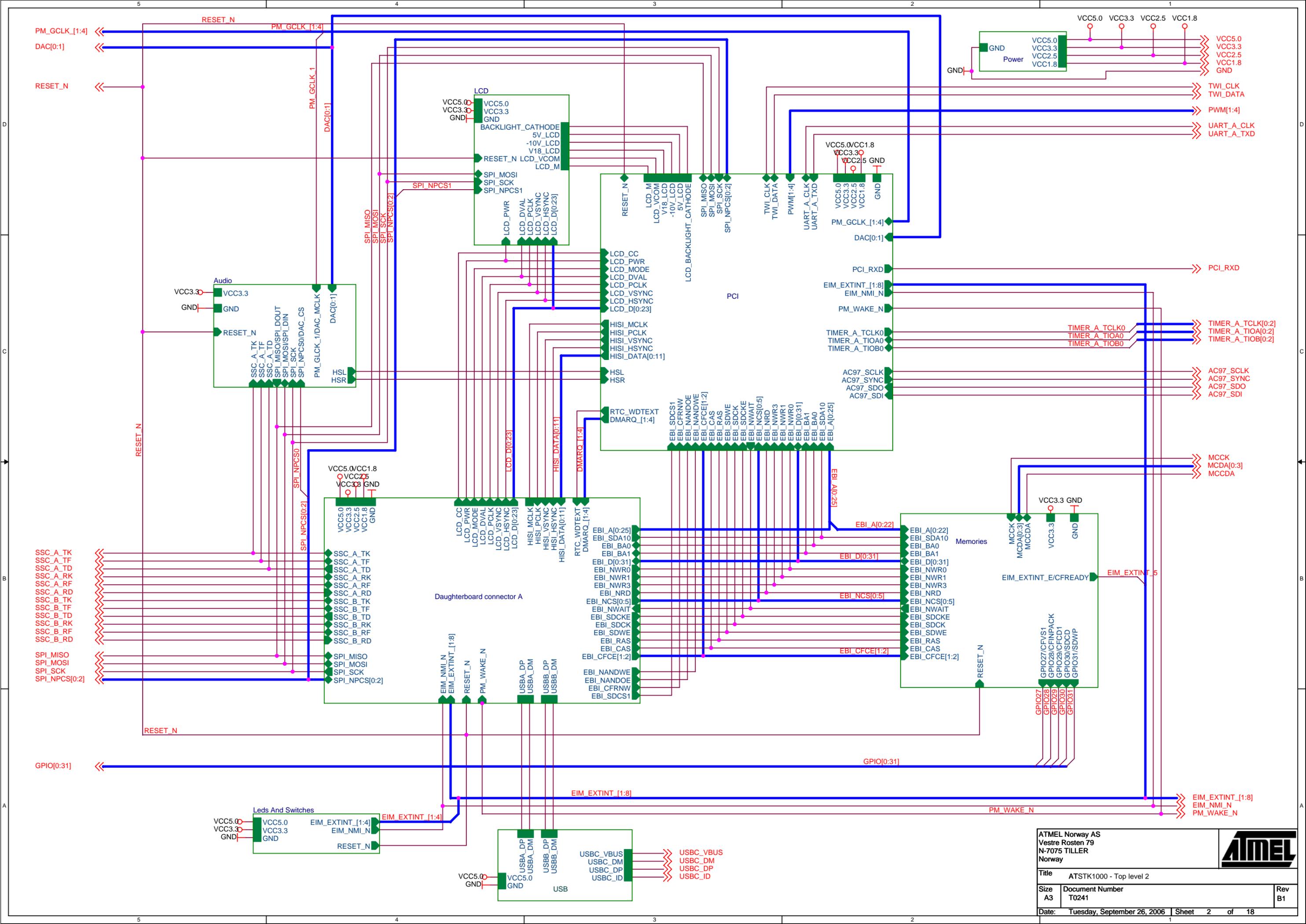
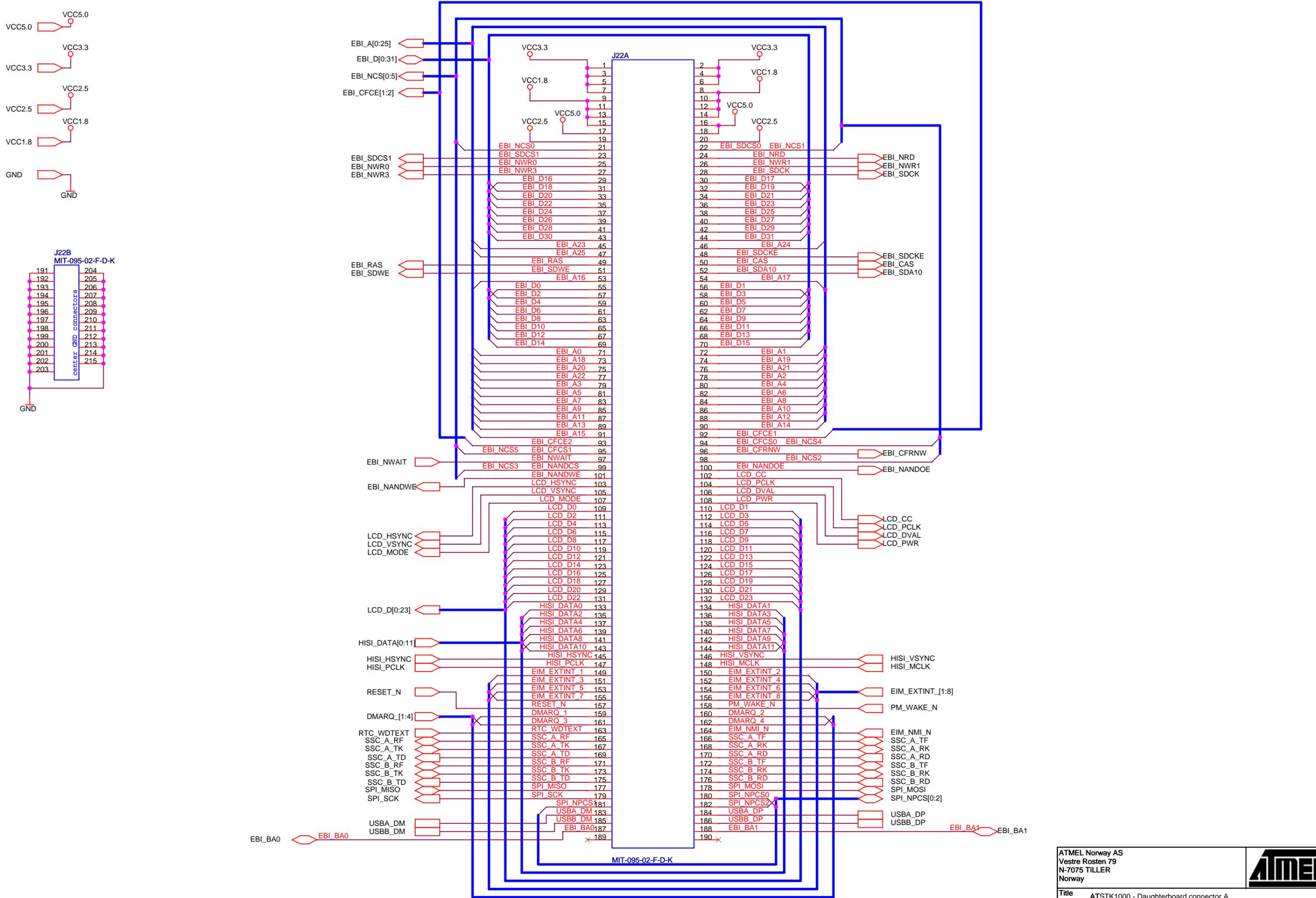
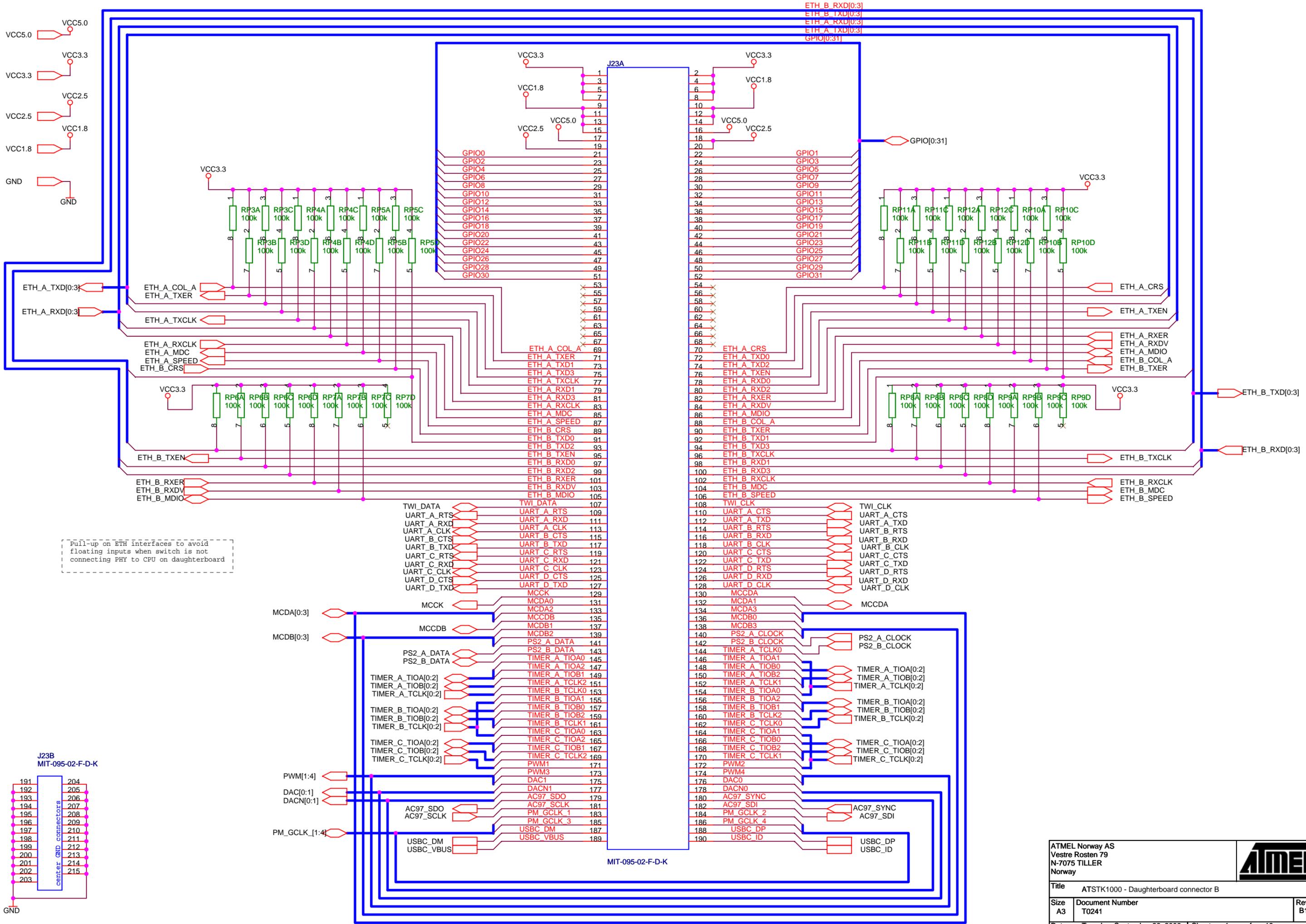


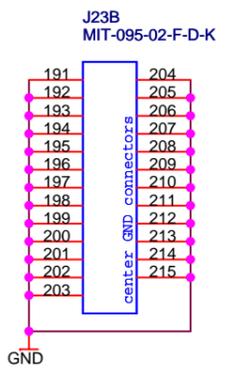
ATMEL Norway AS Vestre Rosten 79 N-7075 TILLER Norway			
Title ATSTK1000 - Top level 1			
Size A3	Document Number T0241	Rev B1	
Date: Tuesday, September 26, 2006			Sheet 1 of 18

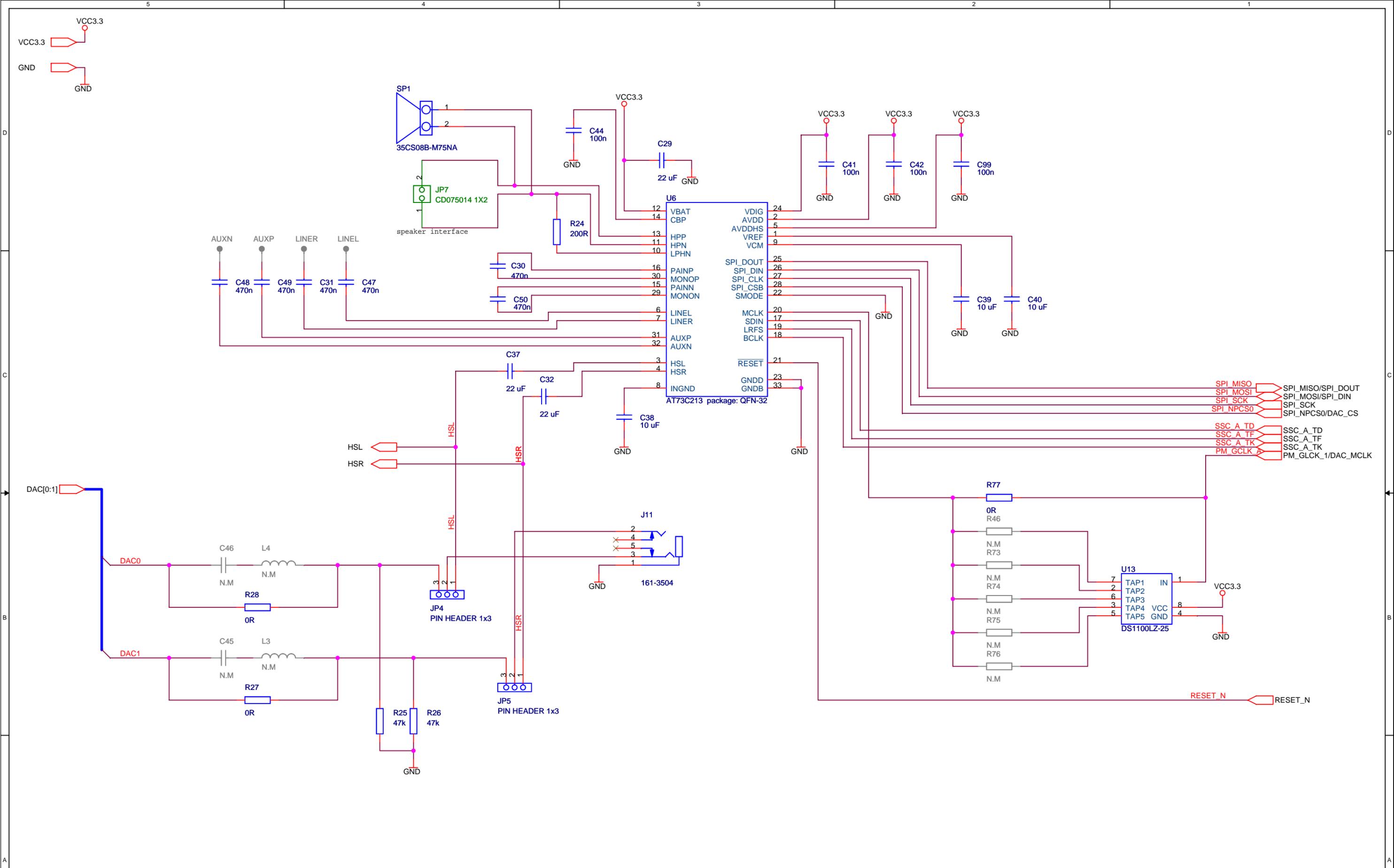


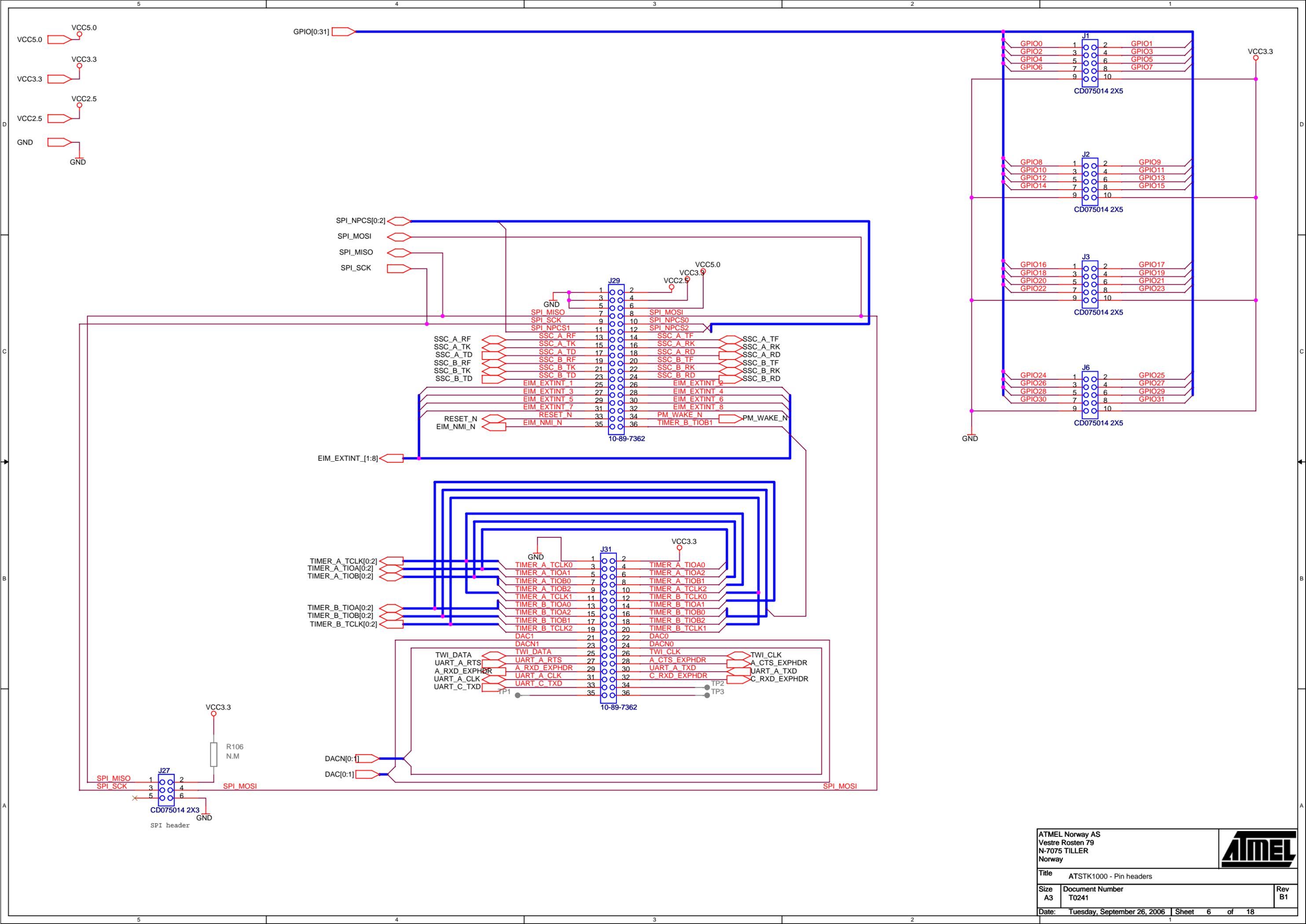


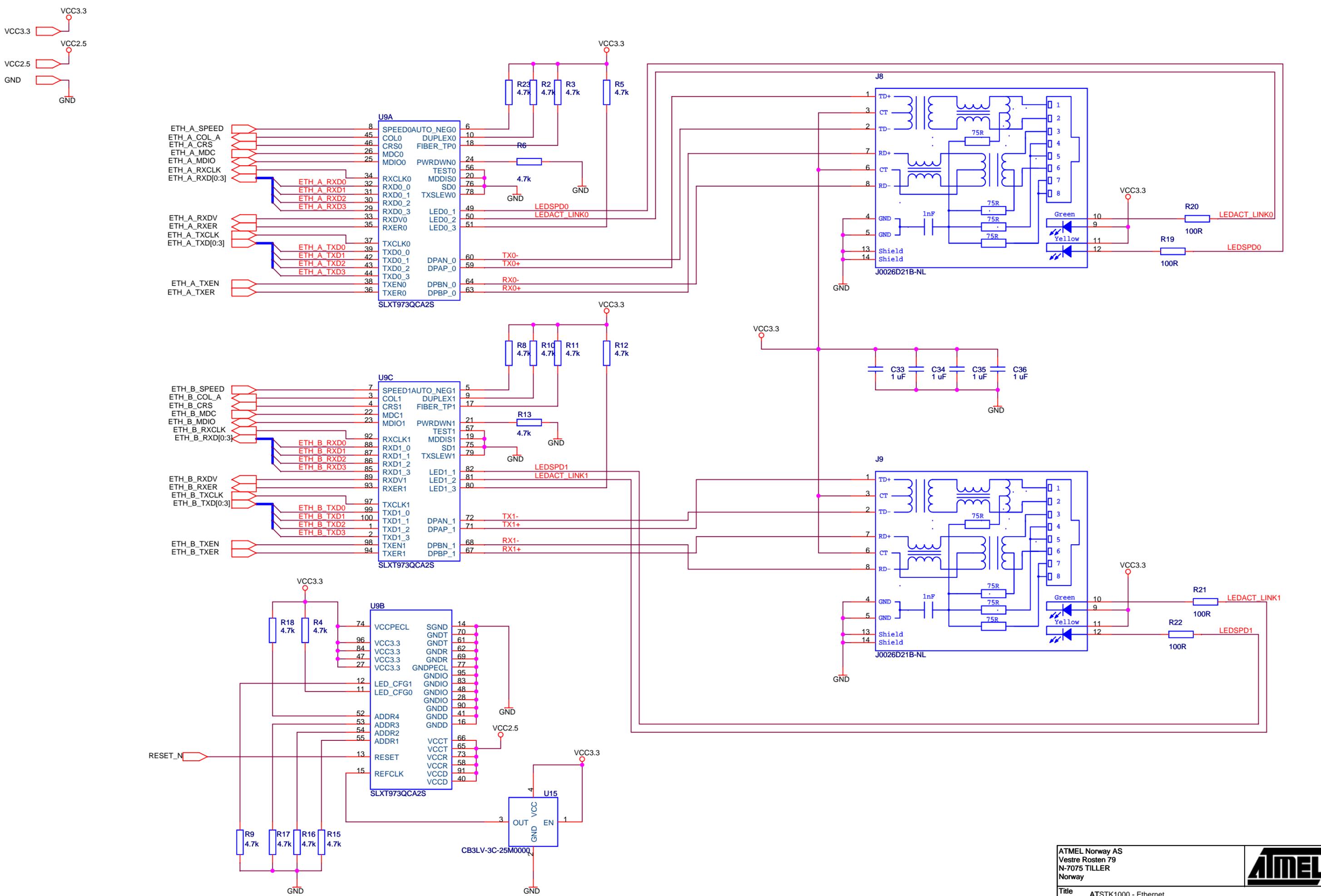


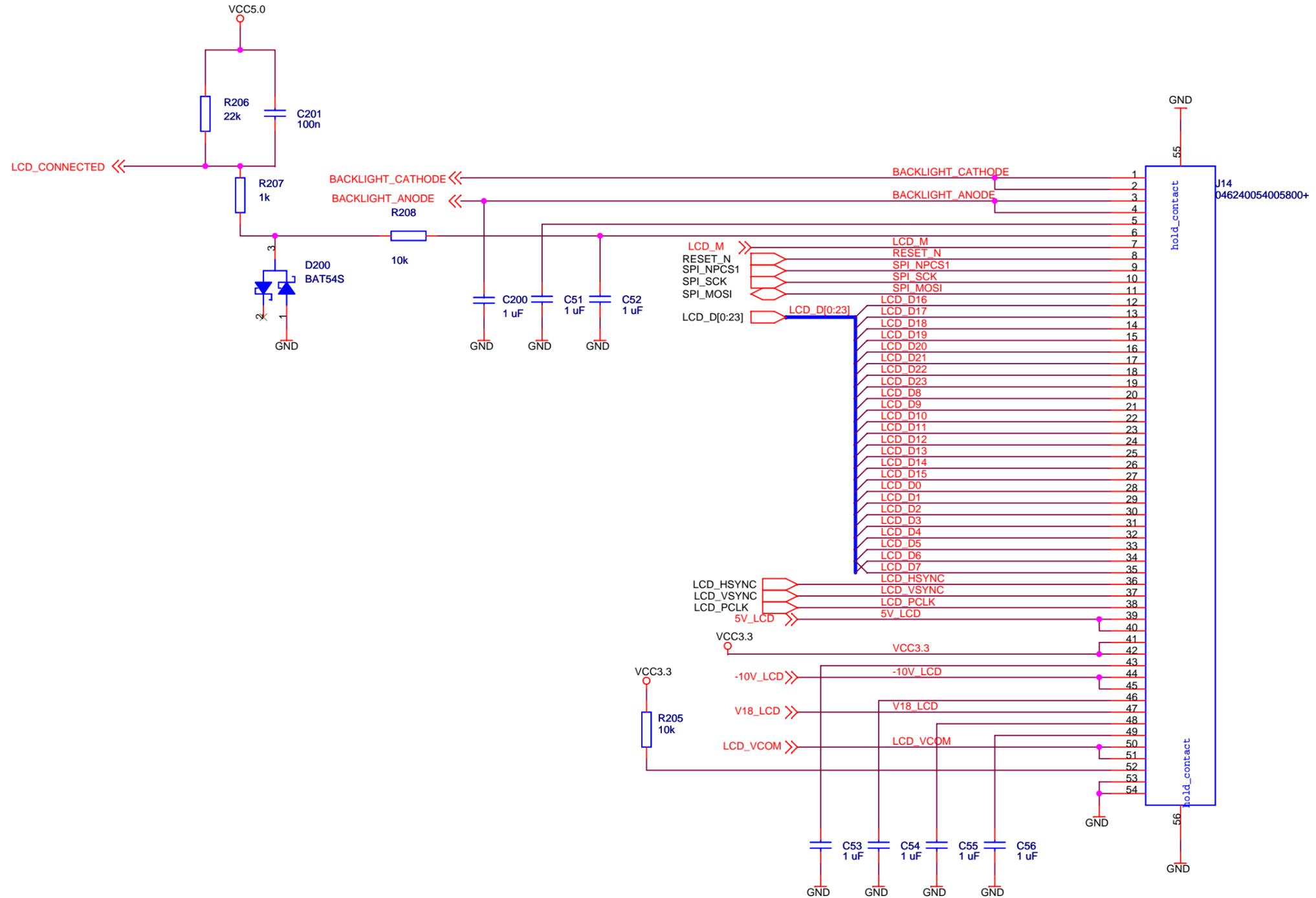
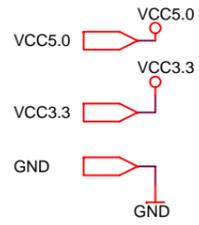
Pull-up on ETH interfaces to avoid floating inputs when switch is not connecting PHY to CPU on daughterboard

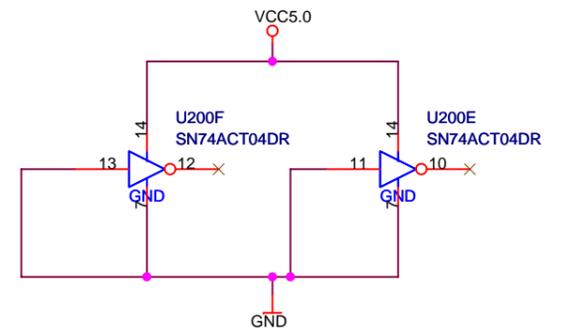
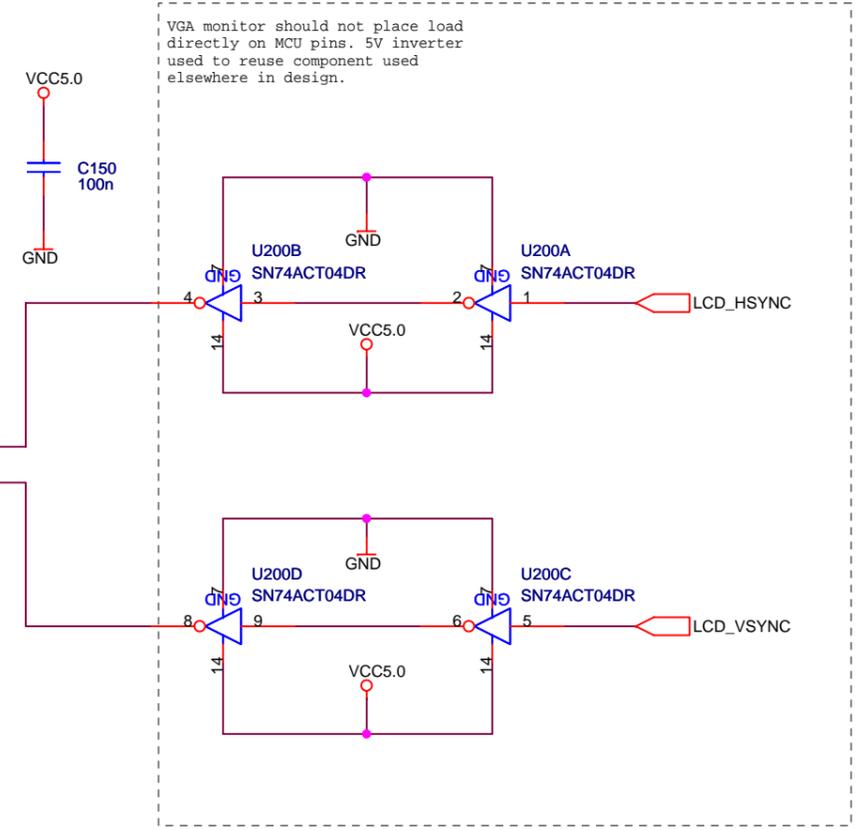
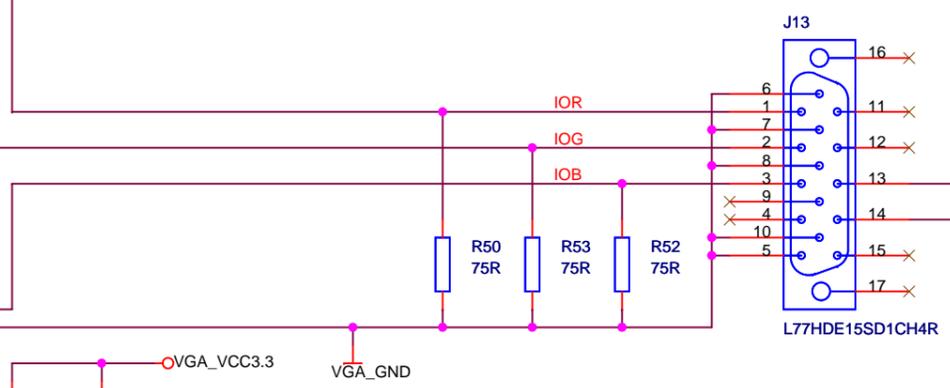
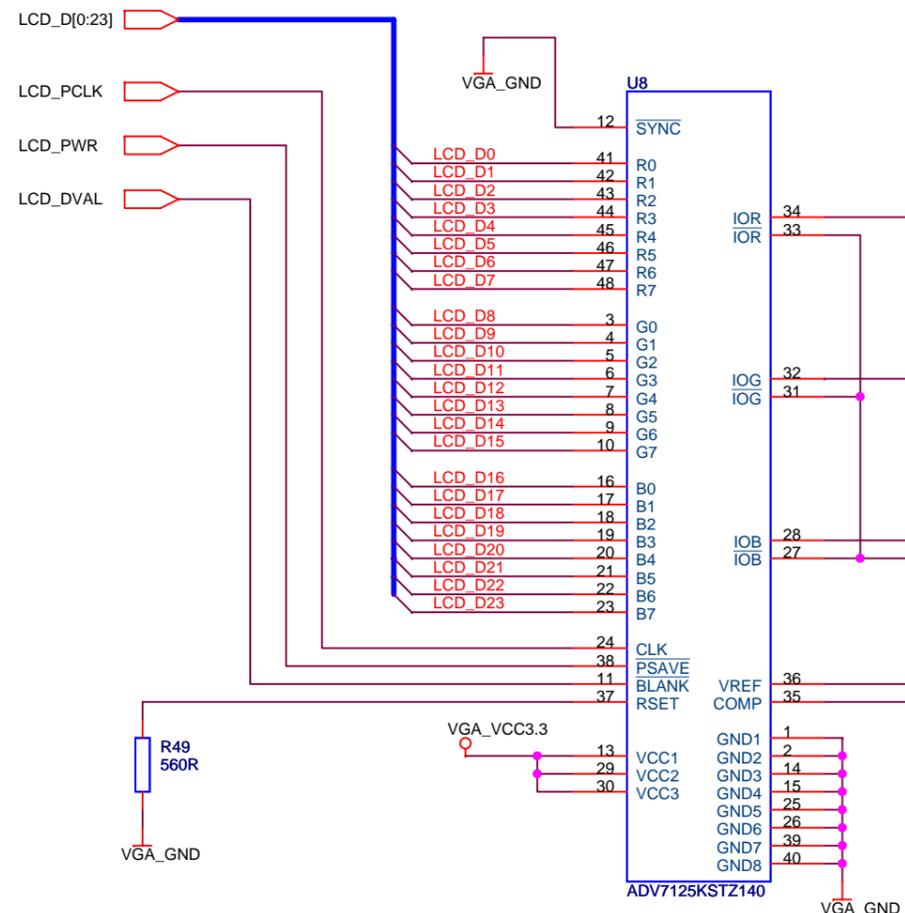
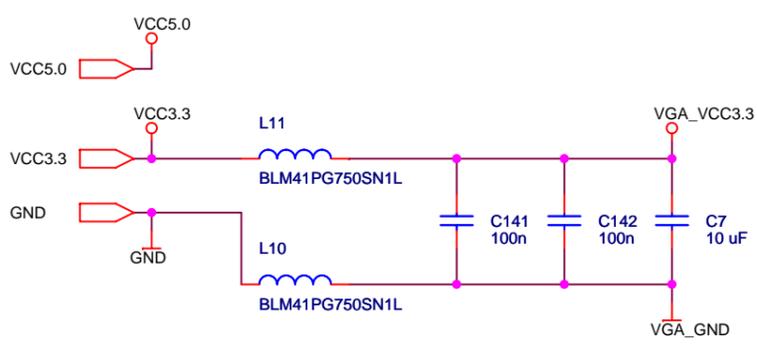


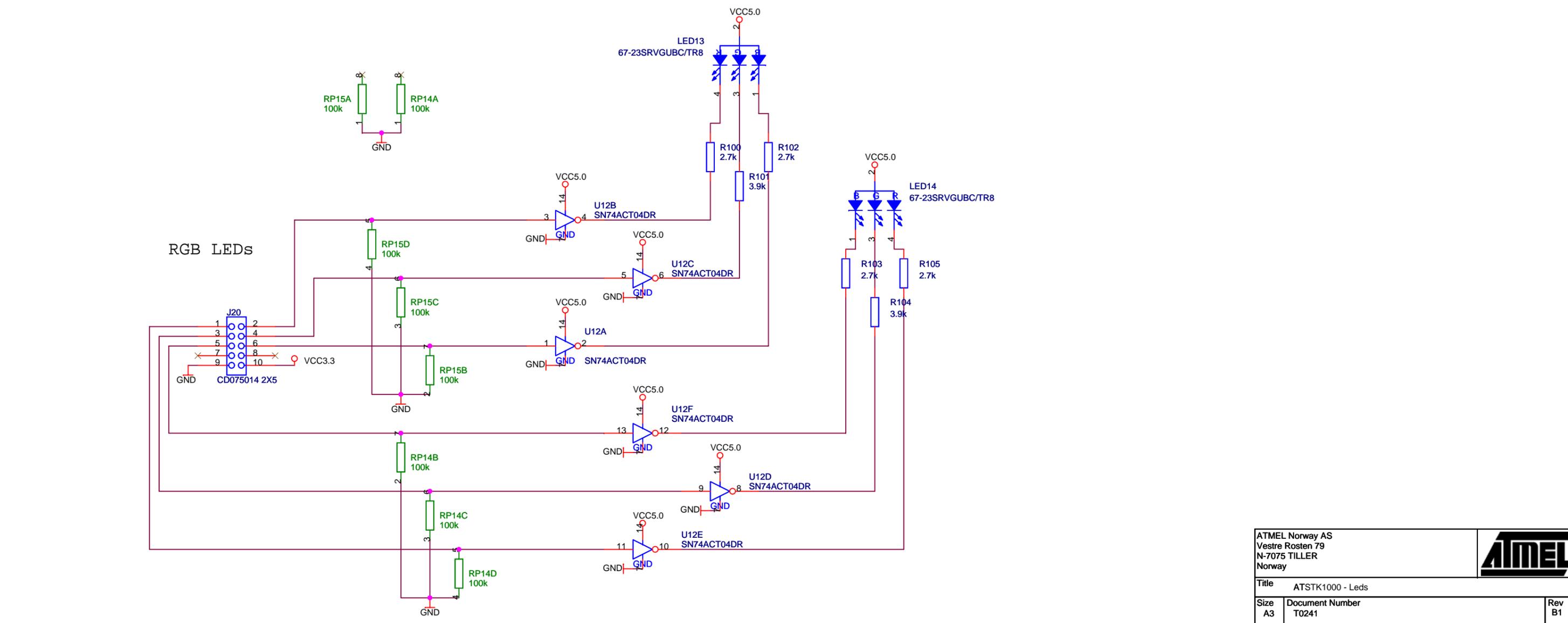
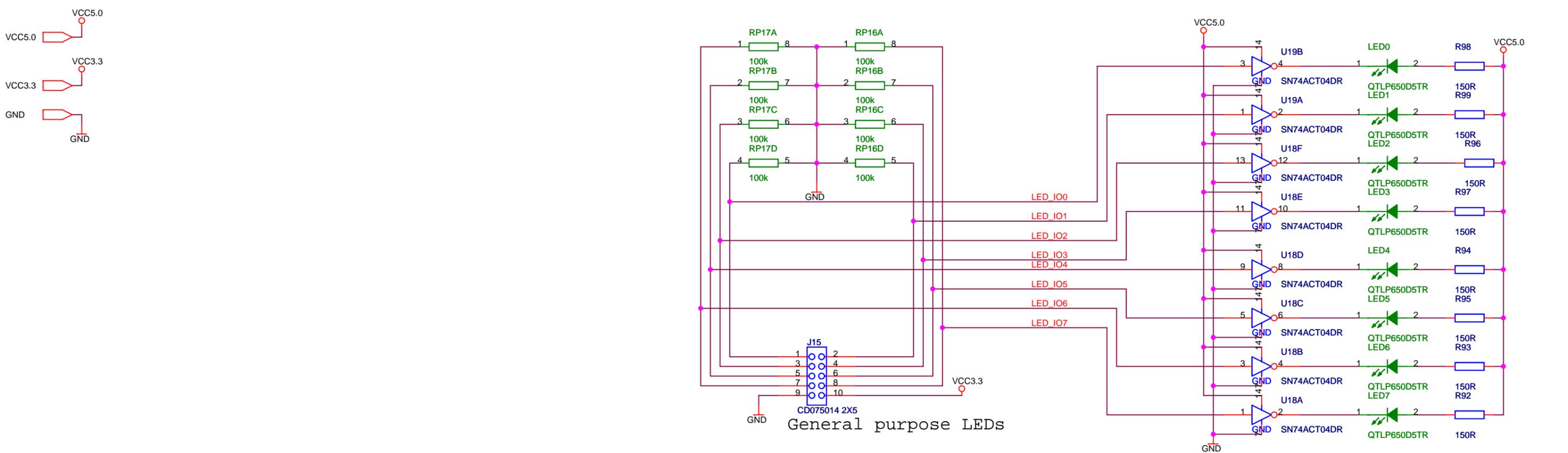


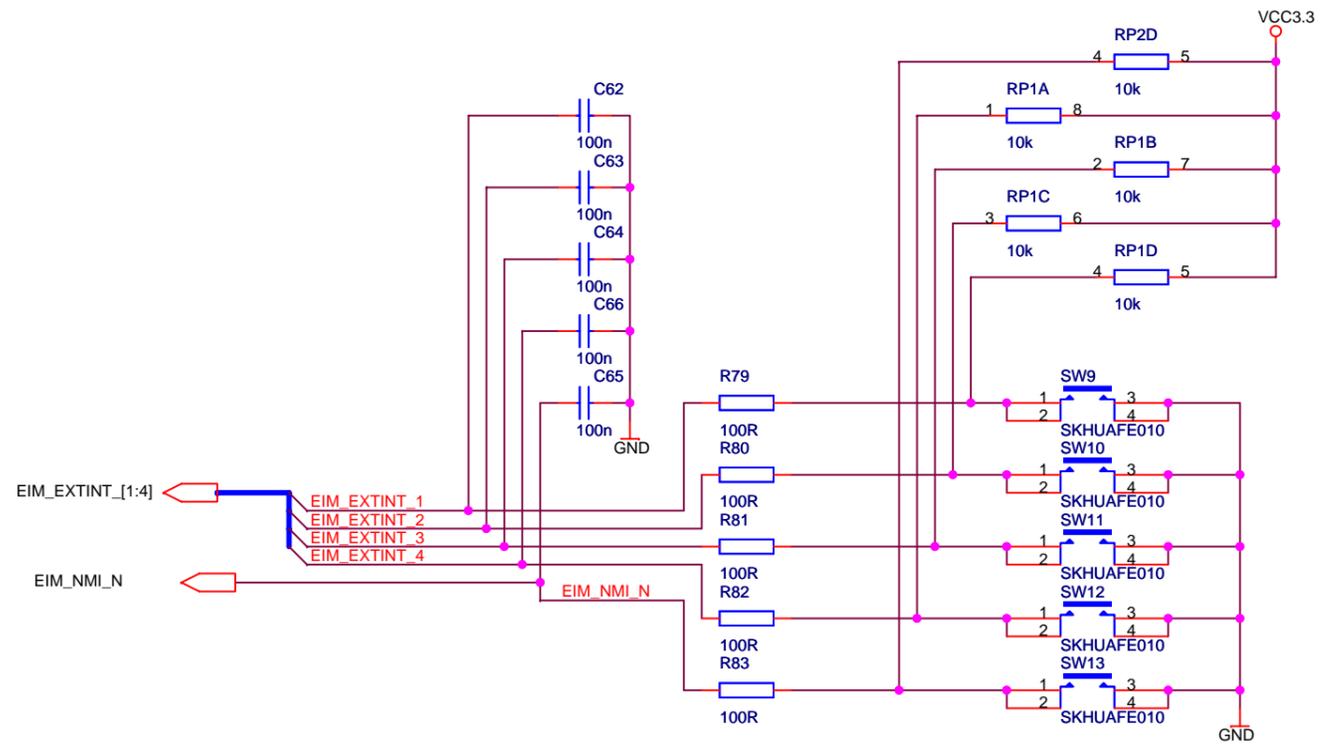




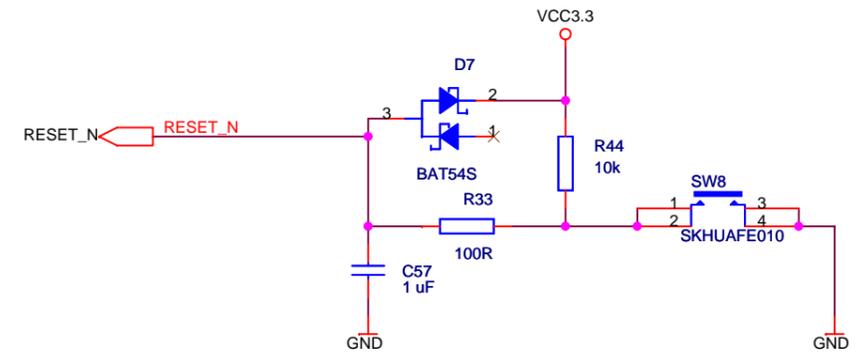




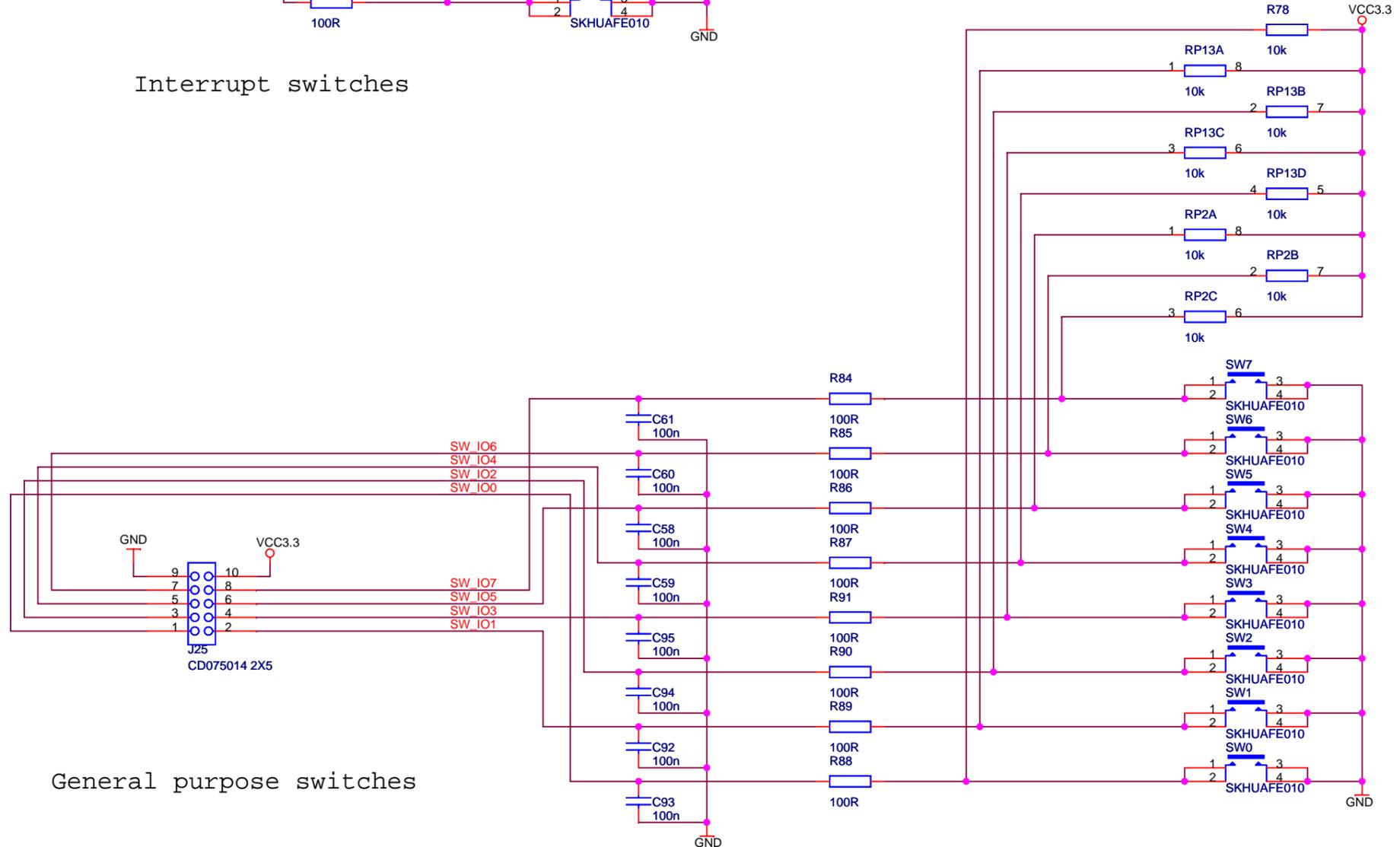




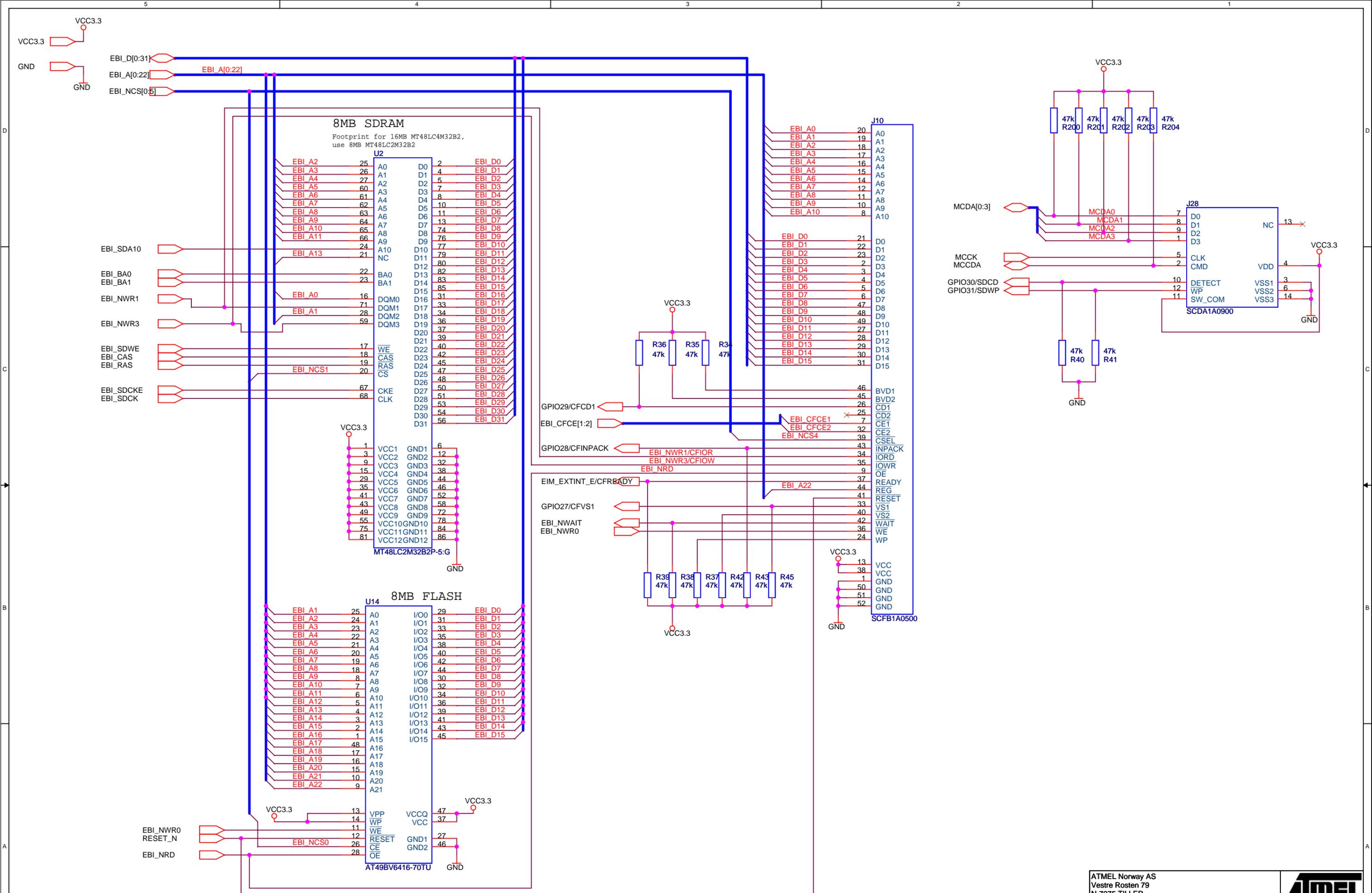
Interrupt switches

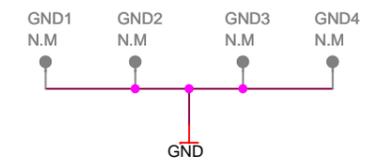
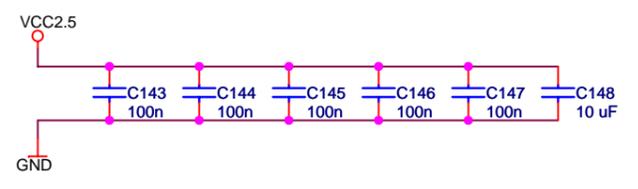
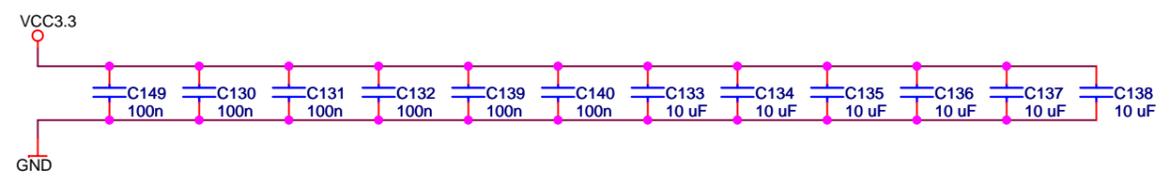
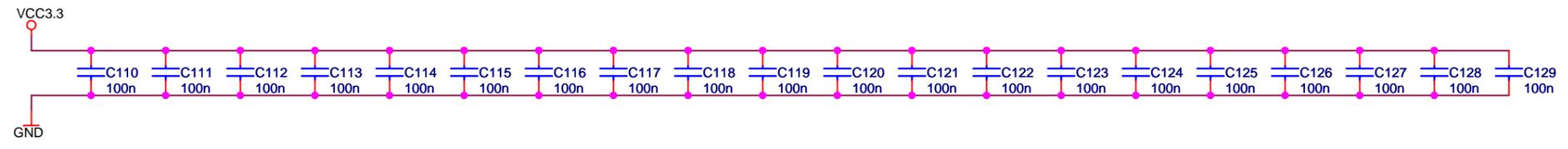
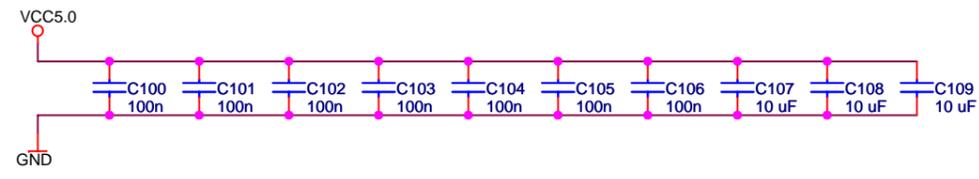
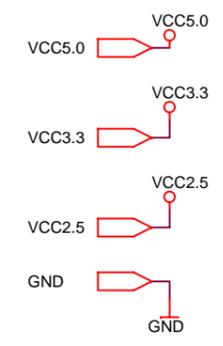


Reset switch

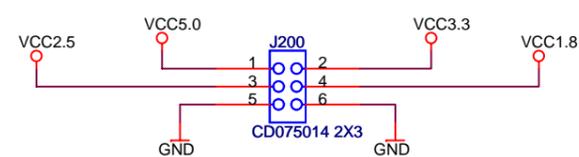


General purpose switches

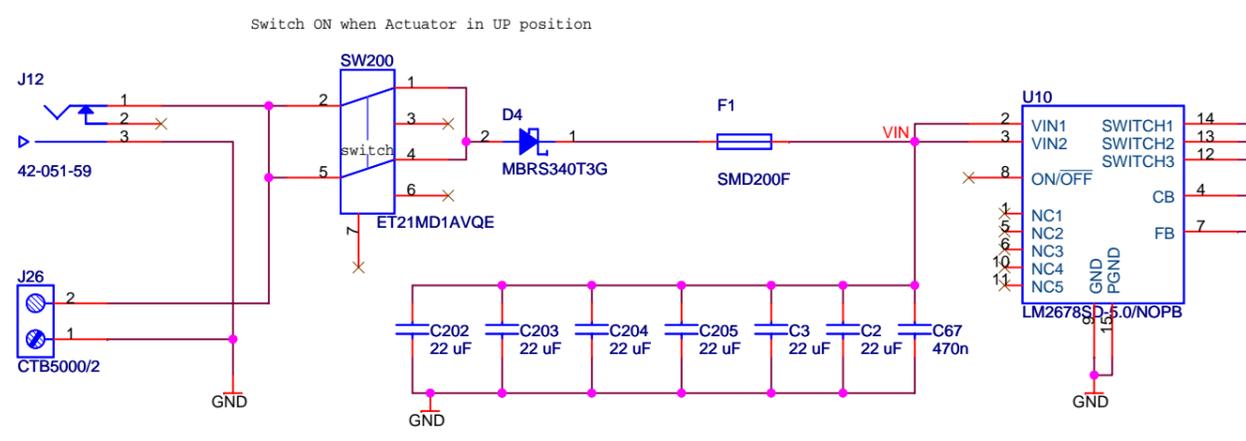
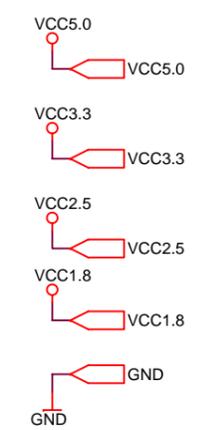




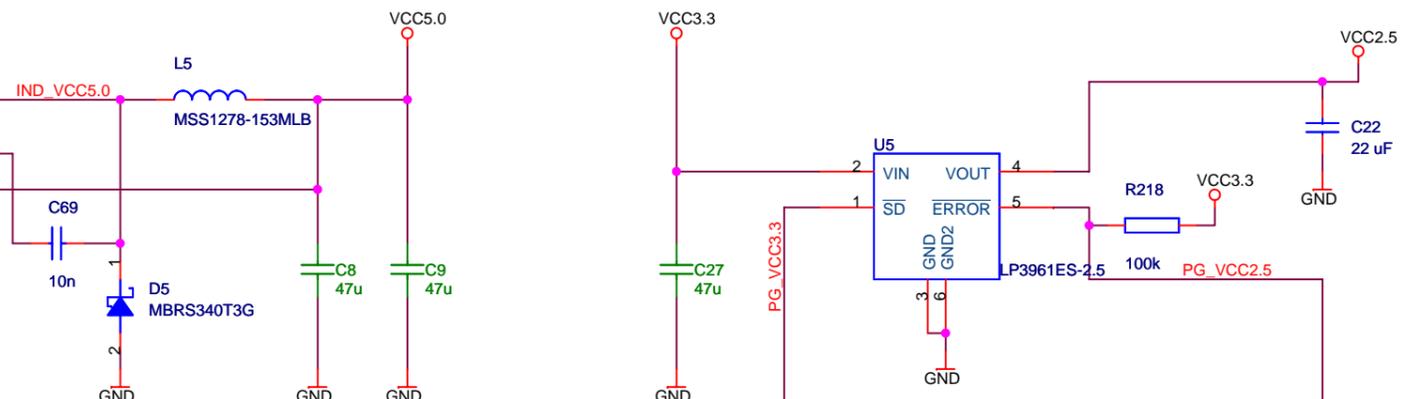
NOTE: Power on sequece is : 5V, followed by 3.3V, followed by 2.5V, followed by 1.8V



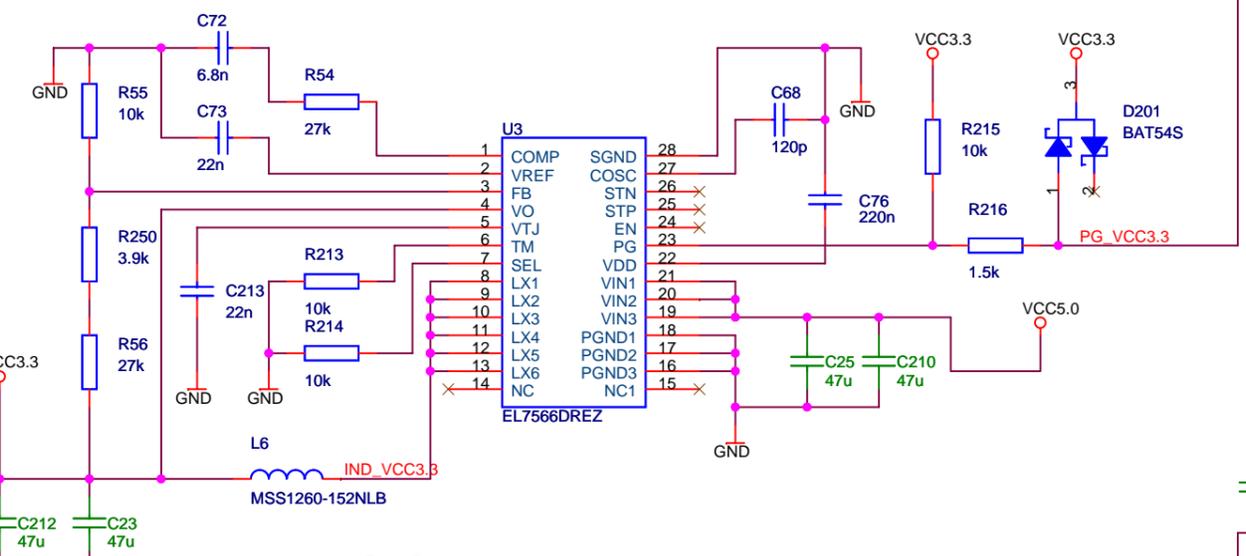
POWER TEST POINTS



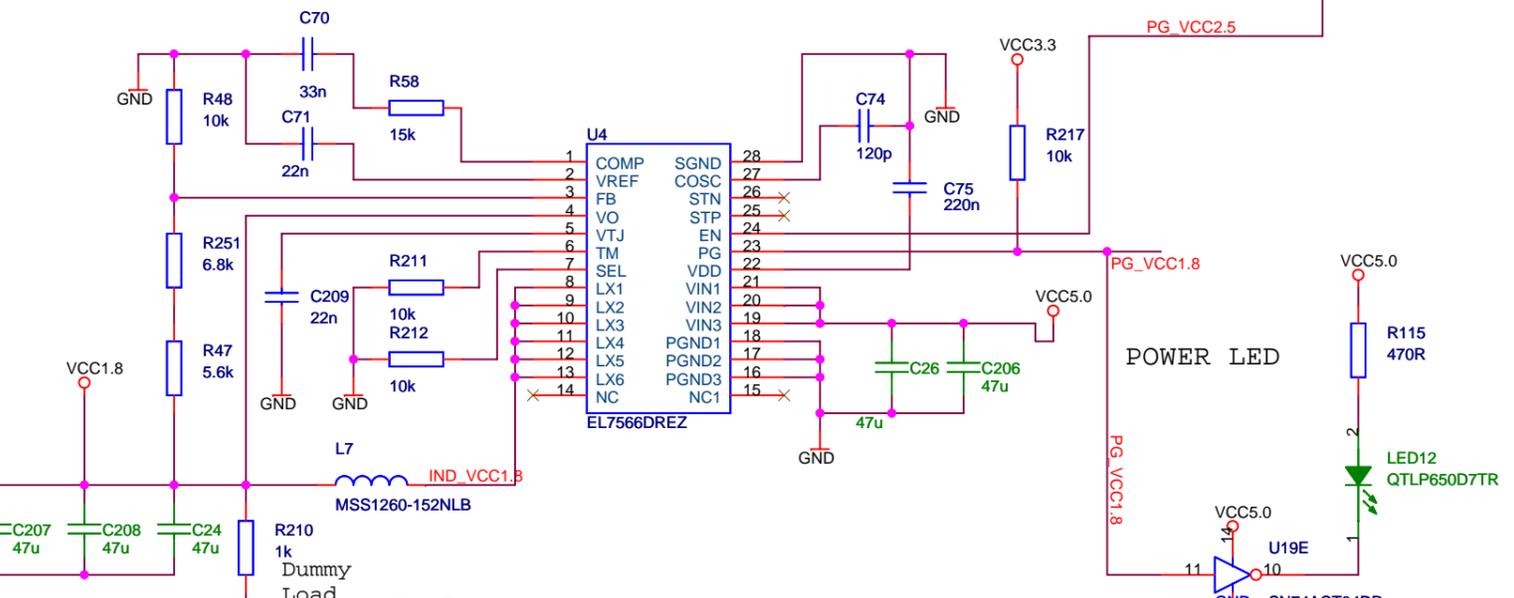
5V Regulator



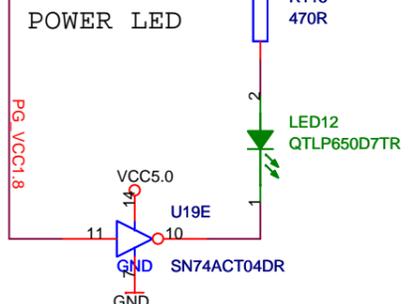
2.5V Regulator



3.3V Regulator



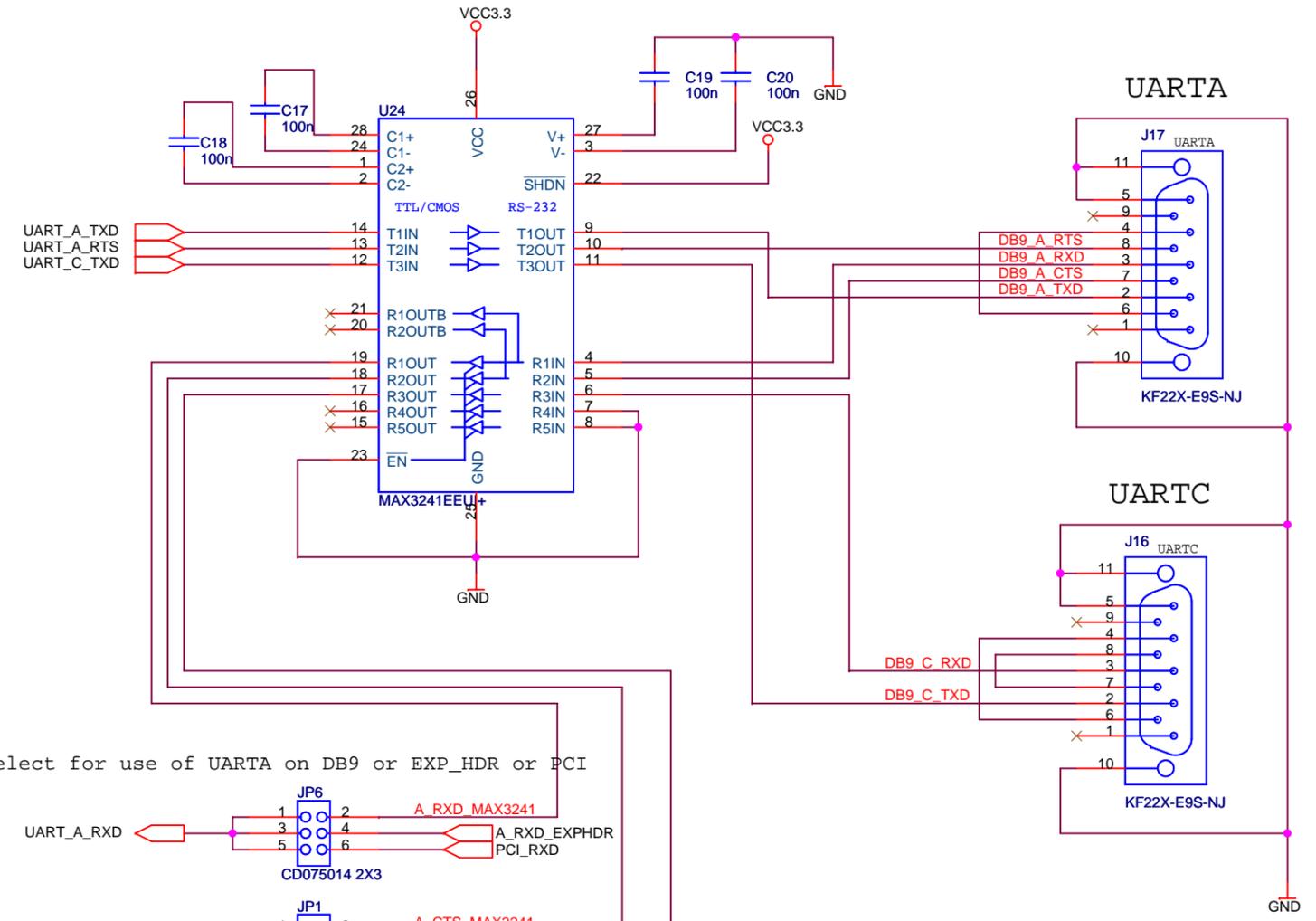
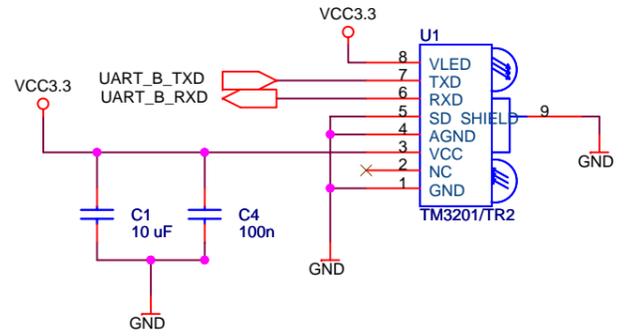
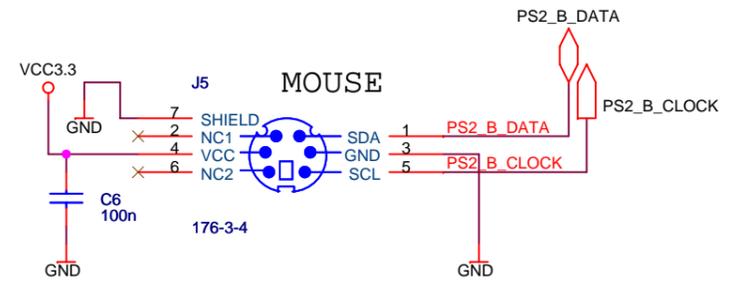
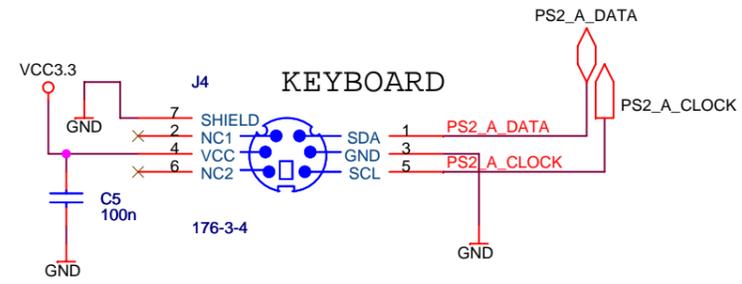
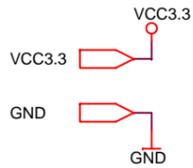
1.8V Regulator



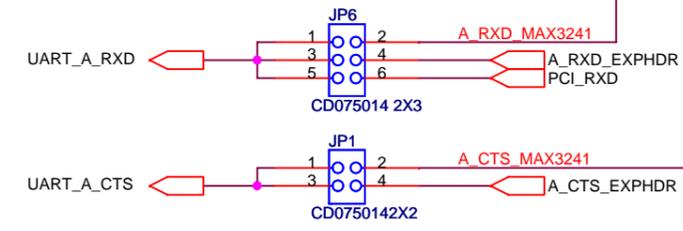
POWER LED

NOTE: Separate SGND and PGND planes are placed on the PCB for U3 and U4

ATMEL Norway AS Vestre Rosten 79 N-7075 TILLER Norway		
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Jumper Select for use of UARTA on DB9 or EXP_HDR or PCI



Jumper Select for use of UARTC on DB9 or EXP_HDR

