



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

SMP3003 — P-Channel Silicon MOSFET General-Purpose Switching Device Applications

Features

- ON-resistance $R_{DS(on)1}=6.2m\Omega$ (typ.)
- Input capacitance $C_{iss}=13400pF$ (typ.)
- 4V drive

Specifications

Absolute Maximum Ratings at $T_a=25^\circ C$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		-75	V
Gate-to-Source Voltage	V_{GSS}		± 20	V
Drain Current (DC)	I_D		-100	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu s$, duty cycle $\leq 1\%$	-400	A
Allowable Power Dissipation	P_D	$T_c=25^\circ C$	90	W
Channel Temperature	T_{ch}		150	$^\circ C$
Storage Temperature	T_{stg}		-55 to +150	$^\circ C$
Avalanche Energy (Single Pulse) *1	E_{AS}		468	mJ
Avalanche Current *2	I_{AV}		-60	A

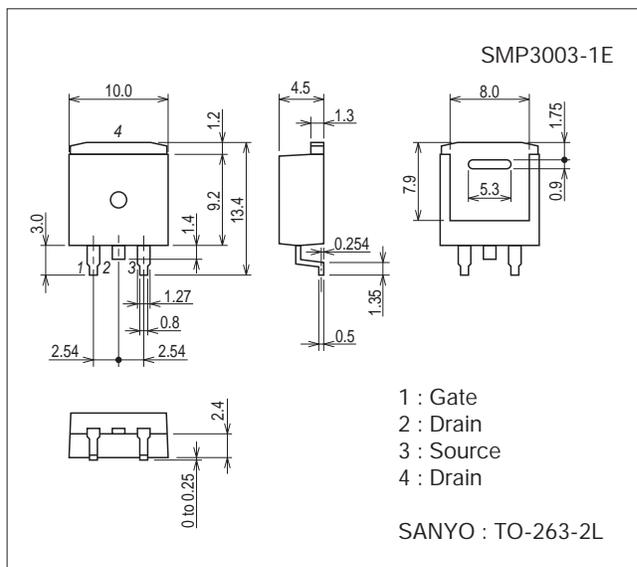
Note : *1 $V_{DD}=-48V$, $L=100\mu H$, $I_{AV}=-60A$ (Fig.1)

*2 $L \leq 100\mu H$, Single pulse

Package Dimensions

unit : mm (typ)

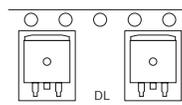
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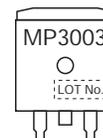
Product & Package Information

- Package : TO-263-2L
- JEITA, JEDEC : SC-83, TO-263
- Minimum Packing Quantity : 800 pcs./reel

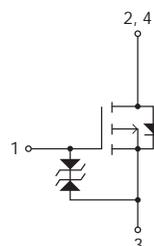
Packing Type: DL



Marking



Electrical Connection



SANYO Semiconductor Co., Ltd.

<http://semicon.sanyo.com/en/network>

SMP3003

Electrical Characteristics at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V(\text{BR})\text{DSS}$	$I_D=-1\text{mA}, V_{GS}=0\text{V}$	-75			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=-75\text{V}, V_{\text{GS}}=0\text{V}$			-10	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 16\text{V}, V_{\text{DS}}=0\text{V}$			± 10	μA
Cutoff Voltage	$V_{\text{GS(off)}}$	$V_{\text{DS}}=-10\text{V}, I_D=-1\text{mA}$	-1.2		-2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{\text{DS}}=-10\text{V}, I_D=-50\text{A}$		140		S
Static Drain-to-Source On-State Resistance	$R_{\text{DS(on)1}}$	$I_D=-50\text{A}, V_{\text{GS}}=-10\text{V}$		6.2	8.0	$\text{m}\Omega$
	$R_{\text{DS(on)2}}$	$I_D=-50\text{A}, V_{\text{GS}}=-4\text{V}$		8.0	11	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{\text{DS}}=-20\text{V}, f=1\text{MHz}$		13400		pF
Output Capacitance	C_{oss}			1000		pF
Reverse Transfer Capacitance	C_{rss}			740		pF
Turn-ON Delay Time	$t_{\text{d(on)}}$		See Fig.2		95	
Rise Time	t_{r}			1000		ns
Turn-OFF Delay Time	$t_{\text{d(off)}}$			800		ns
Fall Time	t_{f}			820		ns
Total Gate Charge	Q_{g}	$V_{\text{DS}}=-48\text{V}, V_{\text{GS}}=-10\text{V}, I_D=-100\text{A}$			280	
Gate-to-Source Charge	Q_{gs}			50		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			55		nC
Diode Forward Voltage	V_{SD}	$I_{\text{S}}=-100\text{A}, V_{\text{GS}}=0\text{V}$		-1.0	-1.5	V
Reverse Recovery Time	t_{rr}	See Fig.3		120		ns
Reverse Recovery Charge	Q_{rr}	$I_{\text{S}}=-100\text{A}, V_{\text{GS}}=0\text{V}, di/dt=-100\text{A}/\mu\text{s}$		380		nC

Fig.1 Avalanche Resistance Test Circuit

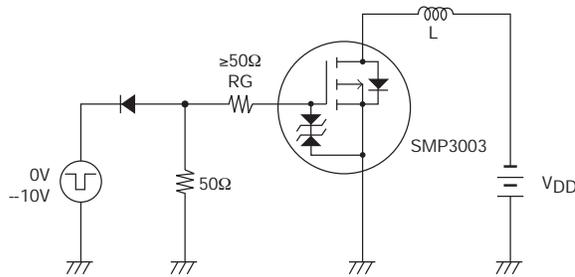


Fig.2 Switching Time Test Circuit

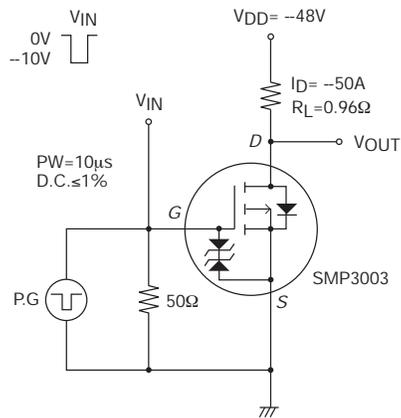
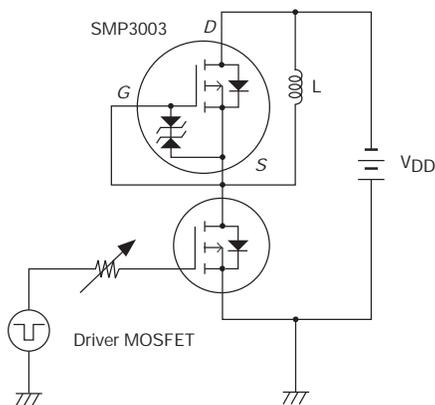
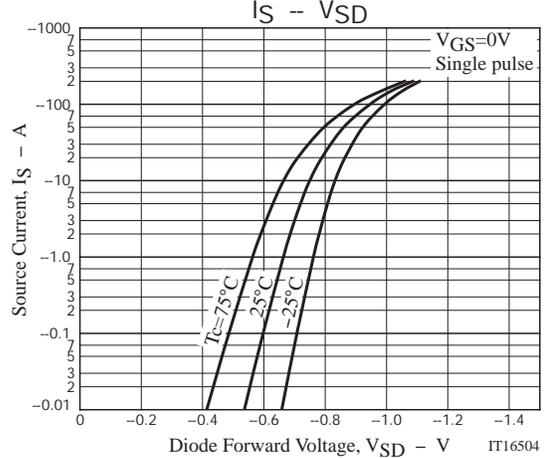
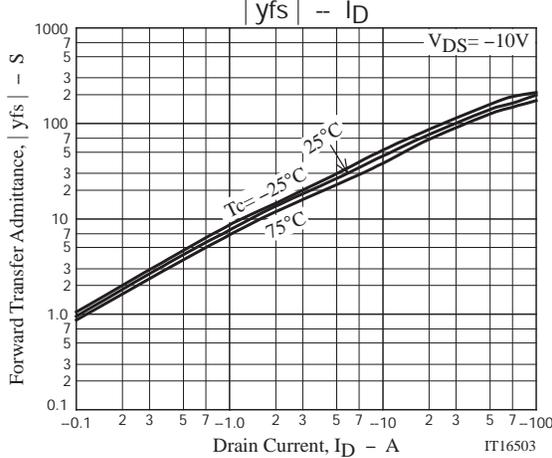
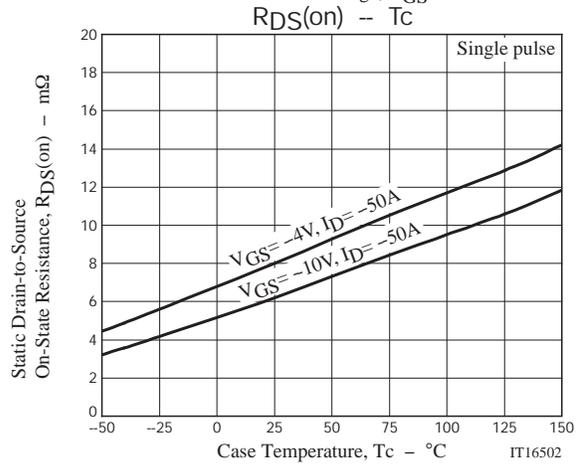
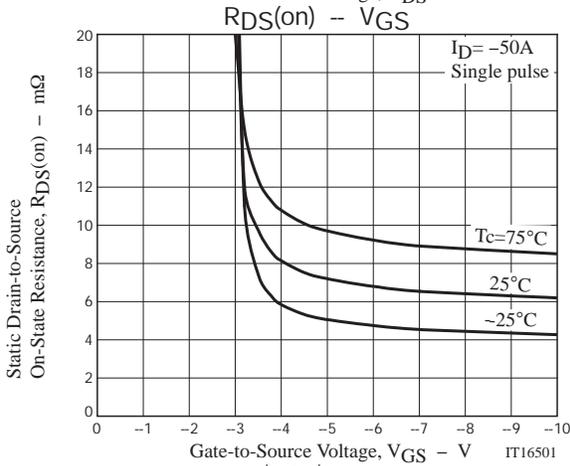
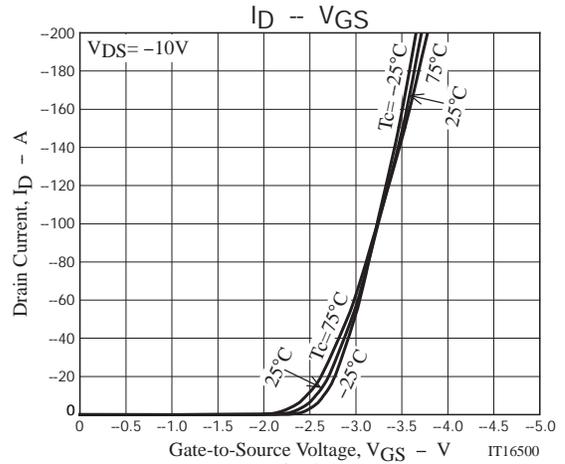
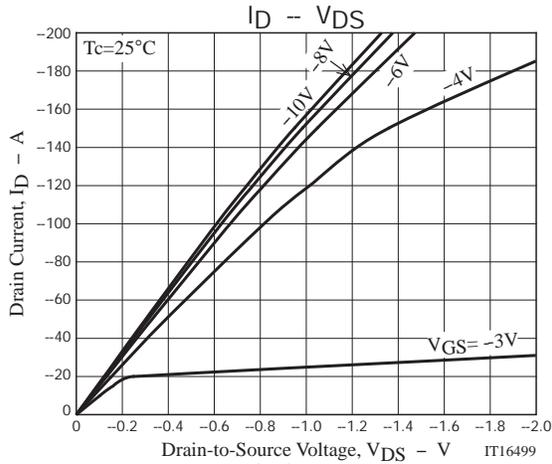


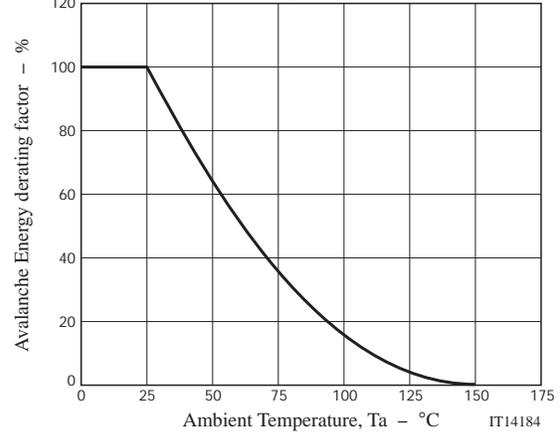
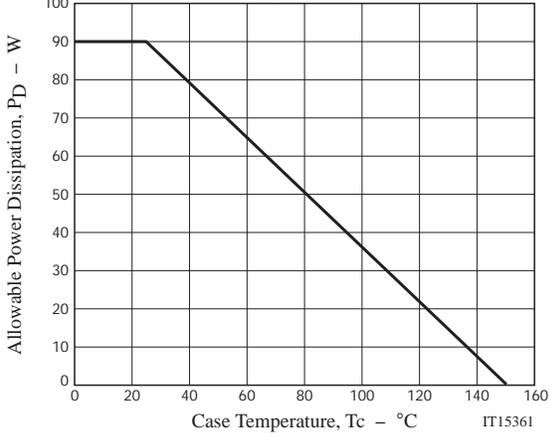
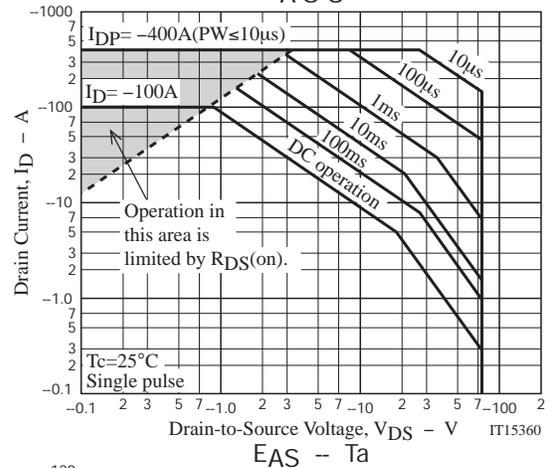
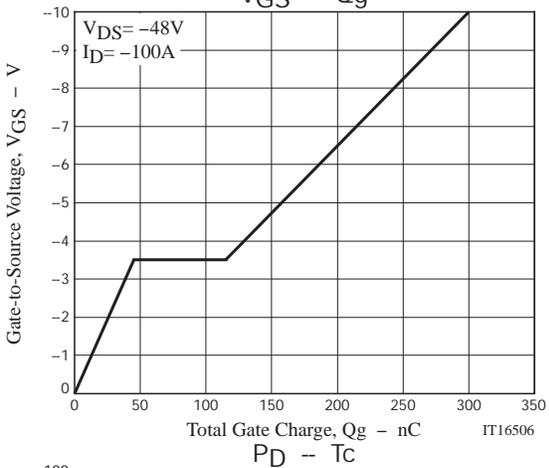
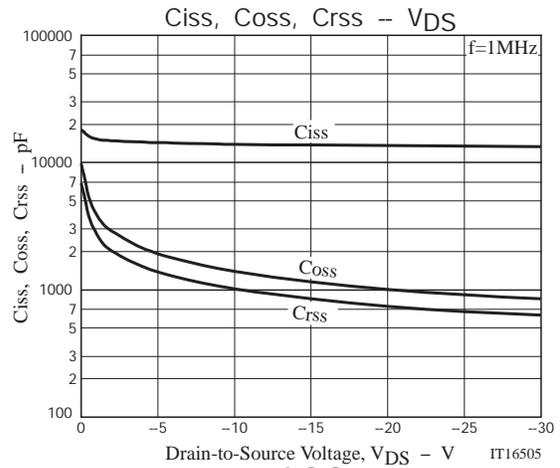
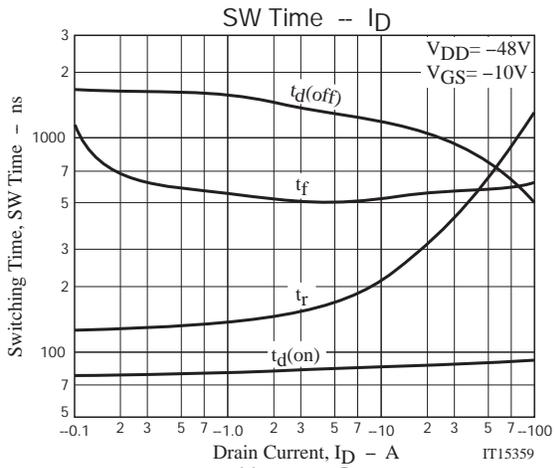
Fig.3 Reverse Recovery Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
SMP3003-DL-1E	TO-263-2L	800pcs./reel	Pb Free





SMP3003

Taping Specification SMP3003-DL-1E

1. Packing Format

Package Name	Maximum Number of devices contained (pcs)			Packing format	
	Reel	Inner box	Outer box	Inner BOX	Outer BOX
TO-263-2L	800	1600	6400	SPD-0V0011 2 reel contained Dimensions:mm (external) 351×340×68	SPD-0V0009 4 inner boxes contained Dimensions:mm (external) 390×370×318

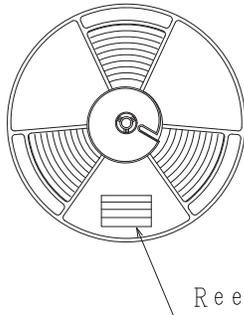
Reel label, Inner box label

Outer box label

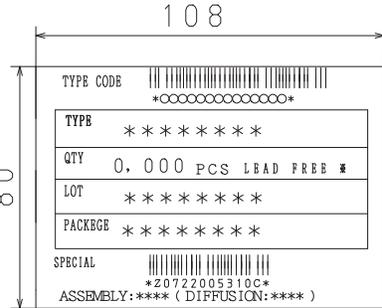
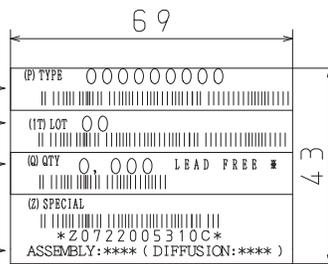
(unit:mm)

It is a label at the time of factory shipments.
The form of a label may change in physical
distribution process.

Packing method



Type No. →
LOT No. →
Quantity →
Origin →
Reel label



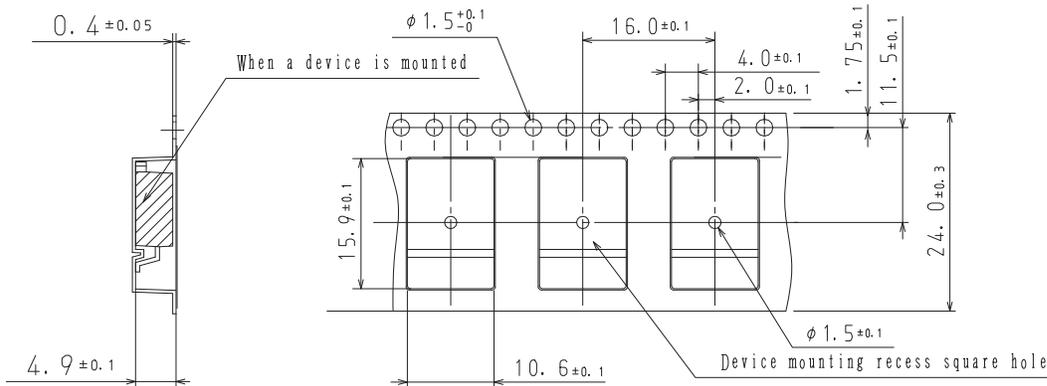
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

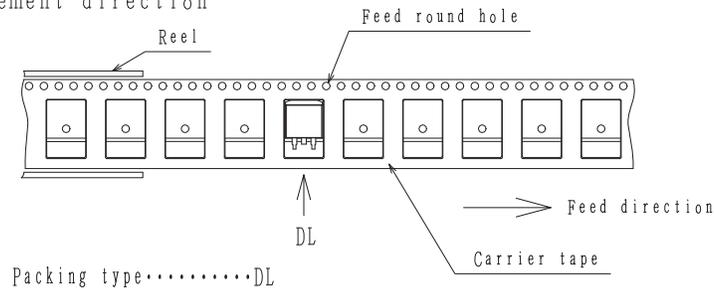
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A

2. Taping configuration

2-1. Carrier tape size (unit:mm)

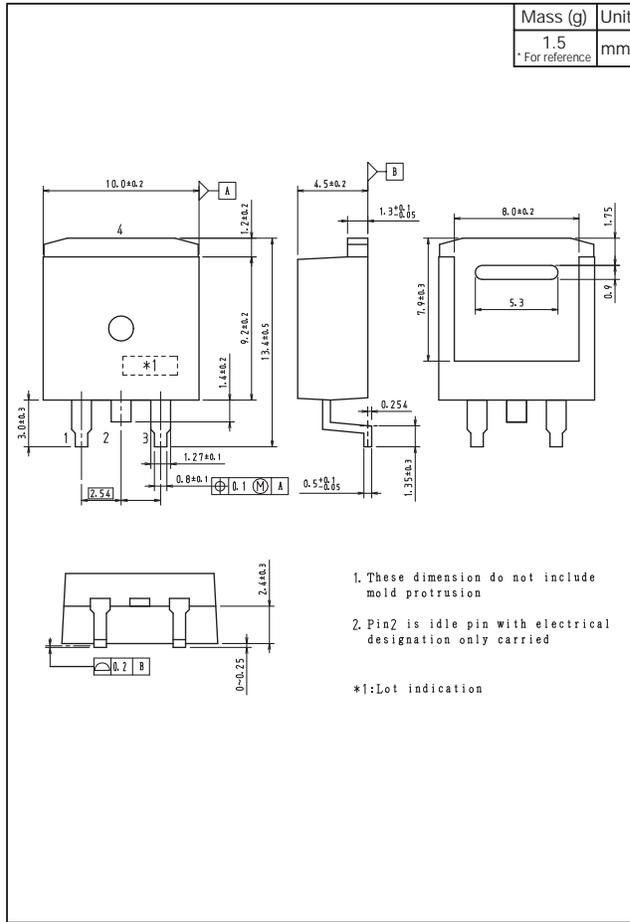


2-2. Device placement direction

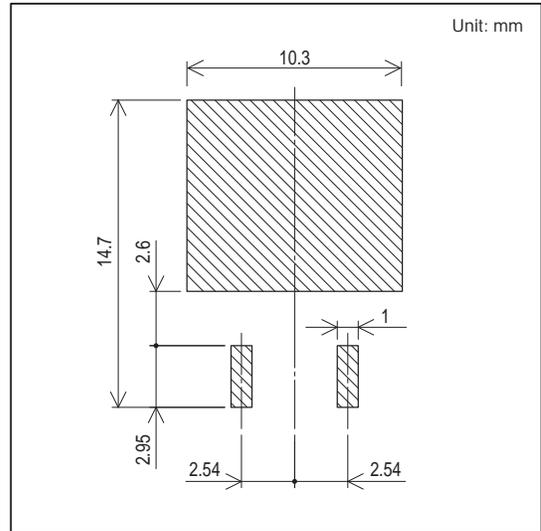


SMP3003

Outline Drawing SMP3003-DL-1E



Land Pattern Example



Note on usage : Since the SMP3003 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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