

Low forward voltage TVS: Transky™

Main applications

- Power rail ESD transient over-voltages and reverse voltages protection for 5 and 12 V supplied IC's

Description

The Transky is designed specifically for miniaturized electronic devices and equipment subject to ESD transient over-voltages. The Transky combines the performance of a Transil™ or TVS (Transient Voltage Suppressor) and low forward voltage Schottky diode in a monolithic structure.

It offers both an overshoot protection in the 6.4 V or 13.2 V clamping ranges and a negative spike protection in the -0.48 V clamping range compared to the -1 V with the standard Transil family on the 5 or 12 V power line.

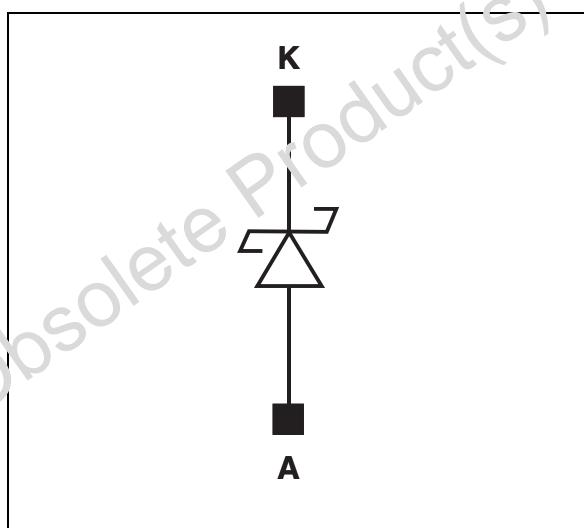
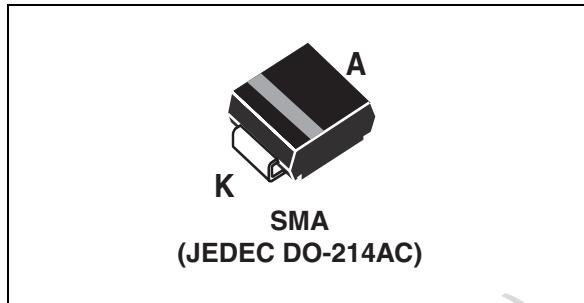
Its 600 W power capability offers high transient capability with SMA package.

Features

- Integration of a Transil with a Schottky diode
- JEDEC registered SMA package outline
- Low clamping factor V_{CL}/V_{BR}
- Fast response time
- RoHS compliant

Benefits

- Optimized PCB area: up to 62% space saving versus discrete solution
- High peak pulse power: up to 600 W
- Stand-off voltage:
5 V for SMTY5.0A
12 V for SMTY12A
- Low forward voltage: 0.48 V @ 1 A
- Very low leakage current:
10 μ A @ 5 V for SMTY5.0A
20 μ A @ 12 V for SMTY12A



Order code

Part number	Marking
SMTY5.0A	Y5.0
SMTY12A	Y12

Complies with following standard

IEC 61000-4-2 Level 4

Air discharge 15 kV
Contact discharge 8 kV

1 Characteristics

Table 1. Absolute ratings (limiting value)

Symbol	Parameter	Value	Unit
V_{pp}	IEC 61000-4-2 level 4 standard	15 8	kV
P	Power dissipation on infinite heatsink	$T_{amb} = 25^\circ C$	4
P_{PP}	Peak pulse Power dissipation ⁽¹⁾	T_j initial = T_{amb}	600
I_{FSM}	Non repetitive surge peak forward current	$t_p=10$ ms T_j initial = T_{amb}	40
T_{stg}	Storage temperature range	-65 to +175	°C
T_j	Maximum operating junction temperature ⁽²⁾	150	°C

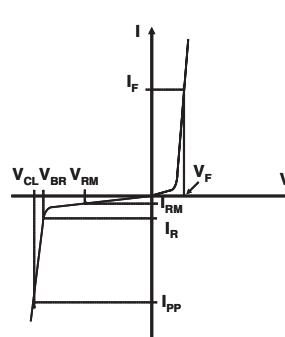
1. 10/1000μs pulse waveform

2. $\frac{dP_{tot}}{dT_j} < \frac{1}{R_{th(j-a)}}$ thermal runaway condition for a Transky

Table 2. Thermal resistance

Symbol	Parameter	Value	Unit
$R_{th(j-a)}$	Junction to ambient on printed circuit	120	°C/W
$R_{th(j-l)}$	Junction to lead	30	°C/W

Table 3. Electrical characteristics

Symbol	Parameter	
I_{RM}	Leakage current @ V_{RM}	
V_{RM}	Stand-off voltage	
V_{BR}	Breakdown voltage	
I_R	Reverse leakage current	
V_{CL}	Clamping voltage	
I_{PP}	Peak pulse current	
V_F	Forward voltage drop	

	$I_{RM\ max} @ V_{RM}$		$I_{RM\ max} @ V_{RM} @ 85^\circ C$		$V_{BR\ min} @ I_R$		$V_{CL\ max} @ I_{PP}$ 10/1000 μs		$V_F\ max @ 1A$ ⁽¹⁾		$\alpha T\ max$
	μA	V	mA	V	V	mA	V	A	V	10 ⁻⁴ /°C	
SMTY5.0A	10	5	0.5	5	6.4	10	9	43.5	0.48	10	
SMTY12A	20	12	1.2	12	13.2	1	18.5	31	0.48	10	

1. Pulse test $t_p = 500$ μs, $\delta < 2\%$

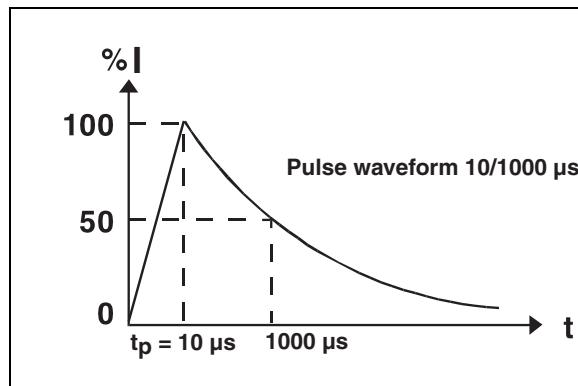
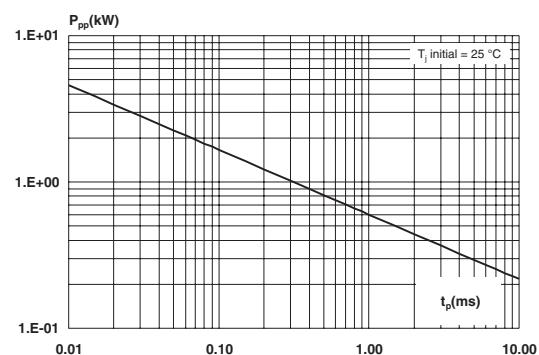
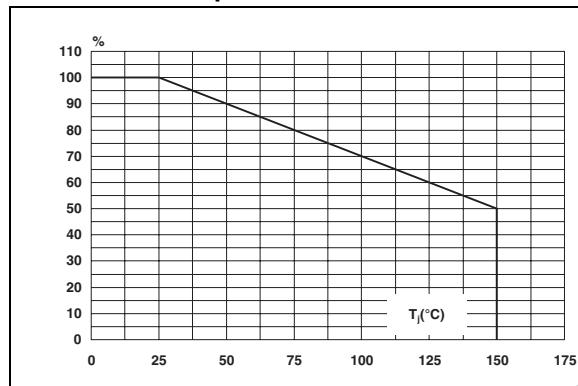
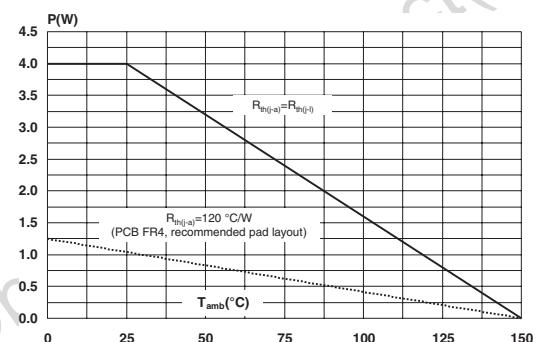
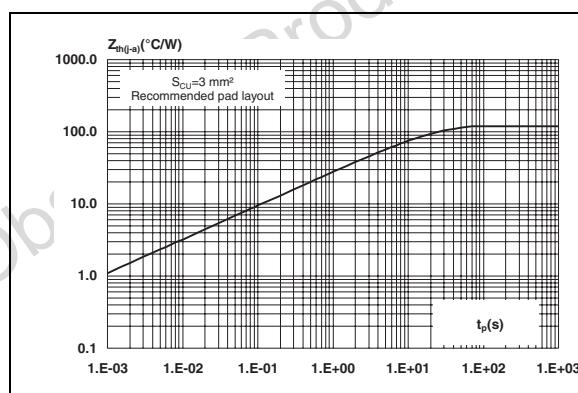
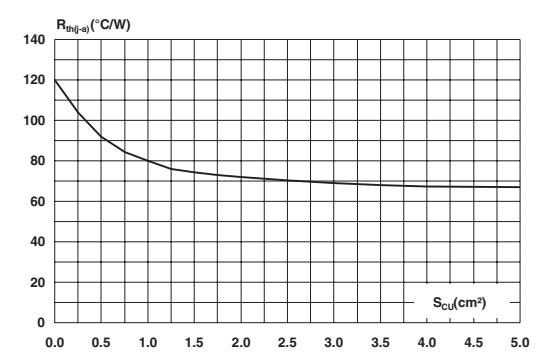
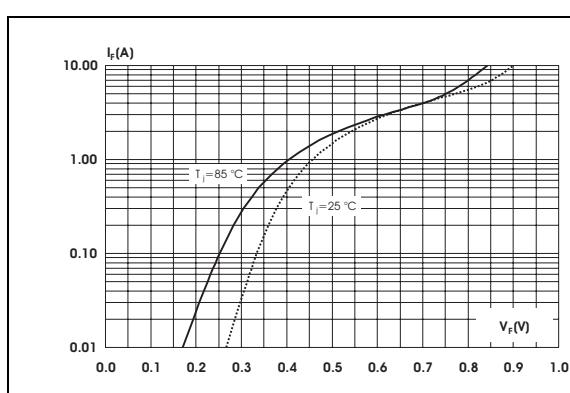
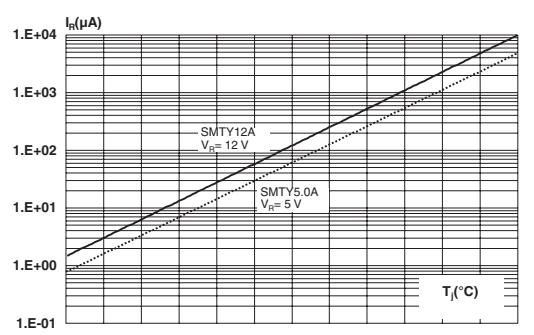
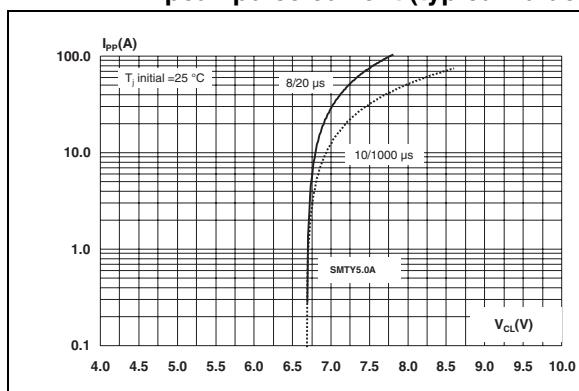
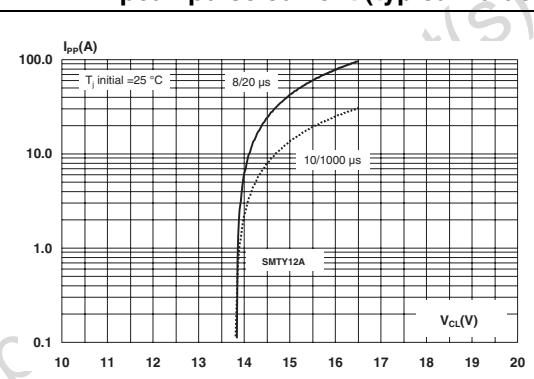
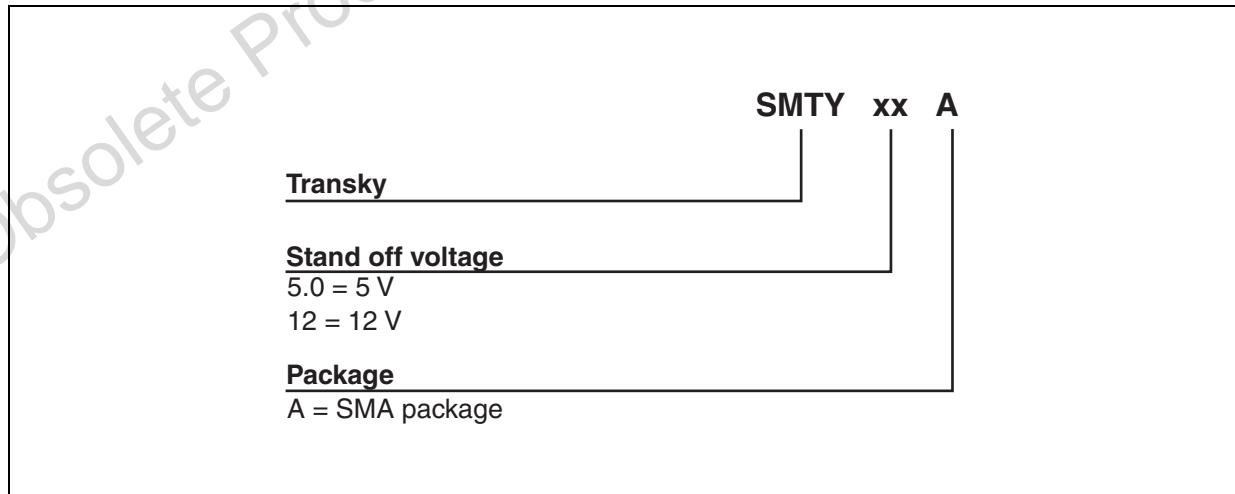
Figure 1. Pulse waveform (10/1000 μ s)**Figure 2. Peak pulse power versus exponential pulse duration (T_j initial = 25°C)****Figure 3. Relative variation of peak pulse power versus initial junction temperature****Figure 4. Average power dissipation versus ambient temperature****Figure 5. Variation of thermal impedance, junction to ambient, versus pulse duration (Epoxy, FR4, $e_{\text{Cu}} = 35 \mu\text{m}$)****Figure 6. Thermal resistance, junction to ambient, versus copper surface under each lead (printed circuit board FR4, $e_{\text{Cu}} = 35 \mu\text{m}$)**

Figure 7. Forward voltage drop versus forward current (typical values)**Figure 8. Reverse leakage current versus junction temperature (typical values)****Figure 9. SMTY5.0A Clamping voltage versus peak pulse current (typical values)****Figure 10. SMTY12A Clamping voltage versus peak pulse current (typical values)**

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Ordering information scheme



3 Package information

Table 4. SMA (plastic) dimensions

Ref.	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A1	1.90	2.03	0.075	0.080
A2	0.05	0.20	0.002	0.008
b	1.25	1.65	0.049	0.065
c	0.15	0.41	0.006	0.016
E	4.80	5.60	0.189	0.220
E1	3.95	4.60	0.156	0.181
D	2.25	2.95	0.089	0.116
L	0.75	1.60	0.030	0.063

Figure 11. Footprint dimensions (millimeter)

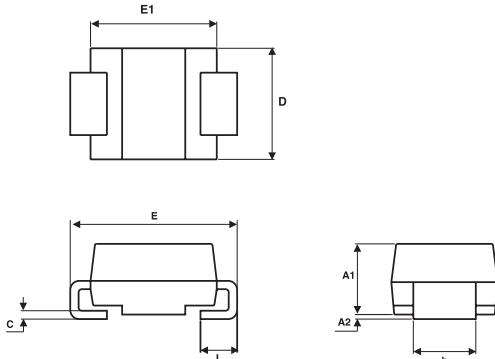
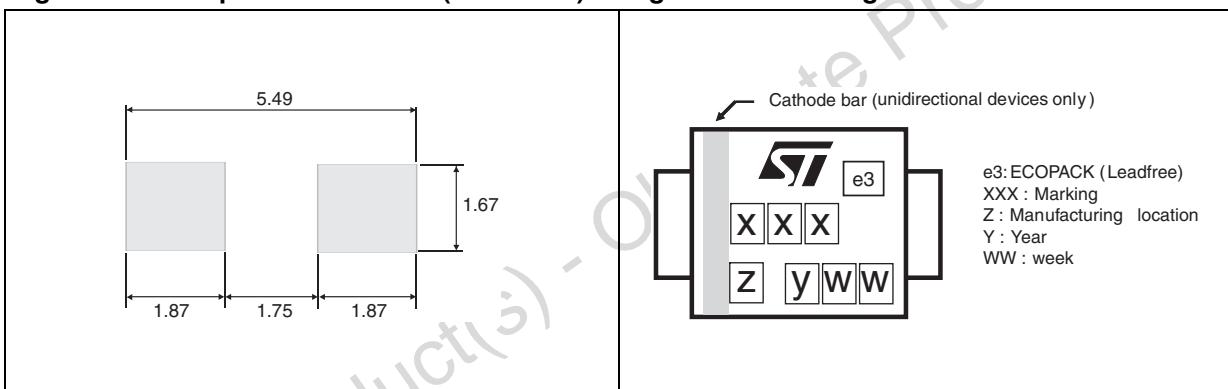


Figure 12. Marking information scheme



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

4 Ordering information

Ordering type	Marking	Package	Weight	Base quantity	Delivery mode
SMTY5.0A	Y5.0	SMA	0.068g	5000	Tape and Reel
SMTY12A	Y12	SMA	0.068g	5000	Tape and Reel

5 Revision history

Date	Revision	Changes
24-Apr-2006	1	Initial release.

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