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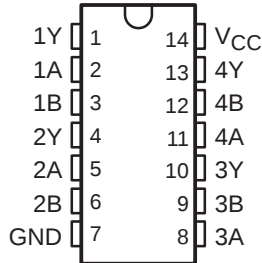
Jameco Part Number 246414

SN54HCT02, SN74HCT02 QUADRUPLE 2-INPUT POSITIVE-NOR GATES

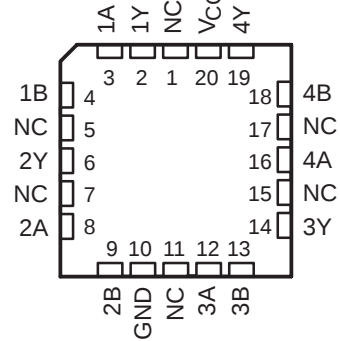
SCLS065E – NOVEMBER 1988 – REVISED JULY 2003

- Operating Voltage Range of 4.5 V to 5.5 V
- Outputs Can Drive Up To 10 LSTTL Loads
- Low Power Consumption, 20-μA Max I_{CC}
- Typical $t_{pd} = 10$ ns
- ± 4 -mA Output Drive at 5 V
- Low Input Current of 1 μA Max
- Inputs Are TTL-Voltage Compatible

SN54HCT02 . . . J OR W PACKAGE
SN74HCT02 . . . D, DB, N, NS, OR PW PACKAGE
(TOP VIEW)



SN54HCT02 . . . FK PACKAGE
(TOP VIEW)



NC – No internal connection

description/ordering information

These devices contain four independent 2-input NOR gates. They perform the Boolean function $Y = \bar{A} \cdot \bar{B}$ or $Y = A + B$ in positive logic.

ORDERING INFORMATION

T _A	PACKAGE [†]		ORDERABLE PART NUMBER	TOP-SIDE MARKING
–40°C to 85°C	PDIP – N	Tube of 25	SN74HCT02N	SN74HCT02N
	SOIC – D	Tube of 50	SN74HCT02D	HCT02
		Reel of 2500	SN74HCT02DR	
		Reel of 250	SN74HCT02DT	
	SOP – NS	Reel of 2000	SN74HCT02NSR	HCT02
	SSOP – DB	Reel of 2000	SN74HCT02DBR	HT02
	TSSOP – PW	Tube of 90	SN74HCT02PW	HT02
		Reel of 2000	SN74HCT02PWR	
Reel of 250		SN74HCT02PWT		
–55°C to 125°C	CDIP – J	Tube of 25	SNJ54HCT02J	SNJ54HCT02J
	CFP – W	Tube of 150	SNJ54HCT02W	SNJ54HCT02W
	LCCC – FK	Tube of 55	SNJ54HCT02FK	SNJ54HCT02FK

[†] Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



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SN54HCT02, SN74HCT02

QUADRUPLE 2-INPUT POSITIVE-NOR GATES

SCLS065E – NOVEMBER 1988 – REVISED JULY 2003

FUNCTION TABLE
(each gate)

INPUTS		OUTPUT
A	B	Y
H	X	L
X	H	L
L	L	H

logic diagram, each gate (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage range, V_{CC}	–0.5 V to 7 V
Input clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{CC}$) (see Note 1)	±20 mA
Output clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{CC}$) (see Note 1)	±20 mA
Continuous output current, I_O ($V_O = 0$ to V_{CC})	±25 mA
Continuous current through V_{CC} or GND	±50 mA
Package thermal impedance, θ_{JA} (see Note 2):	
D package	86°C/W
DB package	96°C/W
N package	80°C/W
NS package	76°C/W
PW package	113°C/W

Storage temperature range, T_{stg} –65°C to 150°C

[†] Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
2. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions (see Note 3)

		SN54HCT02			SN74HCT02			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V_{CC}	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V_{IH}	High-level input voltage	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$		2	2			V
V_{IL}	Low-level input voltage	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$		0.8	0.8			V
V_I	Input voltage	0		V_{CC}	0		V_{CC}	V
V_O	Output voltage	0		V_{CC}	0		V_{CC}	V
$\Delta t/\Delta v$	Input transition rise/fall time			500			500	ns
T_A	Operating free-air temperature	–55		125	–40		85	°C

NOTE 3: All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

SN54HCT02, SN74HCT02 QUADRUPLE 2-INPUT POSITIVE-NOR GATES

SCLS065E – NOVEMBER 1988 – REVISED JULY 2003

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		V _{CC}	T _A = 25°C			SN54HCT02		SN74HCT02		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
V _{OH}	V _I = V _{IH} or V _{IL}	I _{OH} = -20 µA	4.5 V	4.4	4.499		4.4		4.4		V
		I _{OH} = -4 mA		3.98	4.3		3.7		3.84		
V _{OL}	V _I = V _{IH} or V _{IL}	I _{OL} = 20 µA	4.5 V		0.001	0.1		0.1		0.1	V
		I _{OL} = 4 mA			0.17	0.26		0.4		0.33	
I _I	V _I = V _{CC} or 0		5.5 V		±0.1	±100		±1000		±1000	nA
I _{CC}	V _I = V _{CC} or 0, I _O = 0		5.5 V			2		40		20	µA
ΔI _{CC} [†]	One input at 0.5 V or 2.4 V, Other inputs at 0 or V _{CC}		5.5 V		1.4	2.4		3		2.9	mA
C _i			4.5 V to 5.5 V		3	10		10		10	pF

[†] This is the increase in supply current for each input that is at one of the specified TTL voltage levels, rather than 0 V or V_{CC}.

switching characteristics over recommended operating free-air temperature range, C_L = 50 pF (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC}	T _A = 25°C			SN54HCT02		SN74HCT02		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t _{pd}	A or B	Y	4.5 V		11	20		30		25	ns
			5.5 V		10	18		27		22	
t _t		Y	4.5 V		9	15		22		19	ns
			5.5 V		8	14		20		17	

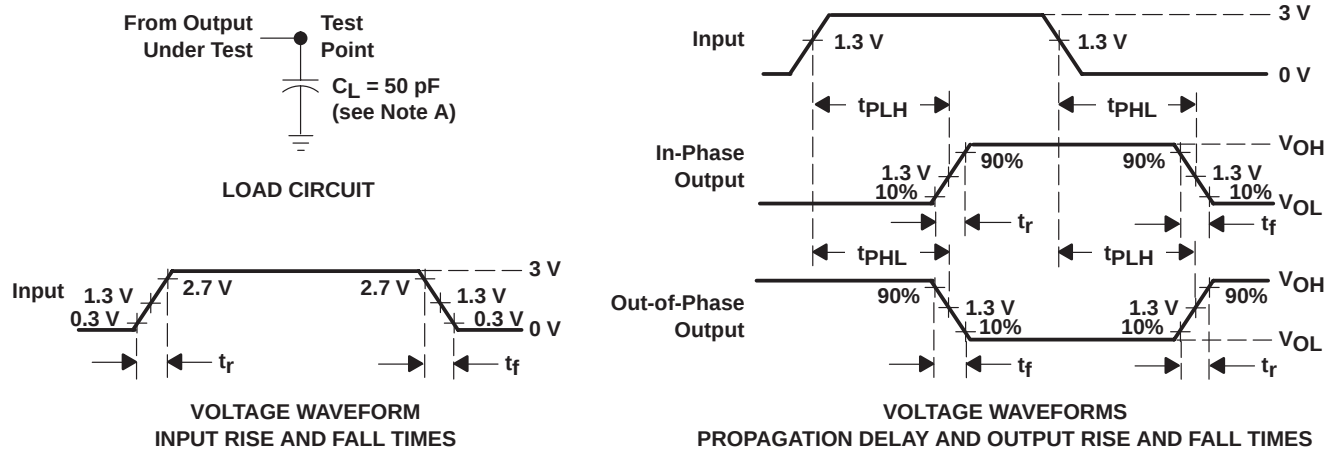
operating characteristics, T_A = 25°C

PARAMETER	TEST CONDITIONS	TYP	UNIT
C _{pd} Power dissipation capacitance	No load	20	pF

SN54HCT02, SN74HCT02 QUADRUPLE 2-INPUT POSITIVE-NOR GATES

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PARAMETER MEASUREMENT INFORMATION



- NOTES:
- A. C_L includes probe and test-fixture capacitance.
 - B. Phase relationships between waveforms were chosen arbitrarily. All input pulses are supplied by generators having the following characteristics: $PRR \leq 1 \text{ MHz}$, $Z_O = 50 \Omega$, $t_r = 6 \text{ ns}$, $t_f = 6 \text{ ns}$.
 - C. The outputs are measured one at a time with one input transition per measurement.
 - D. t_{PLH} and t_{PHL} are the same as t_{pd} .

Figure 1. Load Circuit and Voltage Waveforms

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
SN74HCT02D	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02DE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02DR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02DRE4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02DT	ACTIVE	SOIC	D	14	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02DTE4	ACTIVE	SOIC	D	14	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02N	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN74HCT02NE4	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN74HCT02NSR	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02NSRE4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02PW	ACTIVE	TSSOP	PW	14	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02PWE4	ACTIVE	TSSOP	PW	14	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02PWR	ACTIVE	TSSOP	PW	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02PWRE4	ACTIVE	TSSOP	PW	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02PWT	ACTIVE	TSSOP	PW	14	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HCT02PWTE4	ACTIVE	TSSOP	PW	14	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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N (R-PDIP-T**)

16 PINS SHOWN

PLASTIC DUAL-IN-LINE PACKAGE



PINS ** DIM	14	16	18	20
A MAX	0.775 (19,69)	0.775 (19,69)	0.920 (23,37)	1.060 (26,92)
A MIN	0.745 (18,92)	0.745 (18,92)	0.850 (21,59)	0.940 (23,88)
MS-001 VARIATION	AA	BB	AC	AD



4040049/E 12/2002

NOTES:

- A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
-  Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
 The 20 pin end lead shoulder width is a vendor option, either half or full width.

D (R-PDSO-G14)

PLASTIC SMALL-OUTLINE PACKAGE



4040047-3/F 07/2004

MECHANICAL DATA

NS (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN



- NOTES:
- All linear dimensions are in millimeters.
 - This drawing is subject to change without notice.
 - Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

PW (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
 D. Falls within JEDEC MO-153

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Features	Samples	Technical Documents
Quality & Pb-Free Data	Pricing/Packaging	Applications Notes
Related Products	Inventory	Simulation Models
Tools & Software	Symbols/Footprints	Reference Designs

Datasheet



Download Datasheet SN54HCT02, SN74HCT02 (Rev. E) (sn74hct02.pdf, 334 KB)
28 Jul 2003 Download

	SN74HCT02
Voltage Nodes(V)	5
Vcc range(V)	4.5 to 5.5
Input Level	TTL
Output Level	CMOS
Output Drive(mA)	-4/4
No. of Gates	4
Static Current	0.02
tpd max(ns)	22
	Samples
	Inventory

Product Information

Features Save this to your personal library

Operating Voltage Range of 4.5 V to 5.5 V
Outputs Can Drive Up To 10 LSTTL Loads
Low Power Consumption, 20-µA Max I_{CC}
Typical t_{pd} = 10 ns
±4-mA Output Drive at 5 V
Low Input Current of 1 µA Max
Inputs Are TTL-Voltage Compatible

Description

These devices contain four independent 2-input NOR gates. They perform the Boolean function $Y = A \downarrow B$ or $Y = (A + B) \downarrow$ in positive logic.

Refine Your Selection
- Logic: NOR Gates

Support
- KnowledgeBase
- Contact Technical Support
- TI Cross Reference
- Training
- Part Marking Lookup
- Part Number Nomenclature



Pricing/Packaging/CAD Design Tools/Samples

☐			Price	Packaging			CAD Design Tools	Samples
Device	Status	Temp (°C)	Budget Price (\$US) QTY	Industry Standard (TI Pkg) Pins	Top Side Marking	Standard Pack Quantity	Footprints	Samples
SN74HCT02D	ACTIVE	-40 to 85	0.15 1KU	SOIC (D) 14	View	50	☐	Purchase Samples
SN74HCT02DE4	ACTIVE	-40 to 85	0.15 1KU	SOIC (D) 14	View	50	☐	Purchase Samples
SN74HCT02DR	ACTIVE	-40 to 85	0.15 1KU	SOIC (D) 14	View	2500	☐	Contact TI Distributor or Sales Office
SN74HCT02DRE4	ACTIVE	-40 to 85	0.15 1KU	SOIC (D) 14	View	2500	☐	Purchase Samples
SN74HCT02DT	ACTIVE	-40 to 85	0.29 1KU	SOIC (D) 14	View	250	☐	Purchase Samples
SN74HCT02DTE4	ACTIVE	-40 to 85	0.29 1KU	SOIC (D) 14	View	250	☐	Purchase Samples
SN74HCT02N	ACTIVE	-40 to 85	0.23 1KU	PDIP (N) 14	View	25	☐	Contact TI Distributor or Sales Office
SN74HCT02NE4	ACTIVE	-40 to 85	0.23 1KU	PDIP (N) 14	View	25	☐	Request Free Samples
SN74HCT02NSR	ACTIVE	-40 to 85	0.15 1KU	SO (NS) 14	View	2000	☐	Purchase Samples
SN74HCT02NSRE4	ACTIVE	-40 to 85	0.15 1KU	SO (NS) 14	View	2000	☐	Purchase Samples
SN74HCT02PW	ACTIVE	-40 to 85	0.15 1KU	TSSOP (PW) 14	View	90	☐	Purchase Samples
SN74HCT02PWE4	ACTIVE	-40 to 85	0.15 1KU	TSSOP (PW) 14	View	90	☐	Purchase Samples
SN74HCT02PWR	ACTIVE	-40 to 85	0.15 1KU	TSSOP (PW) 14	View	2000	☐	Purchase Samples
SN74HCT02PWRE4	ACTIVE	-40 to 85	0.15 1KU	TSSOP (PW) 14	View	2000	☐	Purchase Samples
SN74HCT02PWT	ACTIVE	-40 to 85	0.29 1KU	TSSOP (PW) 14	View	250	☐	Purchase Samples
SN74HCT02PWTE4	ACTIVE	-40 to 85	0.29 1KU	TSSOP (PW) 14	View	250	☐	Purchase Samples

Inventory

☐	TI Inventory Status			Reported Distributor Inventory			
SN74HCT02D	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	5300 16 Jan	10 Weeks	Americas	Arrow	>1k	
		1000 27 Jan			DigiKey	>1k	
		1000 24 Feb			Newark InOne	>1k	
		>10k 6 Mar		Europe	Abacus Polar	>1k	
					Arrow Southern Europe	>1k	
					Avnet-SILICA	>1k	
					Rutronik	850	
SN74HCT02DE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	5300 16 Jan	10 Weeks	None Reported View Distributors			
		1000 27 Jan					
		1000 24 Feb					
		>10k 6 Mar					

View all Distributors

Choose a Region



SN74HCT02DR	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 6 Feb	10 Weeks	Americas	DigiKey	>1k	
				Europe	Arrow Southern Europe	>1k	
					EBV Elektronik	>1k	
SN74HCT02DRE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 6 Feb	10 Weeks	None Reported View Distributors			
SN74HCT02DT	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 6 Mar	14 Weeks	None Reported View Distributors			
SN74HCT02DTE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 6 Mar	14 Weeks	None Reported View Distributors			
SN74HCT02N	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	>10k*	4426 30 Nov	10 Weeks	Americas	Avnet	>1k	
		>10k 23 Jan			DigiKey	>1k	
					Newark InOne	>1k	
				Asia	P&S	950	
				Europe	Abacus Polar	>1k	
					Arrow Northern Europe	775	
					Arrow Southern Europe	50	
					Avnet-SILICA	>1k	
					EBV Elektronik	100	
					Rutronik	>1k	
					Spoerle	>1k	
SN74HCT02NE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	>10k*	4426 30 Nov	10 Weeks	None Reported View Distributors			
		>10k 23 Jan					
SN74HCT02NSR	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	629 20 Jan	12 Weeks	None Reported View Distributors			
		973 27 Jan					
		1967 17 Feb					
		609 24 Feb					
		4919 3 Mar					

SN74HCT02NSRE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	629 20 Jan	12 Weeks	None Reported View Distributors			
		973 27 Jan					
		1967 17 Feb					
		609 24 Feb					
		4919 3 Mar					
SN74HCT02PW	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 3 Apr	16 Weeks	Europe	EBV Elektronik	900	
					Spoerle	>1k	
SN74HCT02PWE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 3 Apr	16 Weeks	None Reported View Distributors			
SN74HCT02PWR	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 3 Apr	16 Weeks	None Reported View Distributors			
SN74HCT02PWRE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k 3 Apr	16 Weeks	None Reported View Distributors			
SN74HCT02PWT	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*		16 Weeks	None Reported View Distributors			
SN74HCT02PWTE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
	0*		16 Weeks	None Reported View Distributors			

* Our information is updated daily, so please check back with us soon if this does not meet your needs. You may also contact your [TI Authorized Distributor](#), including those [listed above](#), for real time stock information.

** Lead time information is not available at this time. However, our information is updated daily so please check back with us soon. Please contact your preferred [TI Authorized Distributor](#) for additional information.

	Product Content				MTBF/FIT Rate
Device	Eco Plan*	Lead/Ball Finish	MSL Rating/Peak Reflow	Details	Details
SN74HCT02D	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02DE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02DR	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02DRE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02DT	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02DTE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02N	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC	View	View
SN74HCT02NE4	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC	View	View
SN74HCT02NSR	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02NSRE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02PW	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02PWE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02PWR	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02PWRE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02PWT	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74HCT02PWTE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View

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