

# LM231B – 2323 Middle Power LED



## Introduction

### Features

- Beam Angle : 120°
- Precondition : JEDEC Level 2a
- Dimension : 2.3 x 2.3 x 0.7 mm
- ESD withstand Voltage : up to  $\pm 5$ KV [HBM]
- Reliability Test : LM-80 qualified

## SAMSUNG ELECTRONICS

95, Samsung2-Ro, Giheung-Gu,  
Yongin-City, Gyeonggi-Do 446-711, KOREA

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# 1. Product Code Information

## 1) Luminous Flux Bins ( $T_s = 25^\circ\text{C}$ )

Nominal CCT	Product Code	Flux Rank	Sorting Condition $I_m @65\text{mA}$	
			Flux Bin	Intensity Range (cd)
				Flux Range ( $\Phi_v, I_m$ )
2700K	SPMWHT223MD5WAW0S0 SPMWHT223MD5WAWKS0 SPMWHT223MD5WAWMS0 SPMWHT223MD5WAWHS0	S0	S1	19.81 ~ 22.78
			S2	22.78 ~ 26.20
			S3	26.20 ~ 30.13
3000K	SPMWHT223MD5WAV0S0 SPMWHT223MD5WAVKS0 SPMWHT223MD5WAVMS0 SPMWHT223MD5WAVHS0	S0	S1	19.81 ~ 22.78
			S2	22.78 ~ 26.20
			S3	26.20 ~ 30.13
3500K	SPMWHT223MD5WAU0S0 SPMWHT223MD5WAUKS0 SPMWHT223MD5WAUMS0 SPMWHT223MD5WAUHS0	S0	S1	19.81 ~ 22.78
			S2	22.78 ~ 26.20
			S3	26.20 ~ 30.13
4000K	SPMWHT223MD5WAT0S0 SPMWHT223MD5WATKS0 SPMWHT223MD5WATMS0 SPMWHT223MD5WATHS0	S0	S1	20.55 ~ 23.56
			S2	23.56 ~ 27.09
			S3	27.09 ~ 31.16
5000K	SPMWHT223MD5WAR0S0 SPMWHT223MD5WARKS0 SPMWHT223MD5WARMS0	S0	S1	20.55 ~ 23.56
			S2	23.56 ~ 27.09
			S3	27.09 ~ 31.16
5700K	SPMWHT223MD5WAQ0S0 SPMWHT223MD5WAQKS0 SPMWHT223MD5WAQMS0	S0	S1	20.55 ~ 23.56
			S2	23.56 ~ 27.09
			S3	27.09 ~ 31.16
6500K	SPMWHT223MD5WAP0S0 SPMWHT223MD5WAPKS0 SPMWHT223MD5WAPMS0	S0	S1	20.55 ~ 23.56
			S2	23.56 ~ 27.09
			S3	27.09 ~ 31.16

**Notes:**

SAMSUNG ELECTRONICS maintains a tolerance of  $\pm 5\%$  on Luminous Flux measurements

## 2) Color Bins ( $T_s = 25^\circ\text{C}$ )

### 1) Color Binning

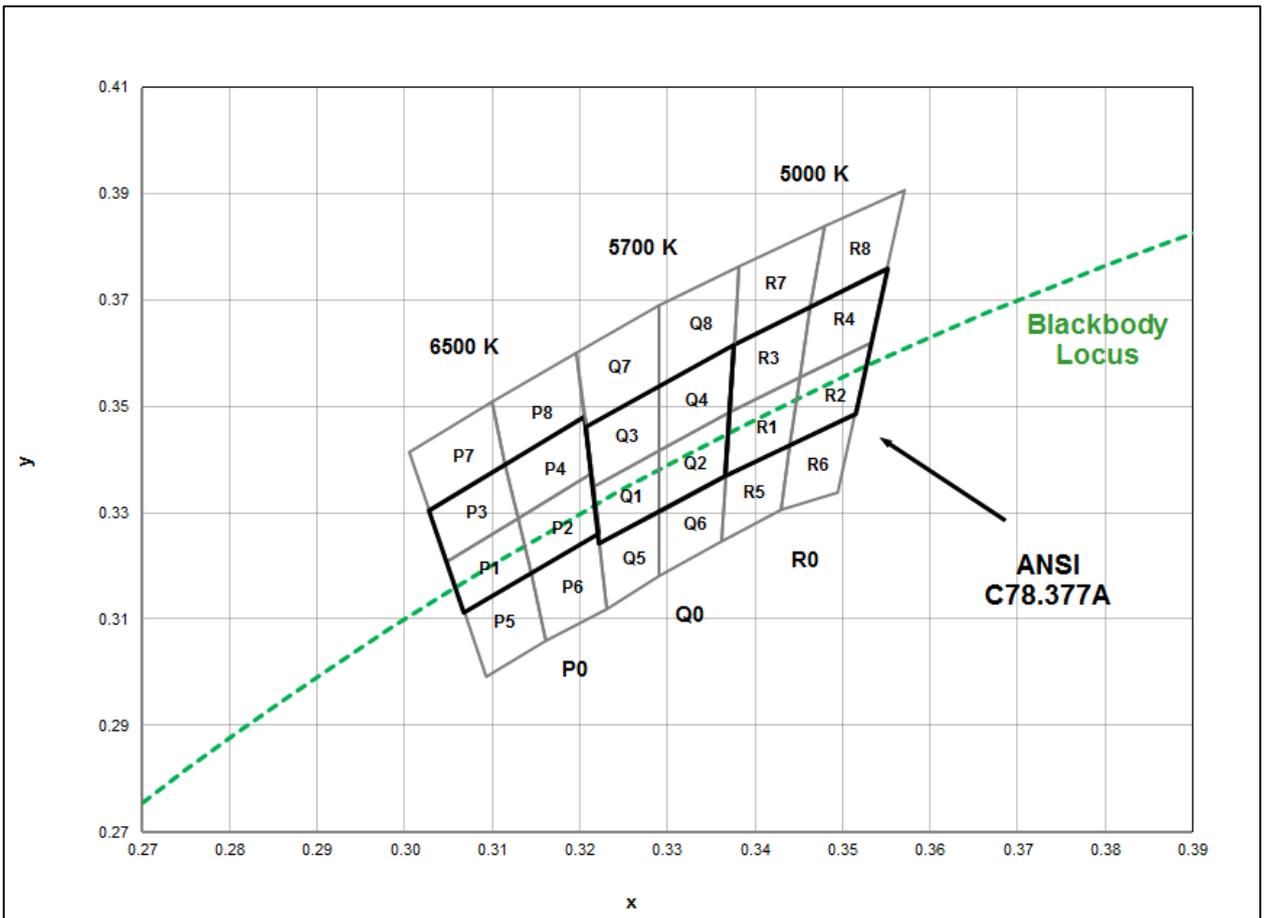
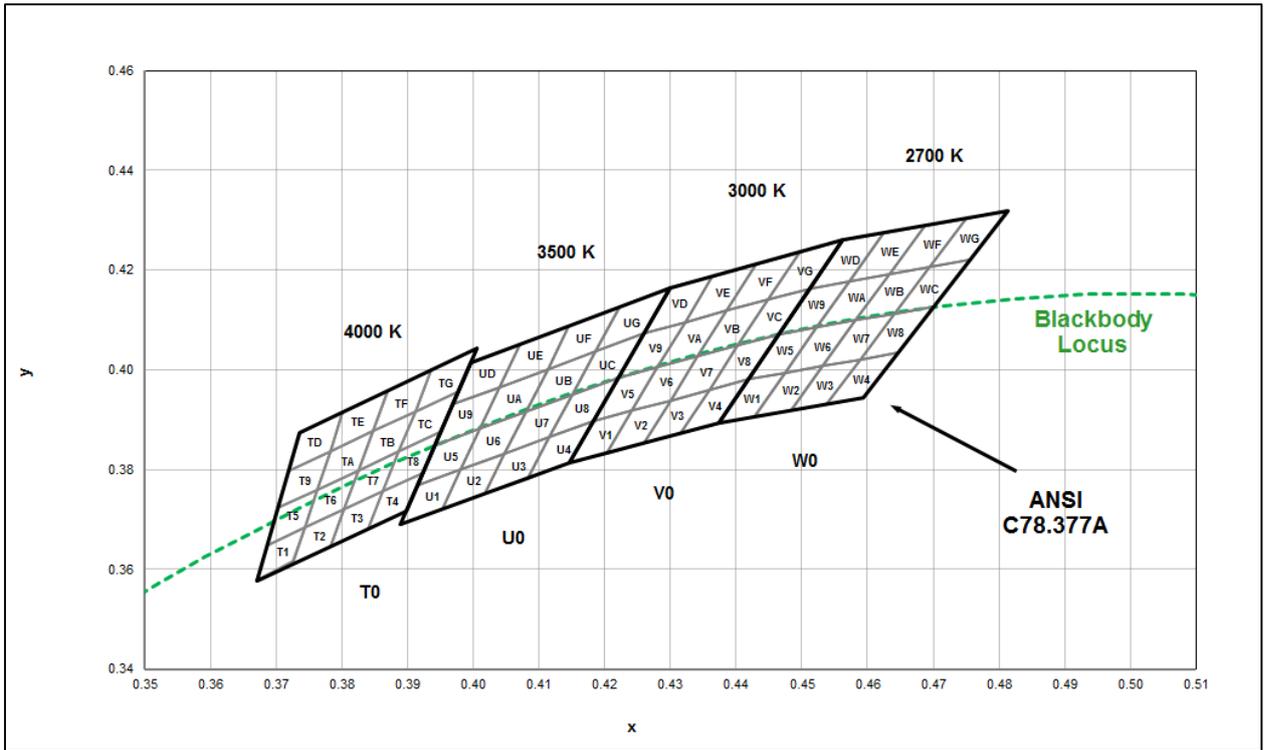
Nominal CCT	Product Code	Color Rank	Chromaticity Bins
2700K	SPMWHT223MD5WAW0S0	W0(Whole bin)	W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG
	SPMWHT223MD5WAWKS0	WK(Kitting bin)	-
	SPMWHT223MD5WAWMS0	WM(Quarter bin)	W6, W7, WA, WB
	SPMWHT223MD5WAWHS0	WH(Half bin)	W5, W6, W7, W8, W9, WA, WB, WC
3000K	SPMWHT223MD5WAV0S0	V0(Whole bin)	V1, V2, V3, V4, V5, V6, V7, V8, V9, VA, VB, VC, VD, VE, VF, VG
	SPMWHT223MD5WAVKS0	VK(Kitting bin)	-
	SPMWHT223MD5WAVMS0	VM(Quarter bin)	V6, V7, VA, VB
	SPMWHT223MD5WAVHS0	VH(Half bin)	V5, V6, V7, V8, V9, VA, VB, VC
3500K	SPMWHT223MD5WAU0S0	U0(Whole bin)	U1, U2, U3, U4, U5, U6, U7, U8, U9, UA, UB, UC, UD, UE, UF, UG
	SPMWHT223MD5WAUKS0	UK(Kitting bin)	-
	SPMWHT223MD5WAUMS0	UM(Quarter bin)	U6, U7, UA, UB
	SPMWHT223MD5WAUHS0	UH(Half bin)	U5, U6, U7, U8, U9, UA, UB, UC
4000K	SPMWHT223MD5WAT0S0	T0(Whole bin)	T1, T2, T3, T4, T5, T6, T7, T8, T9, TA, TB, TC, TD, TE, TF, TG
	SPMWHT223MD5WATKS0	TK(Kitting bin)	-
	SPMWHT223MD5WATMS0	TM(Quarter bin)	T6, T7, TA, TB
	SPMWHT223MD5WATHS0	TH(Half bin)	T5, T6, T7, T8, T9, TA, TB, TC



1) Color Binning(Continued)

Nominal CCT	Product Code	Color Rank	Chromaticity Bins
5000K	SPMWHT223MD5WAR0S0	R0(Whole bin)	R1, R2, R3, R4, R5, R6, R7, R8
	SPMWHT223MD5WARKS0	RK(Kitting bin)	-
	SPMWHT223MD5WARMS0	RM(Quarter bin)	R1, R2, R3, R4
5700K	SPMWHT223MD5WAQ0S0	Q0(Whole bin)	Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8
	SPMWHT223MD5WAQKS0	QK(Kitting bin)	-
	SPMWHT223MD5WAQMS0	QM(Quarter bin)	Q1, Q2, Q3, Q4
6500K	SPMWHT223MD5WAP0S0	P0(Whole bin)	P1, P2, P3, P4, P5, P6, P7, P8
	SPMWHT223MD5WAPKS0	PK(Kitting bin)	-
	SPMWHT223MD5WAPMS0	PM(Quarter bin)	P1, P2, P3, P4

## 2) Chromaticity Region & Coordinates





## 2) Chromaticity Region & Coordinates (Continued)

Region	CIE X	CIE Y	Region	CIE X	CIE Y
<b>W rank (2700K)</b>					
W1	0.4373	0.3893	W9	0.4465	0.4071
	0.4418	0.3981		0.4513	0.4164
	0.4475	0.3994		0.4573	0.4178
	0.4428	0.3906		0.4523	0.4085
W2	0.4428	0.3906	WA	0.4523	0.4085
	0.4475	0.3994		0.4573	0.4178
	0.4532	0.4008		0.4634	0.4193
	0.4483	0.3919		0.4582	0.4099
W3	0.4483	0.3919	WB	0.4582	0.4099
	0.4532	0.4008		0.4634	0.4193
	0.4589	0.4021		0.4695	0.4207
	0.4538	0.3931		0.4641	0.4112
W4	0.4538	0.3931	WC	0.4641	0.4112
	0.4589	0.4021		0.4695	0.4207
	0.4646	0.4034		0.4756	0.4221
	0.4593	0.3944		0.4700	0.4126
W5	0.4418	0.3981	WD	0.4513	0.4164
	0.4465	0.4071		0.4562	0.4260
	0.4523	0.4085		0.4624	0.4274
	0.4475	0.3994		0.4573	0.4178
W6	0.4475	0.3994	WE	0.4573	0.4178
	0.4523	0.4085		0.4624	0.4274
	0.4582	0.4099		0.4687	0.4289
	0.4532	0.4008		0.4634	0.4193
W7	0.4532	0.4008	WF	0.4634	0.4193
	0.4582	0.4099		0.4687	0.4289
	0.4641	0.4112		0.4750	0.4304
	0.4589	0.4021		0.4695	0.4207
W8	0.4589	0.4021	WG	0.4695	0.4207
	0.4641	0.4112		0.4750	0.4304
	0.4700	0.4126		0.4813	0.4319
	0.4646	0.4034		0.4756	0.4221

Region	CIE X	CIE Y	Region	CIE X	CIE Y
<b>V rank (3000K)</b>					
V1	0.4147	0.3814	V9	0.4221	0.3984
	0.4183	0.3898		0.4259	0.4073
	0.4242	0.3919		0.4322	0.4096
	0.4203	0.3833		0.4281	0.4006
V2	0.4203	0.3833	VA	0.4281	0.4006
	0.4242	0.3919		0.4322	0.4096
	0.4300	0.3939		0.4385	0.4119
	0.4259	0.3853		0.4342	0.4028
V3	0.4259	0.3853	VB	0.4342	0.4028
	0.4300	0.3939		0.4385	0.4119
	0.4359	0.3960		0.4449	0.4141
	0.4316	0.3873		0.4403	0.4049
V4	0.4316	0.3873	VC	0.4403	0.4049
	0.4359	0.3960		0.4449	0.4141
	0.4418	0.3981		0.4513	0.4164
	0.4373	0.3893		0.4465	0.4071
V5	0.4183	0.3898	VD	0.4259	0.4073
	0.4221	0.3984		0.4299	0.4165
	0.4281	0.4006		0.4364	0.4188
	0.4242	0.3919		0.4322	0.4096
V6	0.4242	0.3919	VE	0.4322	0.4096
	0.4281	0.4006		0.4364	0.4188
	0.4342	0.4028		0.4430	0.4212
	0.4300	0.3939		0.4385	0.4119
V7	0.4300	0.3939	VF	0.4385	0.4119
	0.4342	0.4028		0.4430	0.4212
	0.4403	0.4049		0.4496	0.4236
	0.4359	0.3960		0.4449	0.4141
V8	0.4359	0.3960	VG	0.4449	0.4141
	0.4403	0.4049		0.4496	0.4236
	0.4465	0.4071		0.4562	0.4260
	0.4418	0.3981		0.4513	0.4164



## 2) Chromaticity Region & Coordinates (Continued)

Region	CIE X	CIE Y	Region	CIE X	CIE Y
<b>U rank (3500K)</b>					
U1	0.3889	0.3690	U9	0.3941	0.3848
	0.3915	0.3768		0.3968	0.3930
	0.3981	0.3800		0.4040	0.3966
	0.3953	0.3720		0.4010	0.3882
U2	0.3953	0.3720	UA	0.4010	0.3882
	0.3981	0.3800		0.4040	0.3966
	0.4048	0.3832		0.4113	0.4001
	0.4017	0.3751		0.4080	0.3916
U3	0.4017	0.3751	UB	0.4080	0.3916
	0.4048	0.3832		0.4113	0.4001
	0.4116	0.3865		0.4186	0.4037
	0.4082	0.3782		0.4150	0.3950
U4	0.4082	0.3782	UC	0.4150	0.3950
	0.4116	0.3865		0.4186	0.4037
	0.4183	0.3898		0.4259	0.4073
	0.4147	0.3814		0.4221	0.3984
U5	0.3915	0.3768	UD	0.3968	0.3930
	0.3941	0.3848		0.3996	0.4015
	0.4010	0.3882		0.4071	0.4052
	0.3981	0.3800		0.4040	0.3966
U6	0.3981	0.3800	UE	0.4040	0.3966
	0.4010	0.3882		0.4071	0.4052
	0.4080	0.3916		0.4146	0.4089
	0.4048	0.3832		0.4113	0.4001
U7	0.4048	0.3832	UF	0.4113	0.4001
	0.4080	0.3916		0.4146	0.4089
	0.4150	0.3950		0.4222	0.4127
	0.4116	0.3865		0.4186	0.4037
U8	0.4116	0.3865	UG	0.4186	0.4037
	0.4150	0.3950		0.4222	0.4127
	0.4221	0.3984		0.4299	0.4165
	0.4183	0.3898		0.4259	0.4073

Region	CIE X	CIE Y	Region	CIE X	CIE Y
<b>T rank (4000K)</b>					
T1	0.367	0.3578	T9	0.3702	0.3722
	0.3726	0.3612		0.3763	0.376
	0.3744	0.3685		0.3782	0.3837
	0.3686	0.3649		0.3719	0.3797
T2	0.3726	0.3612	TA	0.3763	0.3760
	0.3783	0.3646		0.3825	0.3798
	0.3804	0.3721		0.3847	0.3877
	0.3744	0.3685		0.3782	0.3837
T3	0.3783	0.3646	TB	0.3825	0.3798
	0.3840	0.3681		0.3887	0.3836
	0.3863	0.3758		0.3912	0.3917
	0.3804	0.3721		0.3847	0.3877
T4	0.384	0.3681	TC	0.3887	0.3837
	0.3898	0.3716		0.395	0.3875
	0.3924	0.3794		0.3978	0.3958
	0.3863	0.3758		0.3912	0.3917
T5	0.3686	0.3649	TD	0.3719	0.3797
	0.3744	0.3685		0.3782	0.3837
	0.3763	0.376		0.3802	0.3916
	0.3702	0.3722		0.3736	0.3874
T6	0.3744	0.3685	TE	0.3782	0.3837
	0.3804	0.3721		0.3847	0.3877
	0.3825	0.3798		0.3869	0.3958
	0.3763	0.376		0.3802	0.3916
T7	0.3804	0.3721	TF	0.3847	0.3877
	0.3863	0.3758		0.3912	0.3917
	0.3887	0.3836		0.3937	0.4001
	0.3825	0.3798		0.3869	0.3958
T8	0.3863	0.3758	TG	0.3912	0.3917
	0.3924	0.3794		0.3978	0.3958
	0.395	0.3875		0.4006	0.4044
	0.3887	0.3836		0.3937	0.4001



## 2) Chromaticity Region & Coordinates (Continued)

Region	CIE X	CIE Y	Region	CIE X	CIE Y
<b>R rank (5000K)</b>					
R1	0.3371	0.3490	R5	0.3366	0.3369
	0.3451	0.3554		0.3440	0.3428
	0.3440	0.3427		0.3429	0.3307
	0.3366	0.3369		0.3361	0.3245
R2	0.3451	0.3554	R6	0.3440	0.3428
	0.3533	0.3620		0.3515	0.3487
	0.3515	0.3487		0.3495	0.3339
	0.3440	0.3427		0.3429	0.3307
R3	0.3376	0.3616	R7	0.3381	0.3762
	0.3463	0.3687		0.3480	0.3840
	0.3451	0.3554		0.3463	0.3687
	0.3371	0.3490		0.3376	0.3616
R4	0.3463	0.3687	R8	0.3480	0.3840
	0.3551	0.3760		0.3571	0.3907
	0.3533	0.3620		0.3551	0.3760
	0.3451	0.3554		0.3463	0.3687
<b>Q rank (5700K)</b>					
Q1	0.3215	0.3350	Q5	0.3222	0.3243
	0.3290	0.3417		0.3290	0.3300
	0.3290	0.3300		0.3290	0.3180
	0.3222	0.3243		0.3231	0.3120
Q2	0.3290	0.3417	Q6	0.3290	0.3300
	0.3371	0.3490		0.3366	0.3369
	0.3366	0.3369		0.3361	0.3245
	0.3290	0.3300		0.3290	0.3180
Q3	0.3207	0.3462	Q7	0.3196	0.3602
	0.3290	0.3538		0.3290	0.3690
	0.3290	0.3417		0.3290	0.3538
	0.3215	0.3350		0.3207	0.3462
Q4	0.3290	0.3538	Q8	0.3290	0.3690
	0.3376	0.3616		0.3381	0.3762
	0.3371	0.3490		0.3376	0.3616
	0.3290	0.3417		0.3290	0.3538

Region	CIE X	CIE Y	Region	CIE X	CIE Y
<b>P rank (6500K)</b>					
P1	0.3068	0.3113	P5	0.3093	0.2993
	0.3144	0.3186		0.3161	0.3059
	0.3130	0.3290		0.3144	0.3186
	0.3048	0.3207		0.3068	0.3113
P2	0.3144	0.3186	P6	0.3161	0.3059
	0.3221	0.3261		0.3231	0.3120
	0.3213	0.3373		0.3221	0.3261
	0.3130	0.3290		0.3144	0.3186
P3	0.3048	0.3207	P7	0.3028	0.3304
	0.3130	0.3290		0.3115	0.3391
	0.3115	0.3391		0.3099	0.3509
	0.3028	0.3304		0.3005	0.3415
P4	0.3130	0.3290	P8	0.3115	0.3391
	0.3213	0.3373		0.3205	0.3481
	0.3205	0.3481		0.3196	0.3602
	0.3115	0.3391		0.3099	0.3509

### Notes:

SAMSUNG ELECTRONICS maintains  $\pm 0.005$  tolerance of CCx, CCy



## 2. Luminous Flux Characteristics ( $T_s = 25^\circ\text{C}$ )

Nominal CCT	Minimum CRI	If(mA)	Vf(V)	Power(W)	Flux(lm)	lm/W
2700K	80	50	2.80	0.14	18.1	129
		60	2.83	0.17	21.6	127
		65	2.86	0.19	23.2	125
		70	2.89	0.20	24.8	123
		80	2.92	0.23	28.3	121
		90	2.95	0.27	31.6	119
		100	2.97	0.30	34.6	116
		150	3.09	0.46	50.1	108
3000K	80	50	2.80	0.14	18.4	131
		60	2.83	0.17	21.9	129
		65	2.86	0.19	23.6	127
		70	2.89	0.20	25.3	125
		80	2.92	0.23	28.8	123
		90	2.95	0.27	32.1	121
		100	2.97	0.30	35.2	118
		150	3.09	0.46	51.0	110
3500K	80	50	2.80	0.14	18.6	133
		60	2.83	0.17	22.2	131
		65	2.86	0.19	23.9	129
		70	2.89	0.20	25.6	126
		80	2.92	0.23	29.2	125
		90	2.95	0.27	32.5	123
		100	2.97	0.30	35.6	120
		150	3.09	0.46	51.6	111
4000K	80	50	2.80	0.14	19.1	136
		60	2.83	0.17	22.8	134
		65	2.86	0.19	24.5	132
		70	2.89	0.20	26.2	130
		80	2.92	0.23	29.9	128
		90	2.95	0.27	33.3	126
		100	2.97	0.30	36.5	123
		150	3.09	0.46	52.9	114

**Notes:**

Luminous Flux( $\Phi_v$ , lm) values are for representative reference only



## 2. Luminous Flux Characteristics (Continued)

Nominal CCT	Minimum CRI	If(mA)	Vf(V)	Power(W)	Flux(lm)	lm/W
5000K	80	50	2.80	0.14	19.3	138
		60	2.83	0.17	23.1	136
		65	2.86	0.19	24.8	133
		70	2.89	0.20	26.5	131
		80	2.92	0.23	30.3	130
		90	2.95	0.27	33.7	127
		100	2.97	0.30	37.0	124
		150	3.09	0.46	53.6	116
5700K	80	50	2.80	0.14	19.3	137
		60	2.83	0.17	23.0	135
		65	2.86	0.19	24.7	133
		70	2.89	0.20	26.4	131
		80	2.92	0.23	30.1	129
		90	2.95	0.27	33.6	127
		100	2.97	0.30	36.8	124
		150	3.09	0.46	53.4	115
6500K	80	50	2.80	0.14	19.3	137
		60	2.83	0.17	23.0	135
		65	2.86	0.19	24.7	133
		70	2.89	0.20	26.4	131
		80	2.92	0.23	30.1	129
		90	2.95	0.27	33.6	127
		100	2.97	0.30	36.8	124
		150	3.09	0.46	53.4	115

**Notes:**

Luminous Flux( $\Phi_v$ , lm) values are for representative reference only

### 3. Characteristics

#### 1) Absolute Maximum Rating

Item	Symbol	Rating	Condition
Operating temperature range	$T_{op}$	-40°C ~ +85°C	-
Storage temperature range	$T_{stg}$	-40°C ~ +100°C	-
LED junction temperature	$T_J$	110°C	-
Forward Current	$I_F$	150 mA	-
Peak Pulsed Forward Current	$I_{FP}$	300 mA	Duty 1/10 pulse width 10ms
Thermal resistance	$R_{th, j-s}$	20°C/W	Junction to solder point
Assembly Process Temperature	-	260°C, < 10sec	-
ESD	-	5kV	HBM

#### 2) Electro-optical Characteristics

Item	Unit	Nominal CCT	Product Code	Rank	Min	Typ	Max		
Forward Voltage <sup>1)</sup> ( $V_F$ ) (@65 mA, $T_s = 25^\circ\text{C}$ )	V	-	-	WA	AZ	-	2.80		
					A1	-	2.90		
					A2	-	3.00		
					A3	-	3.10		
					A4	-	3.20		
Luminous Flux <sup>2)</sup> ( $\Phi_v$ ) (@65 mA, $T_s = 25^\circ\text{C}$ )	lm	2700K (W☆)	*WAW☆S0	S1	19.81	-	22.78		
				S2	22.78	-	26.20		
				S3	26.20	-	30.13		
		3000K (V☆)	*WAV☆S0	S1	19.81	-	22.78		
				S2	22.78	-	26.20		
				S3	26.20	-	30.13		
		3500K (U☆)	*WAU☆S0	S1	19.81	-	22.78		
				S2	22.78	-	26.20		
				S3	26.20	-	30.13		
		4000K (T☆)	*WAT☆S0	S1	20.55	-	23.56		
				S2	23.56	-	27.09		
				S3	27.09	-	31.16		
		5000K (R☆)	*WAR☆S0	S1	20.55	-	23.56		
				S2	23.56	-	27.09		
				S3	27.09	-	31.16		
		5700K (Q☆)	*WAQ☆S0	S1	20.55	-	23.56		
				S2	23.56	-	27.09		
				S3	27.09	-	31.16		
		6500K (P☆)	*WAP☆S0	S1	20.55	-	23.56		
				S2	23.56	-	27.09		
				S3	27.09	-	31.16		
		Reverse Voltage (@5 mA, $T_s = 25^\circ\text{C}$ )	V	-	-	-	0.7	-	1.2
		Color Rendering Index <sup>3)</sup> ( $R_a$ )	-	-	-	5	80	-	-
		Special CRI <sup>4)</sup> (R9)	-	-	-	-	0	-	-

**Notes:**

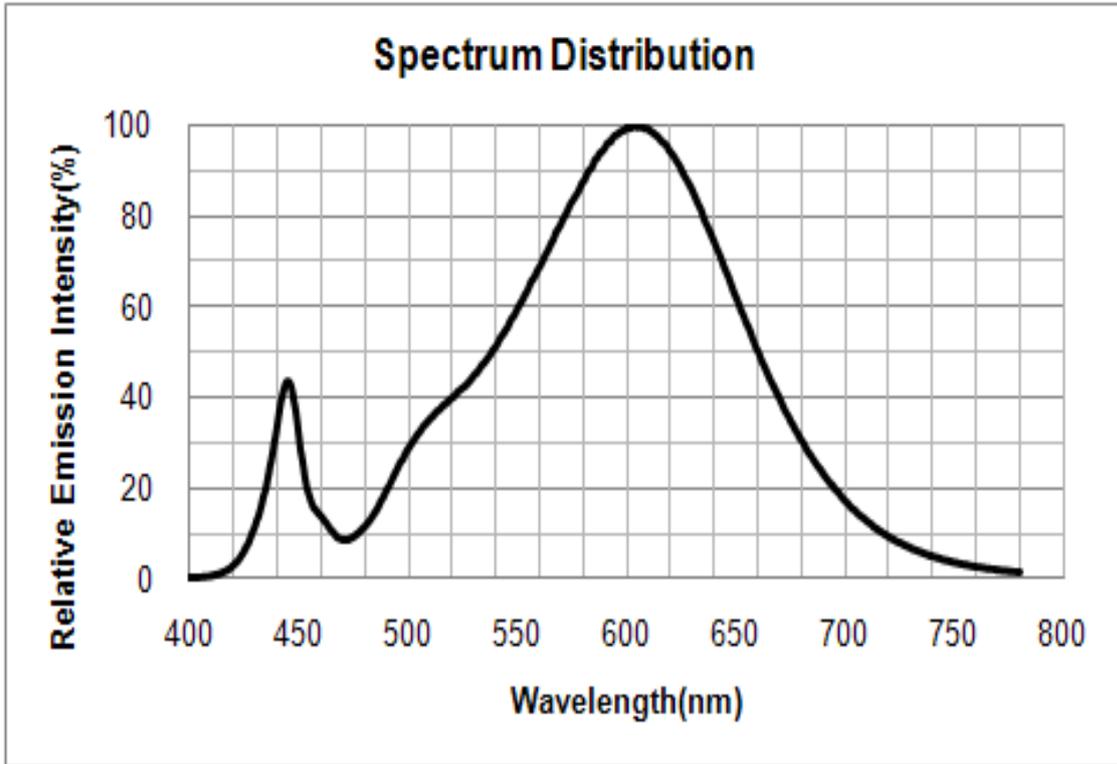
1)~4) SAMSUNG ELECTRONICS maintains a tolerance of  $V_F:\pm 0.1$  V,  $\Phi_v:\pm 5$  %,  $R_a:\pm 3.0$ ,  $R9:\pm 6.5$  on measurements

5) " \* " is Product Code of "SPMWHT223MD5"

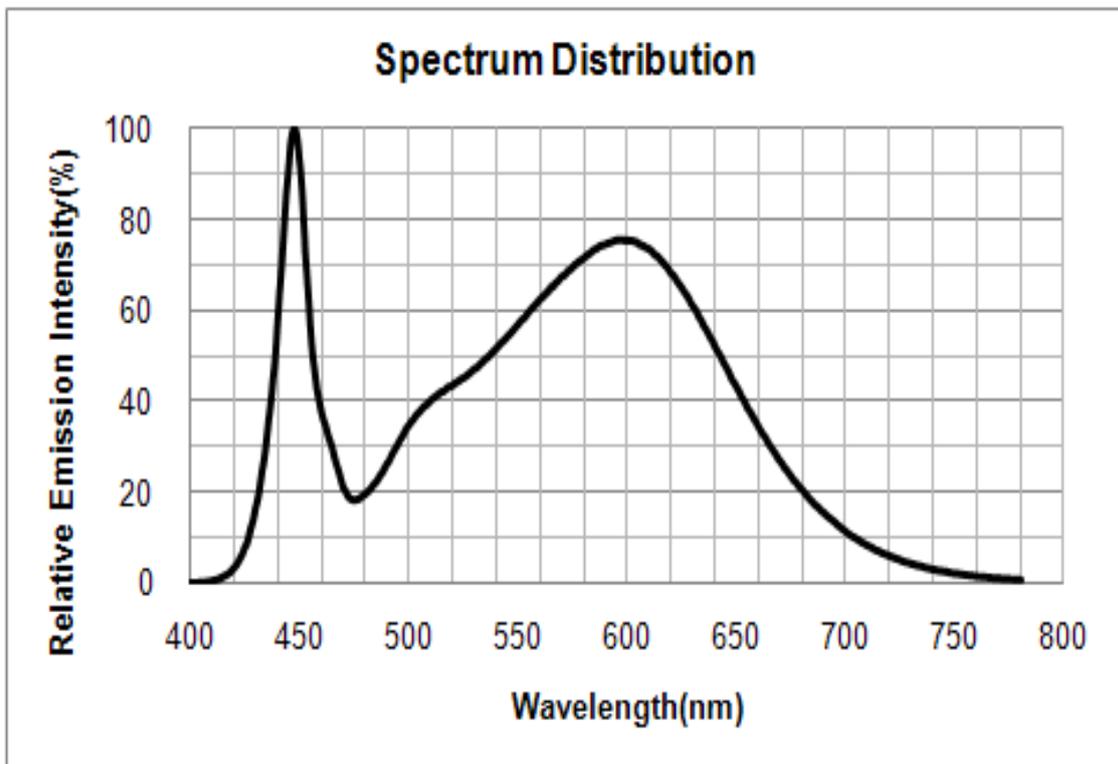
#### 4. Typical Characteristics Graph (@65mA, $T_s = 25^\circ\text{C}$ )

##### 1) Spectrum Distribution

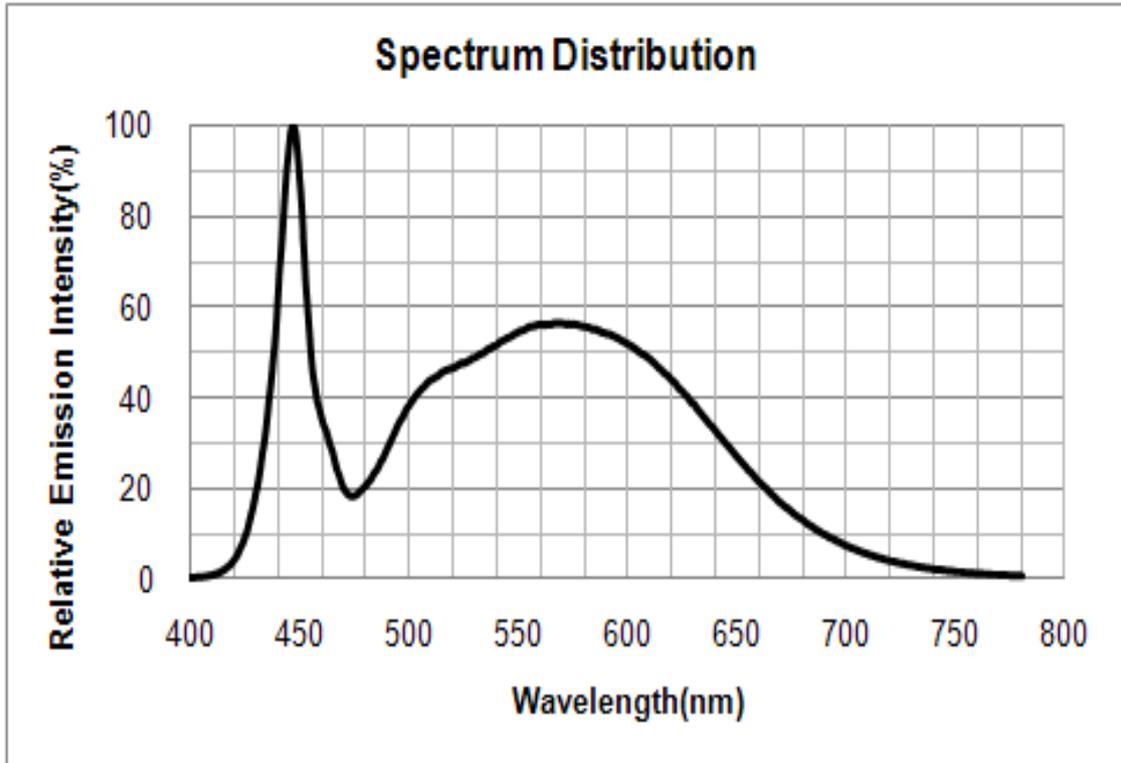
[CCT : 2700K & 3000K]



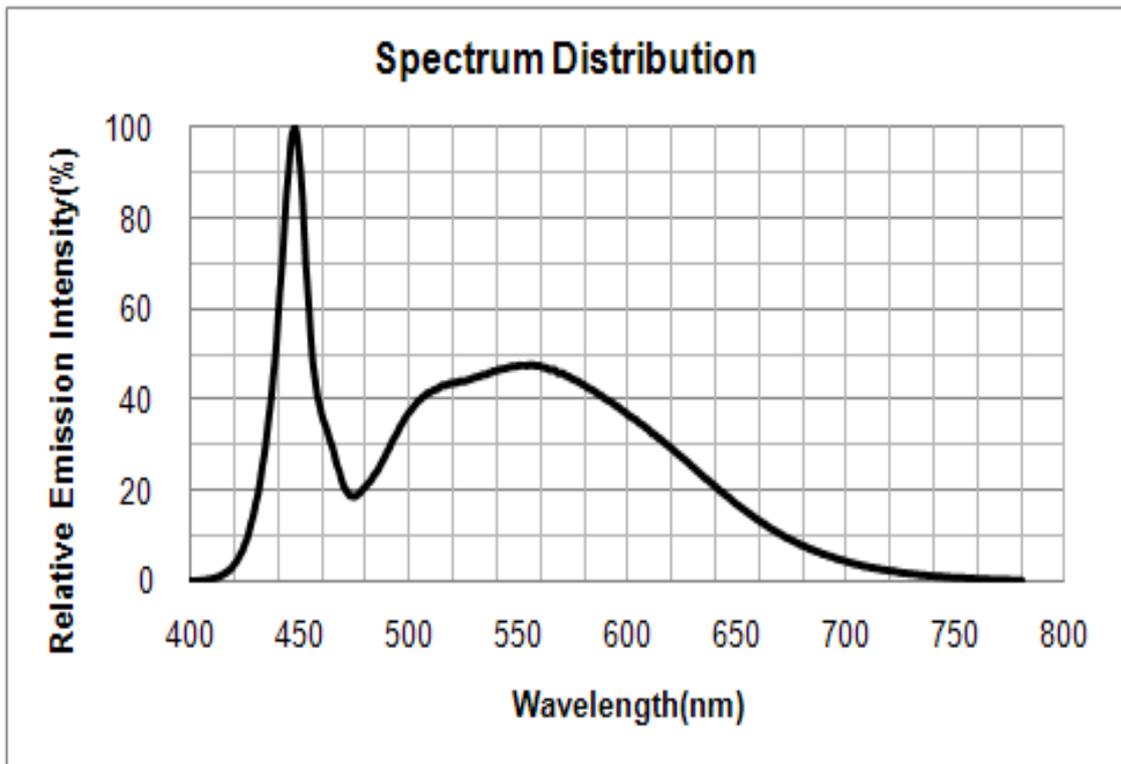
[CCT : 3500K & 4000K]



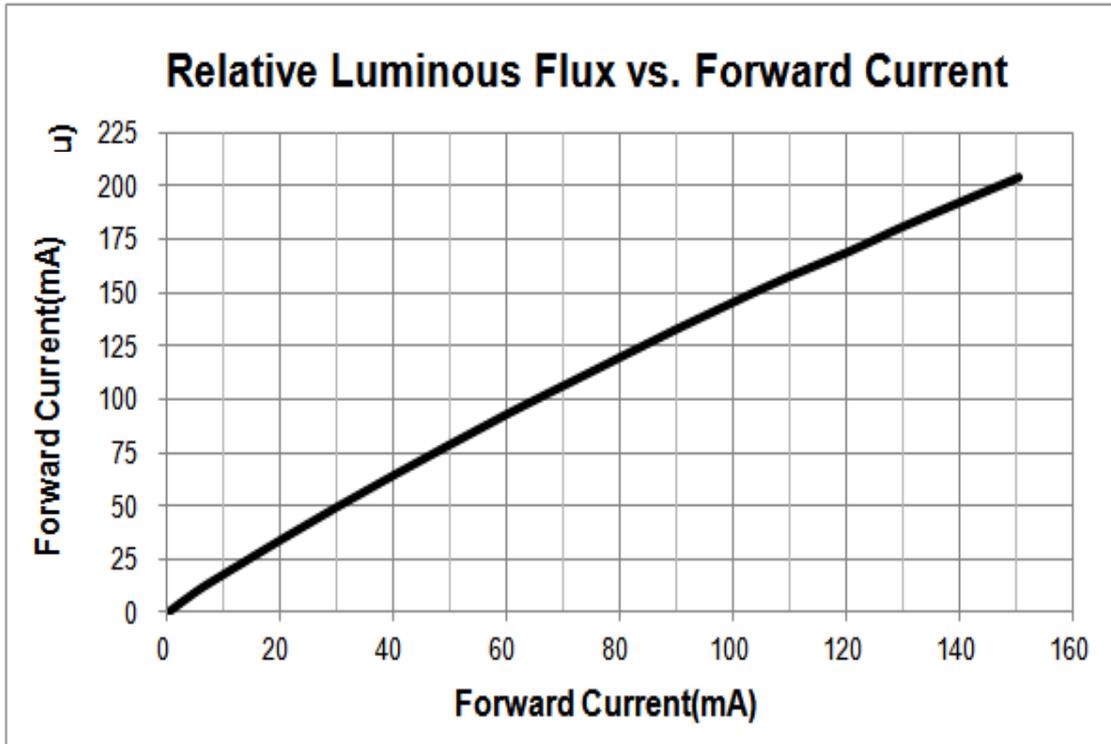
[CCT : 5000K]



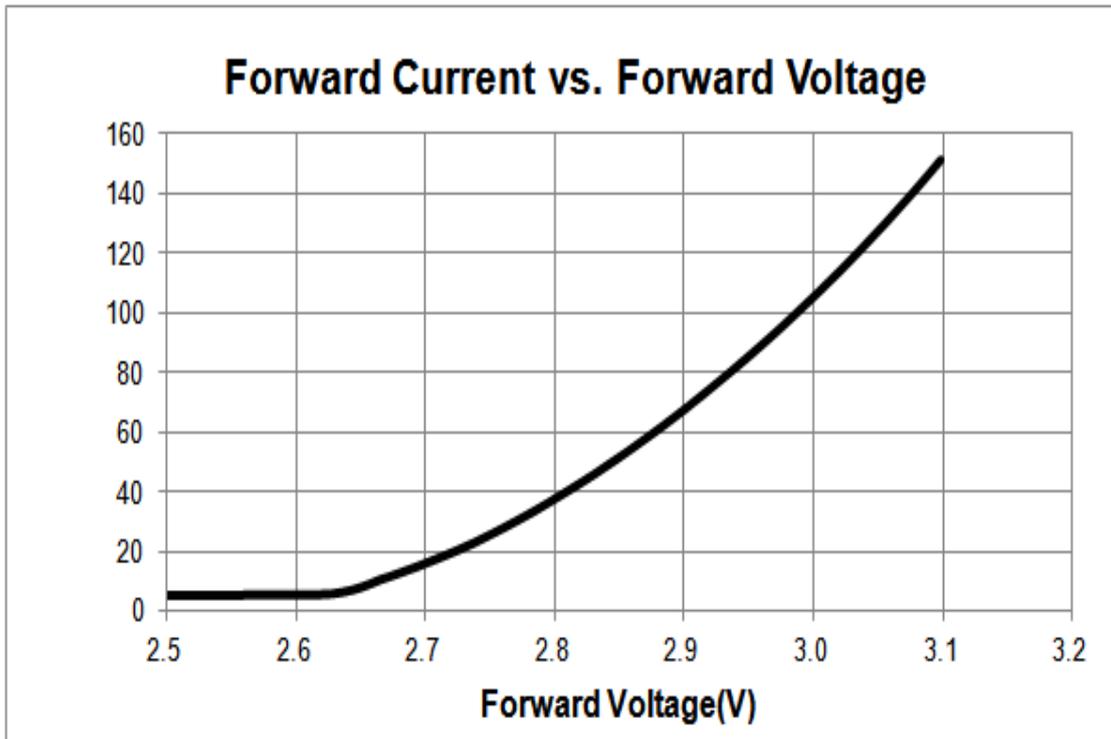
[CCT : 5700K~6500K]



2) Forward Current Characteristics ( $T_s = 25^\circ\text{C}$ )  
[Relative Luminous Flux vs. Forward Current]

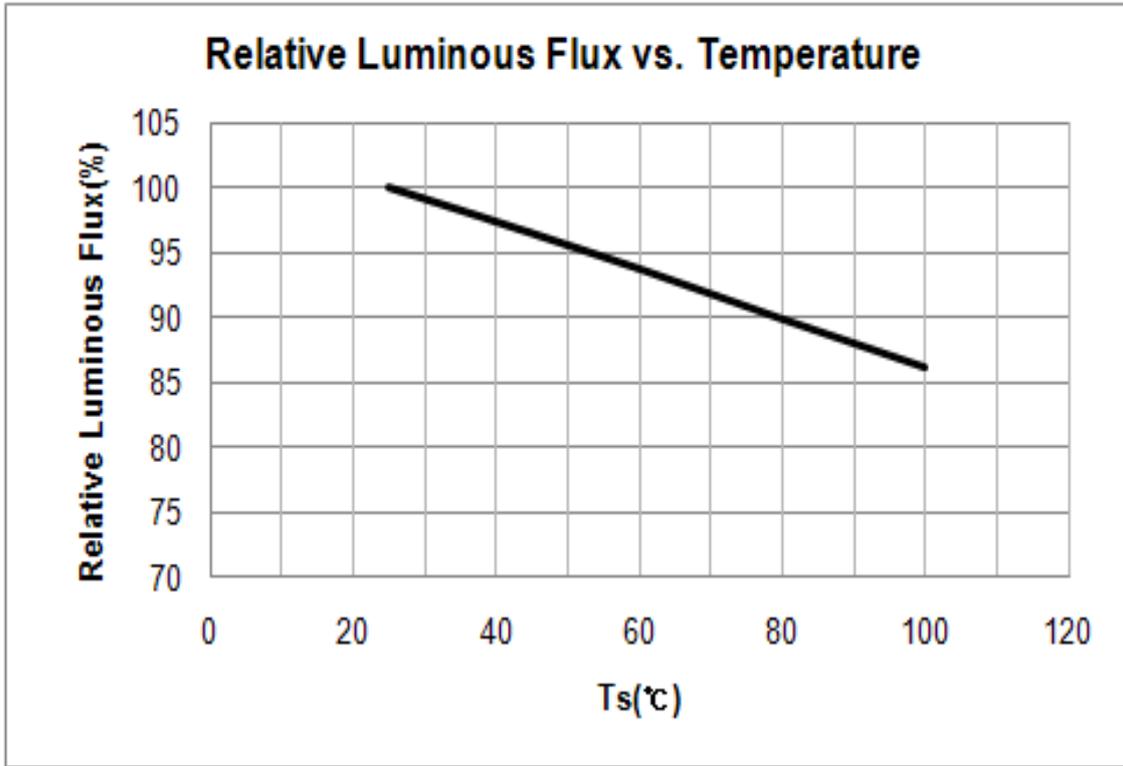


[Forward Current vs. Forward Voltage]

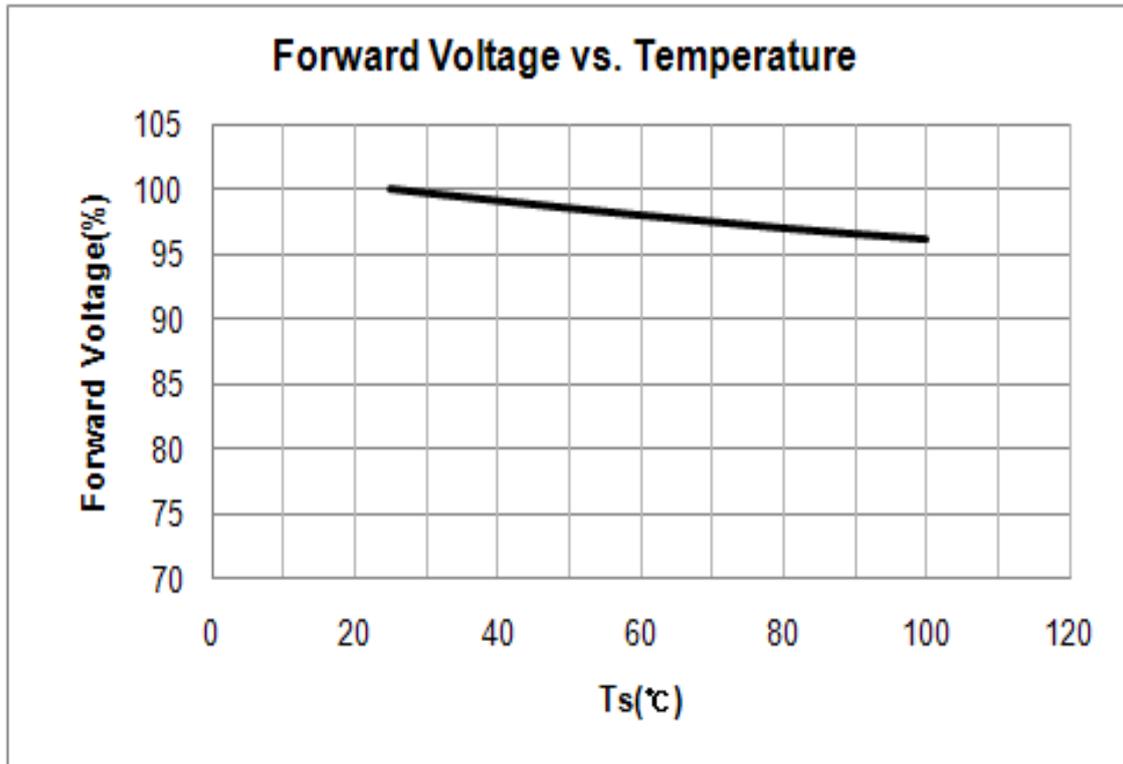


### 3) Temperature Characteristics (@65mA)

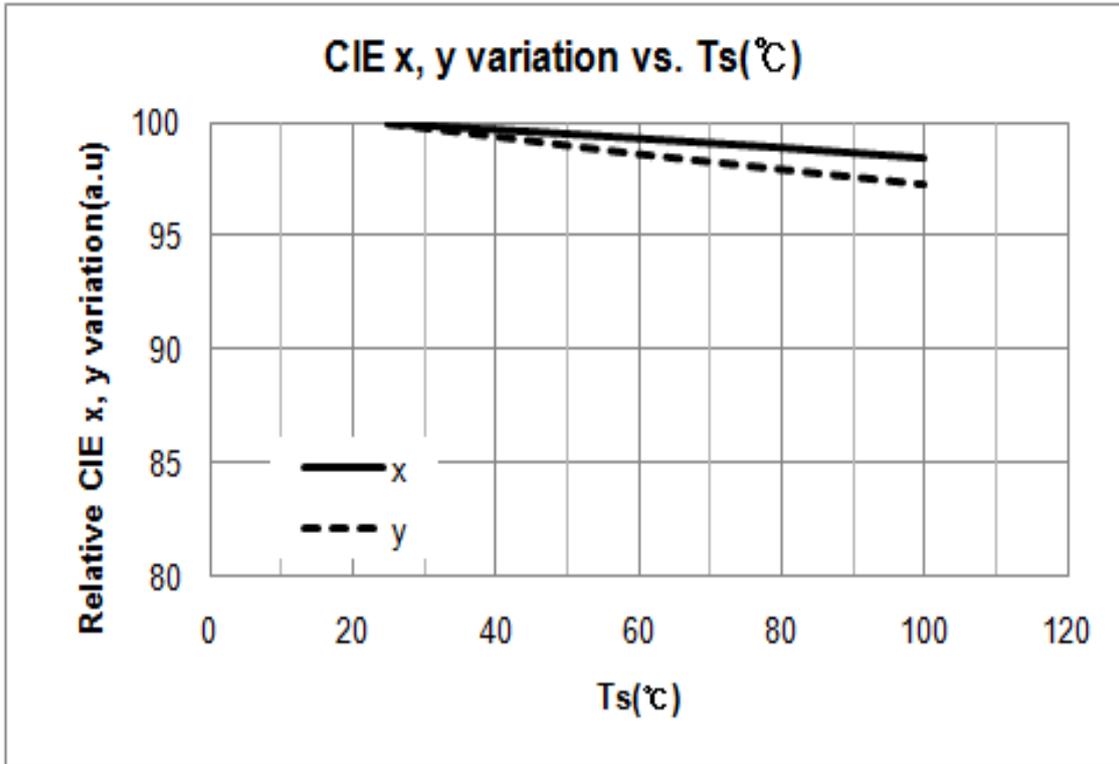
[Relative Luminous Flux vs. Ts]



[Forward Voltage vs. Ts]

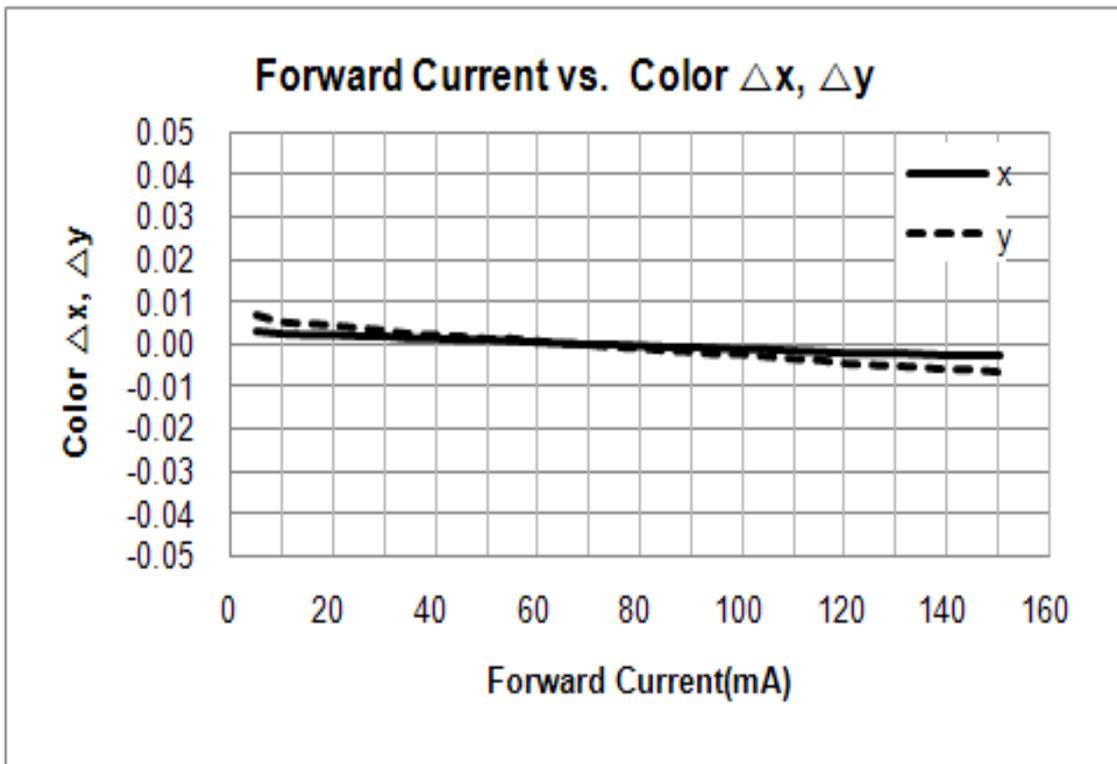


[Color  $\Delta x$ ,  $\Delta y$  vs.  $T_s$ ]

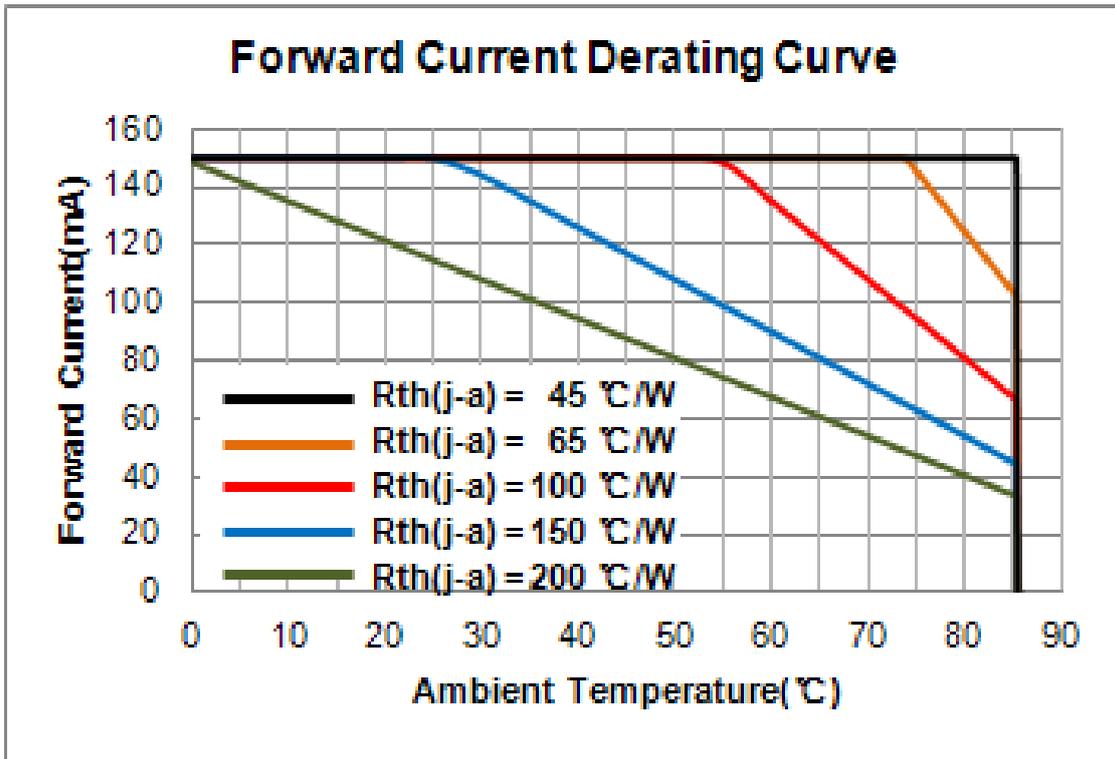


#### 4) Color shift Characteristics ( $T_s = 25^\circ\text{C}$ )

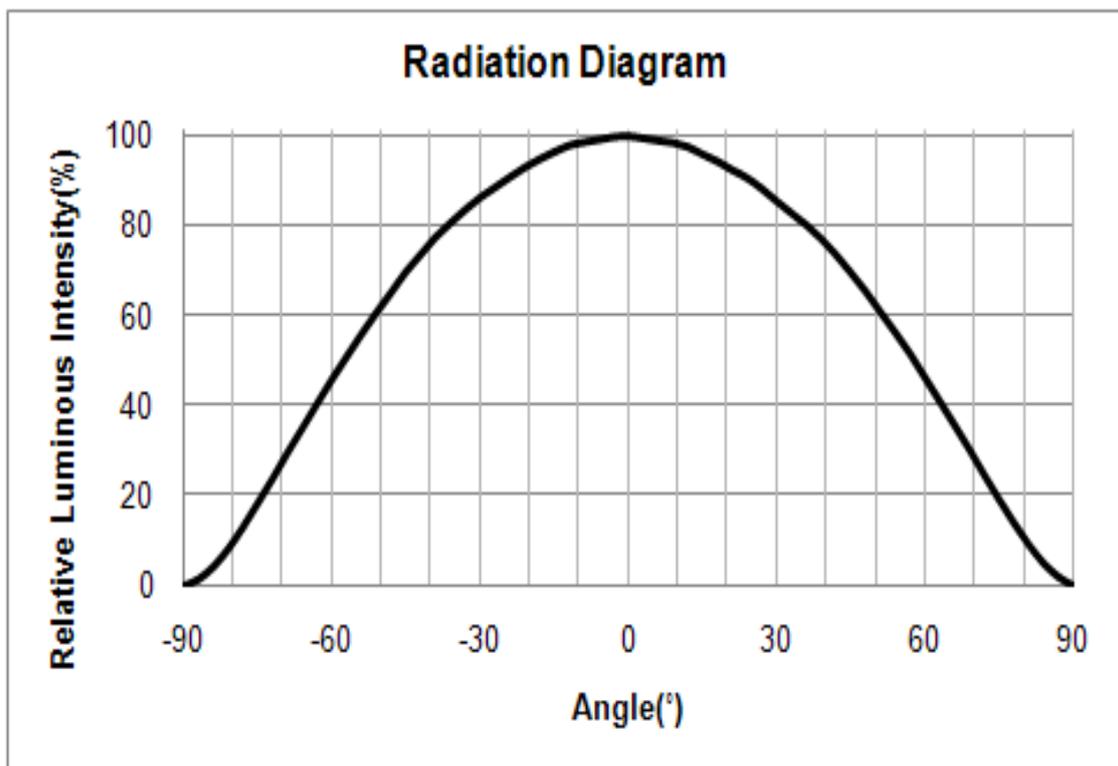
[Forward Current vs. Color  $\Delta x$ ,  $\Delta y$ ]



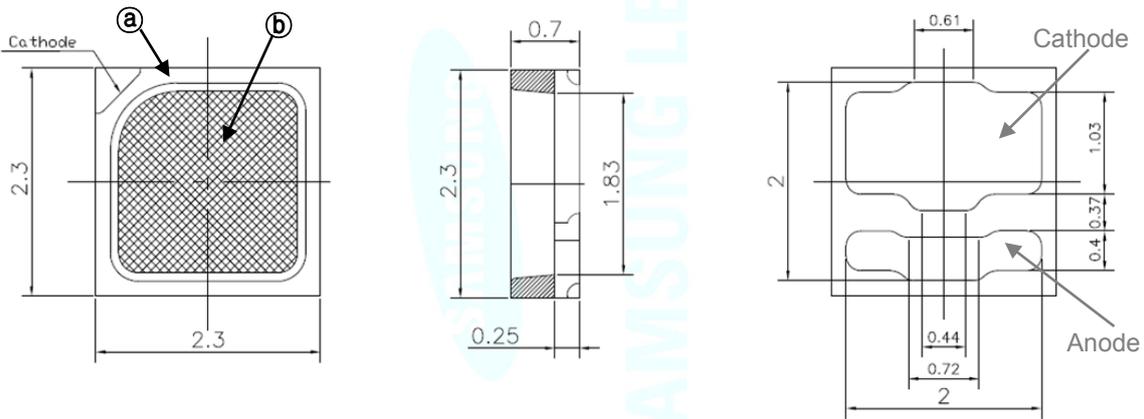
### 5) Derating Curve



### 6) Beam Angle Characteristics (@65mA, $T_s = 25^\circ\text{C}$ )

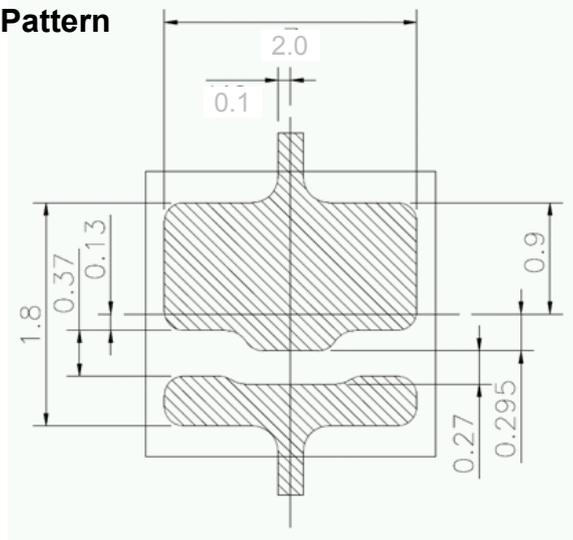


## 5. Outline Drawing & Dimension



1. Tolerance is  $\pm 0.1$  mm
2. The maximum compressing force is 15N on the silicone ①
3. Do not place pressure on the encapsulation resin ②

### Recommended Land Pattern



### Notes:

- 1) This LED has built-in ESD protection device(s) connected in parallel to LED Chip(s).
- 2) Ts point & measurement method
  - ① Measure the nearest point to the thermal pad. If necessary, remove PSR of PCB to reach Ts point.
  - ② Thermal pad must be soldered to the PCB to dissipate heat properly. Otherwise, LED can be damaged.
- 3) Precautions
  - ① The pressure on the LEDs will influence to the reliability of the LEDs. Precautions should be taken to avoid the strong pressure on the LEDs. Do not put stress on the LEDs during heating.
  - ② Re-soldering should not be done after the LEDs have been soldered. If re-soldering is unavoidable, LED's characteristics should be carefully checked before and after such repair.
  - ③ Do not stack assembled PCBs together. Since materials of LEDs is soft, abrasion between two PCB assembled with LED might cause catastrophic failure of the LEDs.

## 6. Reliability Test Items & Conditions

### 1) Test Items and Results

Test Item	Test Conditions	Test Hours/Cycles	Sample No	
MSL Test	125°C, 24hrs → 60°C, 60%RH, 120hrs → Peak 260±5°C, 220°C over time 60sec, 3 cycles	1 cycle	11	
Room Temperature Life Test	25°C±3°C, DC 150mA	1,000 hrs	22	
High Temperature Life Test	85°C±3°C, DC 150mA	1,000 hrs	22	
High Temperature Humidity Life Test	85°C±3°C, 85%±2%RH, DC 150mA	1,000 hrs	22	
Low Temperature Life Test	-40°C±3°C, DC 150mA	1,000 hrs	22	
Powered Temperature Cycle Test	-45°C/20min ↔ 85°C/20min, Sweep 100min cycle on/off: each 5min, DC 150mA	100 cycles	22	
Thermal Cycle	-45°C/15min ↔ 125°C/15min, → Hot plate 180°C	500 cycles	100	
High Temperature Storage	Ta=120°C±3°C	1,000 hrs	11	
Low Temperature Storage	Ta=-40°C±3°C	1,000 hrs	11	
ESD(HBM)		R1 : 10MΩ, R2 : 1.5KΩ, C : 100pF, V = ±5kV	5 times	5
ESD(MM)		R1 : 10MΩ, R2 : 0, C : 200pF, V = ±0.5kV	5 times	5
Vibration Test	20~2000~20 Hz 200 m/s <sup>2</sup> , Sweep 4 min X, Y, Z 3 direction, each 1 cycle	4 cycles	11	
Mechanical Shock Test	1500G, 0.5ms	5 cycles	11	

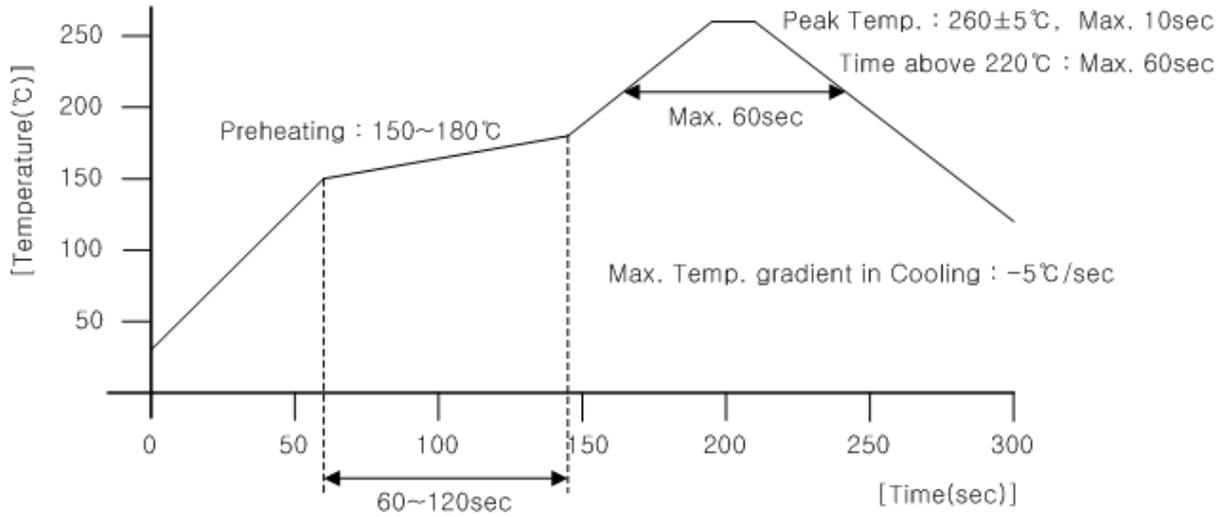
### 2) Criteria for Judging the Damage

Item	Symbol	Test Condition	Limit	
			Min	Max
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 65 mA	Init. Value*0.9	Init. Value*1.1
Luminous Flux	Φ <sub>v</sub>	I <sub>F</sub> = 65 mA	Init. Value*0.7	Init. Value*1.1

## 7. Solder Conditions

### 1) Reflow Conditions ( Pb Free )

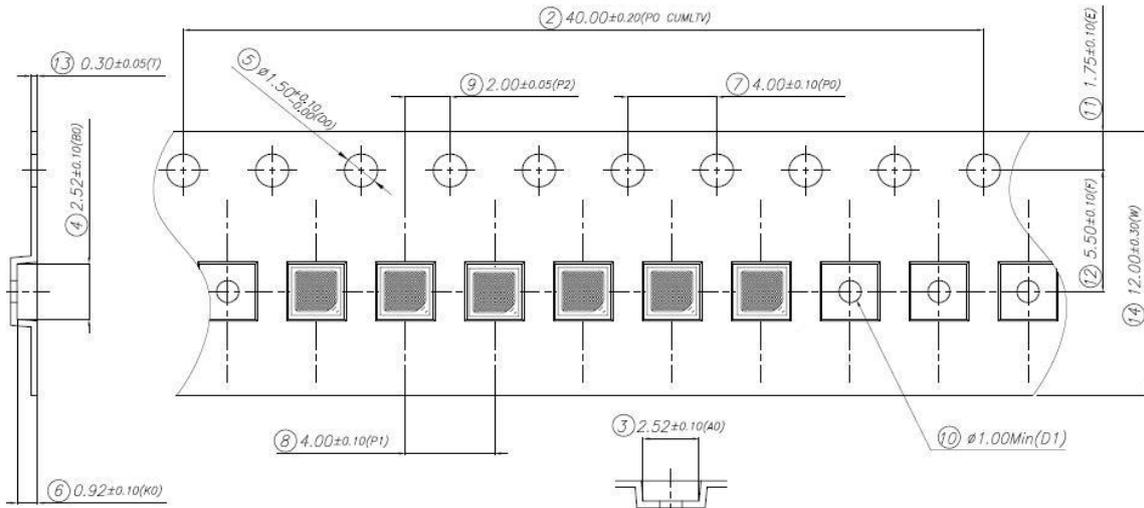
Reflow Frequency : 2 times max.



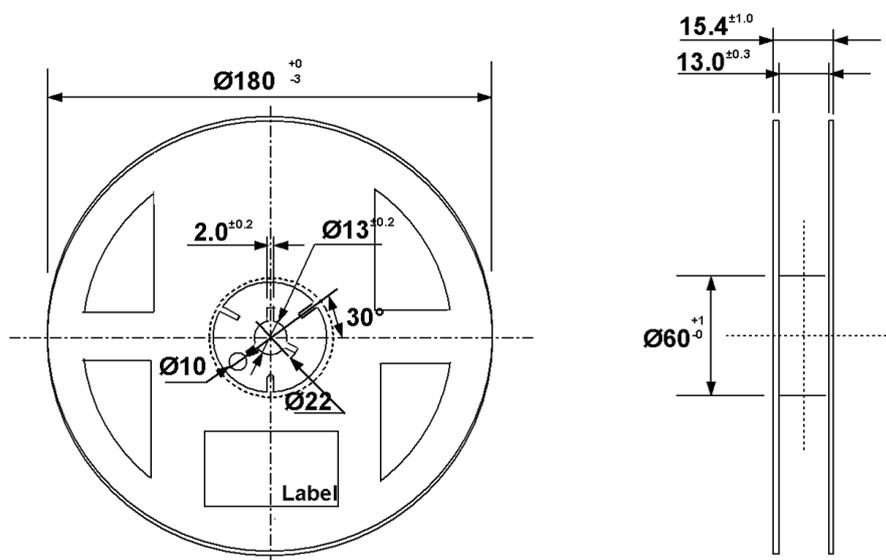
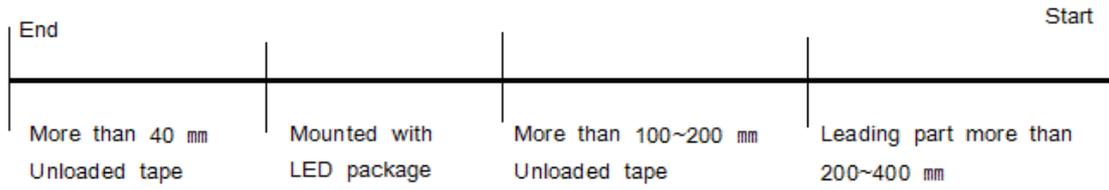
### 2) For Manual Soldering

Not more than 5 seconds @Max. 300°C, under soldering iron.

## 8. Tape & Reel



← Taping Direction

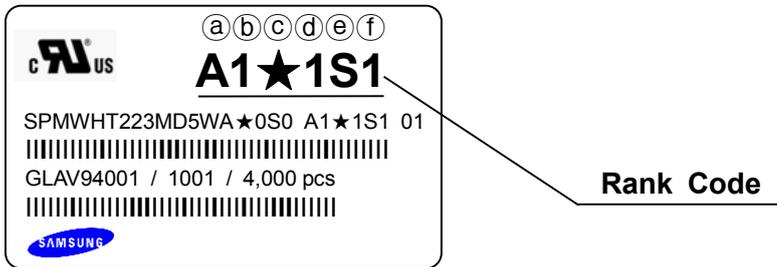


Tolerance  $\pm 0.2$  , Unit:mm

- (1) Quantity : The quantity/reel to be 4,000 pcs.
- (2) Cumulative Tolerance : Cumulative tolerance/10 pitches to be  $\pm 0.2$  mm
- (3) Adhesion Strength of Cover Tape : Adhesion strength to be 0.1-0.7 N when the cover tape is turned off from the carrier tape at 10 °C angle to be the carrier tape.
- (4) Packaging : P/N, Manufacturing data code no. and quantity to be indicated on a damp proof package.

## 9. Label Structure

### 1) Label Structure



N.B) Denoted rank is the only example.

'★' means All kind of Chromaticity Coordinate Rank.

### Rank Code

- ①② : Forward Voltage( $V_F$ ) Rank (refer to page. 12)
- ③④ : Chromaticity Coordinate Rank (refer to page. 7~9)
- ⑤⑥ : Luminous Intensity(cd) Rank (refer to page. 3)

### 2) LOT Number

The Lot number is composed of the following characters



①②③④⑤⑥⑦⑧⑨ / 1①②③ / 4,000 PCS

- ① : Production Site (S:Giheung Korea, G: Tianjin China)
- ② : L (LED)
- ③ : Product State (A:Normality, B:Bulk, C:First Production, R:Reproduction, S:Sample)
- ④ : Year ( Z:2015, A:2016, B:2017...)
- ⑤ : Month (1 ~ 9, A, B, C)
- ⑥ : Day (1 ~ 9, A, B ~ V)
- ⑦⑧⑨ : SAMSUNG ELECTRONICS LED Product number (1 ~ 999)
- ①②③ : Reel Number (1 ~ 999)

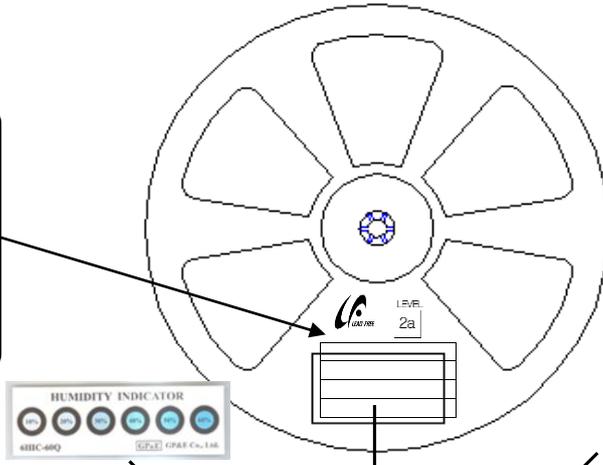


# 10. Packing Structure

## 1) Packing Process

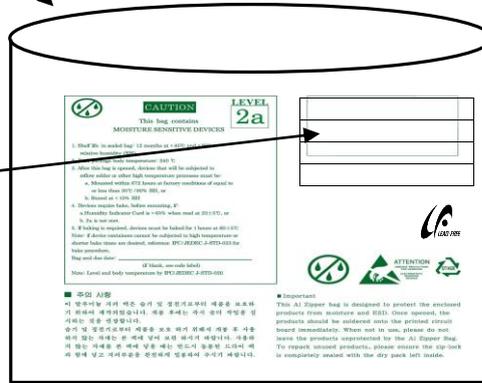
### Reel

**A1★1S1**  
 SPMWHT223MD5WA★0S0 A1★1S1 01  
 GLAV94001 / 1001 / 4,000 pcs  
 SAMSUNG



### Aluminum Vinyl Bag

**A1★1S1**  
 SPMWHT223MD5WA★0S0 A1★1S1 01  
 GLAV94001 / 1001 / 4,000 pcs  
 SAMSUNG

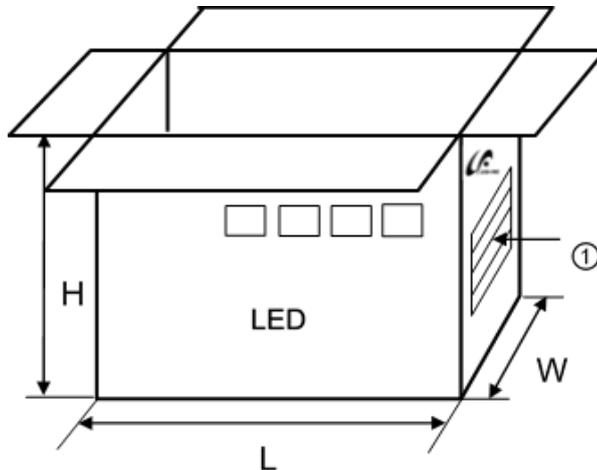


Material : Paper(SW3B(B))

TYPE	SIZE(mm)			Reels/ box
	a	b	c	
7inch	245	220	182	Up to 10 Reels
	245	220	86	Up to 5 Reels

### ① SIDE

**A1★1S1**  
 SPMWHT223MD5WA★0S0 A1★1S1 01  
 GLAV94001 / 1001 / 40,000 pcs  
 SAMSUNG [Box Label]



## 2) Aluminum Packing Bag



**CAUTION**

This bag contains  
**MOISTURE SENSITIVE DEVICES**

**LEVEL**  
**2a**

1. Shelf life in sealed bag: 12 months at <40°C and < 90% relative humidity (RH)
2. Peak package body temperature: 240 °C
3. After this bag is opened, devices that will be subjected to reflow solder or other high temperature processes must be:
  - a. Mounted within 672 hours at factory conditions of equal to or less than 30°C / 60% RH, or
  - b. Stored at < 10% RH
4. Devices require bake, before mounting, if:
  - a. Humidity Indicator Card is > 60% when read at 23±5°C, or
  - b. 2a is not met.
5. If baking is required, devices must be baked for 10 ~ 24 hours at 60±5°C

Note: If device containers cannot be subjected to high temperature or shorter bake times are desired, reference IPC/JEDEC J-STD-033 for bake procedure.

Bag seal due date: \_\_\_\_\_  
(if blank, see code label)

Note: Level and body temperature by IPC/JEDEC J-STD-020



**A1★1S1**

SPMWHT223MD5WA★0S0 A1★1S1 01  
 GLAV94001 / 1001 / 4,000 pcs







**ATTENTION**

OBSERVE PRECAUTIONS  
FOR HANDLING  
ELECTROSTATIC  
SENSITIVE  
DEVICES



**■ 주의 사항**

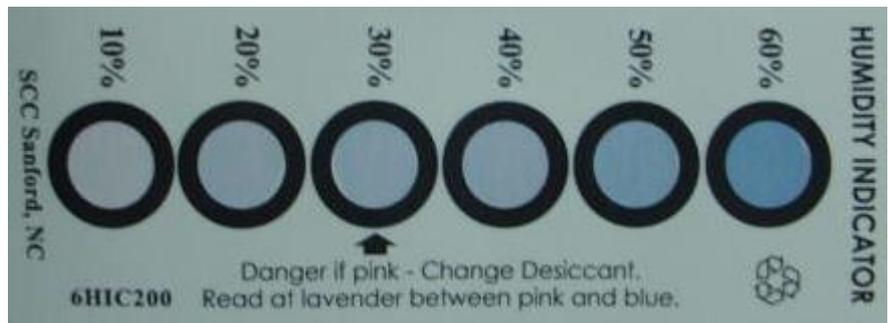
이 알루미늄 지퍼 백은 습기 및 정전기로부터 제품을 보호하기 위하여 제작되었습니다. 개봉 후에는 즉시 솔더 작업을 실시하는 것을 권장합니다.

습기 및 정전기로부터 제품을 보호 하기 위해서 개봉 후 사용하지 않는 자재는 본 팩에 넣어 보관 하시기 바랍니다. 사용하지 않는 자재를 본 팩에 넣을 때는 반드시 동봉된 드라이 팩과 함께 넣고 지퍼부분을 완전하게 밀봉하여 주시기 바랍니다.

**■ Important**

This Al Zipper bag is designed to protect the enclosed products from moisture and ESD. Once opened, the products should be soldered onto the printed circuit board immediately. When not in use, please do not leave the products unprotected by the Al Zipper Bag. To repack unused products., please ensure the zip-lock is completely sealed with the dry pack left inside.

## Silica gel & Humidity Indicator Card in Aluminum Vinyl Bag



# 11. Kitting Rule

## 1) Kitting bin Concept – 2700K, 3000K, 3500K and 4000K

1. This item is included to ☆K models.
2. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin( $V_F$ , Color, lm).
3. A forward voltage( $V_F$ ) of kitting bin is combined by a pair of same  $V_F$  rank such as (A1+A1), (A2+A2), (A3+A3), (A4+A4) or (AZ+AZ).
4. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation)  
Especially, one of 1, 2, 3, or 4 rank can be mixed with other rank, or can be used alone.
5. A luminous flux(lm) is average by kitting procedure.(below kitting simulation)  
For example Kitting lm is average S1 and S2 [ Kitting lm =  $(S1+S2)/2$  ]
6. '□' means one of the W(2700K), V(3000K), U(3500K) and T(4000K) a segment of the CCT rank.

### [Kitting example]

Target

D	E	F	G
9	A	B	C
5	6	7	8
1	2	3	4

User can get the green box position by kitting combination.

Kitting Combination : +

D	E	F	G	D	E	F	G	D	E	F	G	D	E	F	G	D	E	F	G																				
9	A	B	C	9	A	B	C	9	A	B	C	9	A	B	C	9	A	B	C	9	A	B	C	9	A	B	C												
5	6	7	8	5	6	7	8	5	6	7	8	5	6	7	8	5	6	7	8	5	6	7	8	5	6	7	8	5	6	7	8								
1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4

D	E	F	G	D	E	F	G	D	E	F	G	D	E	F	G	D	E	F	G								
9	A	B	C	9	A	B	C	9	A	B	C	9	A	B	C	9	A	B	C	9	A	B	C	9	A	B	C
5	6	7	8	5	6	7	8	5	6	7	8	5	6	7	8	5	6	7	8	5	6	7	8	5	6	7	8
1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4

D	E	F	G	D	E	F	G	D	E	F	G	D	E	F	G
9	A	B	C	9	A	B	C	9	A	B	C	9	A	B	C
5	6	7	8	5	6	7	8	5	6	7	8	5	6	7	8
1	2	3	4	1	2	3	4	1	2	3	4	1	2	3	4

















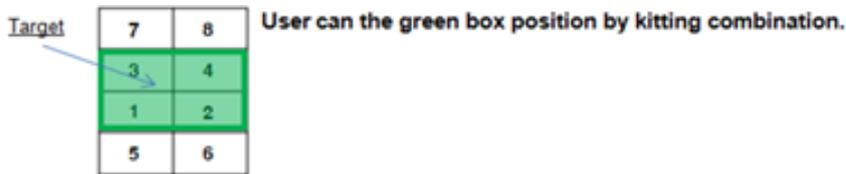
**[Kitting combination - 2700K, 3000K, 3500K and 4000K]**

-	RANK 1	RANK 2										
1	A3□7S1	A3□7S1	A4□BS1	A4□BS1								
2	A3□7S1	A3□7S2	A4□BS1	A4□BS2								
3	A3□7S1	A3□7S3	A4□BS1	A4□BS3								
4	A3□7S2	A3□7S1	A4□BS2	A4□BS1								
5	A3□7S2	A3□7S2	A4□BS2	A4□BS2								
6	A3□7S2	A3□7S3	A4□BS2	A4□BS3								
7	A3□7S3	A3□7S1	A4□BS3	A4□BS1								
8	A3□7S3	A3□7S2	A4□BS3	A4□BS2								
9	A3□7S3	A3□7S3	A4□BS3	A4□BS3								
10	A4□7S1	A4□7S1	AZ□BS1	AZ□BS1								
11	A4□7S1	A4□7S2	AZ□BS1	AZ□BS2								
12	A4□7S1	A4□7S3	AZ□BS1	AZ□BS3								
13	A4□7S2	A4□7S1	AZ□BS2	AZ□BS1								
14	A4□7S2	A4□7S2	AZ□BS2	AZ□BS2								
15	A4□7S2	A4□7S3	AZ□BS2	AZ□BS3								
16	A4□7S3	A4□7S1	AZ□BS3	AZ□BS1								
17	A4□7S3	A4□7S2	AZ□BS3	AZ□BS2								
18	A4□7S3	A4□7S3	AZ□BS3	AZ□BS3								
19	AZ□7S1	AZ□7S1										
20	AZ□7S1	AZ□7S2										
21	AZ□7S1	AZ□7S3										
22	AZ□7S2	AZ□7S1										
23	AZ□7S2	AZ□7S2										
24	AZ□7S2	AZ□7S3										
25	AZ□7S3	AZ□7S1										
26	AZ□7S3	AZ□7S2										
27	AZ□7S3	AZ□7S3										
28	A1□BS1	A1□BS1										
29	A1□BS1	A1□BS2										
30	A1□BS1	A1□BS3										
31	A1□BS2	A1□BS1										
32	A1□BS2	A1□BS2										
33	A1□BS2	A1□BS3										
34	A1□BS3	A1□BS1										
35	A1□BS3	A1□BS2										
36	A1□BS3	A1□BS3										
37	A2□BS1	A2□BS1										
38	A2□BS1	A2□BS2										
39	A2□BS1	A2□BS3										
40	A2□BS2	A2□BS1										
41	A2□BS2	A2□BS2										
42	A2□BS2	A2□BS3										
43	A2□BS3	A2□BS1										
44	A2□BS3	A2□BS2										
45	A2□BS3	A2□BS3										
46	A3□BS1	A3□BS1										
47	A3□BS1	A3□BS2										
48	A3□BS1	A3□BS3										
49	A3□BS2	A3□BS1										
50	A3□BS2	A3□BS2										
51	A3□BS2	A3□BS3										
52	A3□BS3	A3□BS1										
53	A3□BS3	A3□BS2										
54	A3□BS3	A3□BS3										

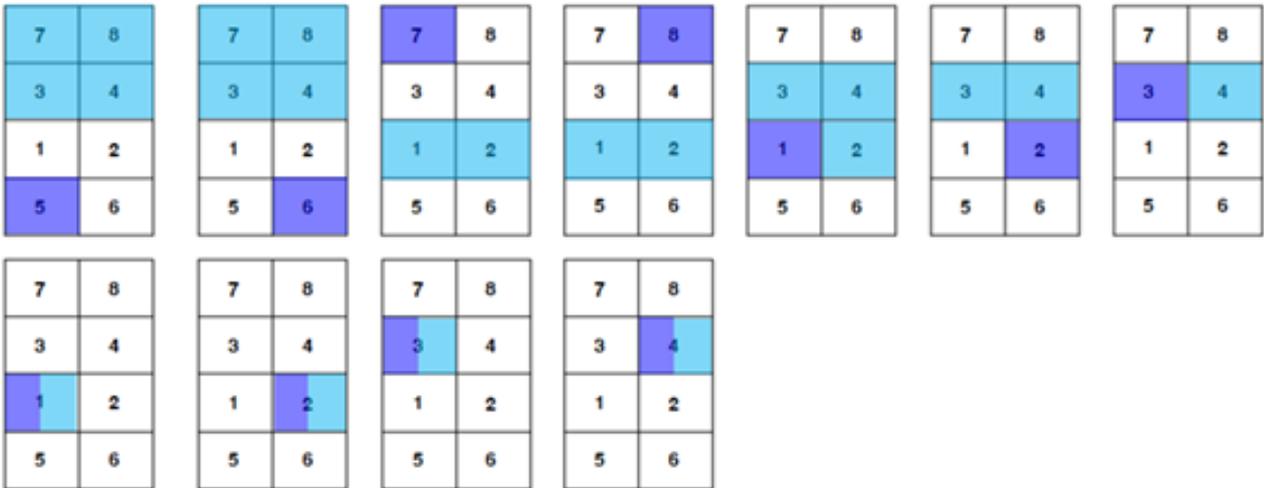
## 2) Kitting bin Concept – 5000K, 5700K and 6500K

1. This item is included to ☆K models.
2. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin( $V_F$ , Color,  $lm$ ).
3. A forward voltage( $V_F$ ) of kitting bin is combined by a pair of same  $V_F$  rank such as (A1+A1), (A2+A2), (A3+A3), (A4+A4) or (AZ+AZ).
4. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation) Especially, one of 1, 2, 3, or 4 rank can be mixed with other rank, or can be used alone.
5. A luminous flux( $lm$ ) is average by kitting procedure.(below kitting simulation)  
For example Kitting  $lm$  is average S1 and S2 [ Kitting  $lm = (S1+S2)/2$  ]
6. '○' means one of the R(5000K), Q(5700K) and P(6500K) a segment of the CCT rank.

### [Kitting example]



Kitting Combination :  + 





[Kitting combination - 5000K, 5700K and 6500K]

-	RANK 1	RANK 2										
1	A1O5S1	A1O7S1	A2O5S1	A2O8S1	A3O5S1	A3O3S1	A4O5S1	A4O4S1	AZO6S1	AZO7S1	A1O6S1	A1O8S1
2	A1O5S1	A1O7S2	A2O5S1	A2O8S2	A3O5S1	A3O3S2	A4O5S1	A4O4S2	AZO6S1	AZO7S2	A1O6S1	A1O8S2
3	A1O5S1	A1O7S3	A2O5S1	A2O8S3	A3O5S1	A3O3S3	A4O5S1	A4O4S3	AZO6S1	AZO7S3	A1O6S1	A1O8S3
4	A1O5S2	A1O7S1	A2O5S2	A2O8S1	A3O5S2	A3O3S1	A4O5S2	A4O4S1	AZO6S2	AZO7S1	A1O6S2	A1O8S1
5	A1O5S2	A1O7S2	A2O5S2	A2O8S2	A3O5S2	A3O3S2	A4O5S2	A4O4S2	AZO6S2	AZO7S2	A1O6S2	A1O8S2
6	A1O5S2	A1O7S3	A2O5S2	A2O8S3	A3O5S2	A3O3S3	A4O5S2	A4O4S3	AZO6S2	AZO7S3	A1O6S2	A1O8S3
7	A1O5S3	A1O7S1	A2O5S3	A2O8S1	A3O5S3	A3O3S1	A4O5S3	A4O4S1	AZO6S3	AZO7S1	A1O6S3	A1O8S1
8	A1O5S3	A1O7S2	A2O5S3	A2O8S2	A3O5S3	A3O3S2	A4O5S3	A4O4S2	AZO6S3	AZO7S2	A1O6S3	A1O8S2
9	A1O5S3	A1O7S3	A2O5S3	A2O8S3	A3O5S3	A3O3S3	A4O5S3	A4O4S3	AZO6S3	AZO7S3	A1O6S3	A1O8S3
10	A2O5S1	A2O7S1	A3O5S1	A3O8S1	A4O5S1	A4O3S1	AZO5S1	AZO4S1	A1O6S1	A1O3S1	A2O6S1	A2O8S1
11	A2O5S1	A2O7S2	A3O5S1	A3O8S2	A4O5S1	A4O3S2	AZO5S1	AZO4S2	A1O6S1	A1O3S2	A2O6S1	A2O8S2
12	A2O5S1	A2O7S3	A3O5S1	A3O8S3	A4O5S1	A4O3S3	AZO5S1	AZO4S3	A1O6S1	A1O3S3	A2O6S1	A2O8S3
13	A2O5S2	A2O7S1	A3O5S2	A3O8S1	A4O5S2	A4O3S1	AZO5S2	AZO4S1	A1O6S2	A1O3S1	A2O6S2	A2O8S1
14	A2O5S2	A2O7S2	A3O5S2	A3O8S2	A4O5S2	A4O3S2	AZO5S2	AZO4S2	A1O6S2	A1O3S2	A2O6S2	A2O8S2
15	A2O5S2	A2O7S3	A3O5S2	A3O8S3	A4O5S2	A4O3S3	AZO5S2	AZO4S3	A1O6S2	A1O3S3	A2O6S2	A2O8S3
16	A2O5S3	A2O7S1	A3O5S3	A3O8S1	A4O5S3	A4O3S1	AZO5S3	AZO4S1	A1O6S3	A1O3S1	A2O6S3	A2O8S1
17	A2O5S3	A2O7S2	A3O5S3	A3O8S2	A4O5S3	A4O3S2	AZO5S3	AZO4S2	A1O6S3	A1O3S2	A2O6S3	A2O8S2
18	A2O5S3	A2O7S3	A3O5S3	A3O8S3	A4O5S3	A4O3S3	AZO5S3	AZO4S3	A1O6S3	A1O3S3	A2O6S3	A2O8S3
19	A3O5S1	A3O7S1	A4O5S1	A4O8S1	AZO5S1	AZO3S1	A1O6S1	A1O7S1	A2O6S1	A2O3S1	A3O6S1	A3O8S1
20	A3O5S1	A3O7S2	A4O5S1	A4O8S2	AZO5S1	AZO3S2	A1O6S1	A1O7S2	A2O6S1	A2O3S2	A3O6S1	A3O8S2
21	A3O5S1	A3O7S3	A4O5S1	A4O8S3	AZO5S1	AZO3S3	A1O6S1	A1O7S3	A2O6S1	A2O3S3	A3O6S1	A3O8S3
22	A3O5S2	A3O7S1	A4O5S2	A4O8S1	AZO5S2	AZO3S1	A1O6S2	A1O7S1	A2O6S2	A2O3S1	A3O6S2	A3O8S1
23	A3O5S2	A3O7S2	A4O5S2	A4O8S2	AZO5S2	AZO3S2	A1O6S2	A1O7S2	A2O6S2	A2O3S2	A3O6S2	A3O8S2
24	A3O5S2	A3O7S3	A4O5S2	A4O8S3	AZO5S2	AZO3S3	A1O6S2	A1O7S3	A2O6S2	A2O3S3	A3O6S2	A3O8S3
25	A3O5S3	A3O7S1	A4O5S3	A4O8S1	AZO5S3	AZO3S1	A1O6S3	A1O7S1	A2O6S3	A2O3S1	A3O6S3	A3O8S1
26	A3O5S3	A3O7S2	A4O5S3	A4O8S2	AZO5S3	AZO3S2	A1O6S3	A1O7S2	A2O6S3	A2O3S2	A3O6S3	A3O8S2
27	A3O5S3	A3O7S3	A4O5S3	A4O8S3	AZO5S3	AZO3S3	A1O6S3	A1O7S3	A2O6S3	A2O3S3	A3O6S3	A3O8S3
28	A4O5S1	A4O7S1	AZO5S1	AZO8S1	A1O5S1	A1O4S1	A2O6S1	A2O7S1	A3O6S1	A3O3S1	A4O6S1	A4O8S1
29	A4O5S1	A4O7S2	AZO5S1	AZO8S2	A1O5S1	A1O4S2	A2O6S1	A2O7S2	A3O6S1	A3O3S2	A4O6S1	A4O8S2
30	A4O5S1	A4O7S3	AZO5S1	AZO8S3	A1O5S1	A1O4S3	A2O6S1	A2O7S3	A3O6S1	A3O3S3	A4O6S1	A4O8S3
31	A4O5S2	A4O7S1	AZO5S2	AZO8S1	A1O5S2	A1O4S1	A2O6S2	A2O7S1	A3O6S2	A3O3S1	A4O6S2	A4O8S1
32	A4O5S2	A4O7S2	AZO5S2	AZO8S2	A1O5S2	A1O4S2	A2O6S2	A2O7S2	A3O6S2	A3O3S2	A4O6S2	A4O8S2
33	A4O5S2	A4O7S3	AZO5S2	AZO8S3	A1O5S2	A1O4S3	A2O6S2	A2O7S3	A3O6S2	A3O3S3	A4O6S2	A4O8S3
34	A4O5S3	A4O7S1	AZO5S3	AZO8S1	A1O5S3	A1O4S1	A2O6S3	A2O7S1	A3O6S3	A3O3S1	A4O6S3	A4O8S1
35	A4O5S3	A4O7S2	AZO5S3	AZO8S2	A1O5S3	A1O4S2	A2O6S3	A2O7S2	A3O6S3	A3O3S2	A4O6S3	A4O8S2
36	A4O5S3	A4O7S3	AZO5S3	AZO8S3	A1O5S3	A1O4S3	A2O6S3	A2O7S3	A3O6S3	A3O3S3	A4O6S3	A4O8S3
37	AZO5S1	AZO7S1	A1O5S1	A1O3S1	A2O5S1	A2O4S1	A3O6S1	A3O7S1	A4O6S1	A4O3S1	AZO6S1	AZO8S1
38	AZO5S1	AZO7S2	A1O5S1	A1O3S2	A2O5S1	A2O4S2	A3O6S1	A3O7S2	A4O6S1	A4O3S2	AZO6S1	AZO8S2
39	AZO5S1	AZO7S3	A1O5S1	A1O3S3	A2O5S1	A2O4S3	A3O6S1	A3O7S3	A4O6S1	A4O3S3	AZO6S1	AZO8S3
40	AZO5S2	AZO7S1	A1O5S2	A1O3S1	A2O5S2	A2O4S1	A3O6S2	A3O7S1	A4O6S2	A4O3S1	AZO6S2	AZO8S1
41	AZO5S2	AZO7S2	A1O5S2	A1O3S2	A2O5S2	A2O4S2	A3O6S2	A3O7S2	A4O6S2	A4O3S2	AZO6S2	AZO8S2
42	AZO5S2	AZO7S3	A1O5S2	A1O3S3	A2O5S2	A2O4S3	A3O6S2	A3O7S3	A4O6S2	A4O3S3	AZO6S2	AZO8S3
43	AZO5S3	AZO7S1	A1O5S3	A1O3S1	A2O5S3	A2O4S1	A3O6S3	A3O7S1	A4O6S3	A4O3S1	AZO6S3	AZO8S1
44	AZO5S3	AZO7S2	A1O5S3	A1O3S2	A2O5S3	A2O4S2	A3O6S3	A3O7S2	A4O6S3	A4O3S2	AZO6S3	AZO8S2
45	AZO5S3	AZO7S3	A1O5S3	A1O3S3	A2O5S3	A2O4S3	A3O6S3	A3O7S3	A4O6S3	A4O3S3	AZO6S3	AZO8S3
46	A1O5S1	A1O8S1	A2O5S1	A2O3S1	A3O5S1	A3O4S1	A4O6S1	A4O7S1	AZO6S1	AZO3S1	A1O6S1	A1O4S1
47	A1O5S1	A1O8S2	A2O5S1	A2O3S2	A3O5S1	A3O4S2	A4O6S1	A4O7S2	AZO6S1	AZO3S2	A1O6S1	A1O4S2
48	A1O5S1	A1O8S3	A2O5S1	A2O3S3	A3O5S1	A3O4S3	A4O6S1	A4O7S3	AZO6S1	AZO3S3	A1O6S1	A1O4S3
49	A1O5S2	A1O8S1	A2O5S2	A2O3S1	A3O5S2	A3O4S1	A4O6S2	A4O7S1	AZO6S2	AZO3S1	A1O6S2	A1O4S1
50	A1O5S2	A1O8S2	A2O5S2	A2O3S2	A3O5S2	A3O4S2	A4O6S2	A4O7S2	AZO6S2	AZO3S2	A1O6S2	A1O4S2
51	A1O5S2	A1O8S3	A2O5S2	A2O3S3	A3O5S2	A3O4S3	A4O6S2	A4O7S3	AZO6S2	AZO3S3	A1O6S2	A1O4S3
52	A1O5S3	A1O8S1	A2O5S3	A2O3S1	A3O5S3	A3O4S1	A4O6S3	A4O7S1	AZO6S3	AZO3S1	A1O6S3	A1O4S1
53	A1O5S3	A1O8S2	A2O5S3	A2O3S2	A3O5S3	A3O4S2	A4O6S3	A4O7S2	AZO6S3	AZO3S2	A1O6S3	A1O4S2
54	A1O5S3	A1O8S3	A2O5S3	A2O3S3	A3O5S3	A3O4S3	A4O6S3	A4O7S3	AZO6S3	AZO3S3	A1O6S3	A1O4S3



[Kitting combination - 5000K, 5700K and 6500K]

-	RANK 1	RANK 2										
1	A2O6S1	A2O4S1	A3O7S1	A3O1S1	A4O7S1	A4O2S1	AZO8S1	AZO1S1	A1O1S1	A1O2S1	A2O1S1	A2O3S1
2	A2O6S1	A2O4S2	A3O7S1	A3O1S2	A4O7S1	A4O2S2	AZO8S1	AZO1S2	A1O1S1	A1O2S2	A2O1S1	A2O3S2
3	A2O6S1	A2O4S3	A3O7S1	A3O1S3	A4O7S1	A4O2S3	AZO8S1	AZO1S3	A1O1S1	A1O2S3	A2O1S1	A2O3S3
4	A2O6S2	A2O4S1	A3O7S2	A3O1S1	A4O7S2	A4O2S1	AZO8S2	AZO1S1	A1O1S2	A1O2S1	A2O1S2	A2O3S1
5	A2O6S2	A2O4S2	A3O7S2	A3O1S2	A4O7S2	A4O2S2	AZO8S2	AZO1S2	A1O1S2	A1O2S2	A2O1S2	A2O3S2
6	A2O6S2	A2O4S3	A3O7S2	A3O1S3	A4O7S2	A4O2S3	AZO8S2	AZO1S3	A1O1S2	A1O2S3	A2O1S2	A2O3S3
7	A2O6S3	A2O4S1	A3O7S3	A3O1S1	A4O7S3	A4O2S1	AZO8S3	AZO1S1	A1O1S3	A1O2S1	A2O1S3	A2O3S1
8	A2O6S3	A2O4S2	A3O7S3	A3O1S2	A4O7S3	A4O2S2	AZO8S3	AZO1S2	A1O1S3	A1O2S2	A2O1S3	A2O3S2
9	A2O6S3	A2O4S3	A3O7S3	A3O1S3	A4O7S3	A4O2S3	AZO8S3	AZO1S3	A1O1S3	A1O2S3	A2O1S3	A2O3S3
10	A3O6S1	A3O4S1	A4O7S1	A4O1S1	AZO7S1	AZO2S1	A1O8S1	A1O2S1	A2O1S1	A2O2S1	A3O1S1	A3O3S1
11	A3O6S1	A3O4S2	A4O7S1	A4O1S2	AZO7S1	AZO2S2	A1O8S1	A1O2S2	A2O1S1	A2O2S2	A3O1S1	A3O3S2
12	A3O6S1	A3O4S3	A4O7S1	A4O1S3	AZO7S1	AZO2S3	A1O8S1	A1O2S3	A2O1S1	A2O2S3	A3O1S1	A3O3S3
13	A3O6S2	A3O4S1	A4O7S2	A4O1S1	AZO7S2	AZO2S1	A1O8S2	A1O2S1	A2O1S2	A2O2S1	A3O1S2	A3O3S1
14	A3O6S2	A3O4S2	A4O7S2	A4O1S2	AZO7S2	AZO2S2	A1O8S2	A1O2S2	A2O1S2	A2O2S2	A3O1S2	A3O3S2
15	A3O6S2	A3O4S3	A4O7S2	A4O1S3	AZO7S2	AZO2S3	A1O8S2	A1O2S3	A2O1S2	A2O2S3	A3O1S2	A3O3S3
16	A3O6S3	A3O4S1	A4O7S3	A4O1S1	AZO7S3	AZO2S1	A1O8S3	A1O2S1	A2O1S3	A2O2S1	A3O1S3	A3O3S1
17	A3O6S3	A3O4S2	A4O7S3	A4O1S2	AZO7S3	AZO2S2	A1O8S3	A1O2S2	A2O1S3	A2O2S2	A3O1S3	A3O3S2
18	A3O6S3	A3O4S3	A4O7S3	A4O1S3	AZO7S3	AZO2S3	A1O8S3	A1O2S3	A2O1S3	A2O2S3	A3O1S3	A3O3S3
19	A4O6S1	A4O4S1	AZO7S1	AZO1S1	A1O8S1	A1O1S1	A2O8S1	A2O2S1	A3O1S1	A3O2S1	A4O1S1	A4O3S1
20	A4O6S1	A4O4S2	AZO7S1	AZO1S2	A1O8S1	A1O1S2	A2O8S1	A2O2S2	A3O1S1	A3O2S2	A4O1S1	A4O3S2
21	A4O6S1	A4O4S3	AZO7S1	AZO1S3	A1O8S1	A1O1S3	A2O8S1	A2O2S3	A3O1S1	A3O2S3	A4O1S1	A4O3S3
22	A4O6S2	A4O4S1	AZO7S2	AZO1S1	A1O8S2	A1O1S1	A2O8S2	A2O2S1	A3O1S2	A3O2S1	A4O1S2	A4O3S1
23	A4O6S2	A4O4S2	AZO7S2	AZO1S2	A1O8S2	A1O1S2	A2O8S2	A2O2S2	A3O1S2	A3O2S2	A4O1S2	A4O3S2
24	A4O6S2	A4O4S3	AZO7S2	AZO1S3	A1O8S2	A1O1S3	A2O8S2	A2O2S3	A3O1S2	A3O2S3	A4O1S2	A4O3S3
25	A4O6S3	A4O4S1	AZO7S3	AZO1S1	A1O8S3	A1O1S1	A2O8S3	A2O2S1	A3O1S3	A3O2S1	A4O1S3	A4O3S1
26	A4O6S3	A4O4S2	AZO7S3	AZO1S2	A1O8S3	A1O1S2	A2O8S3	A2O2S2	A3O1S3	A3O2S2	A4O1S3	A4O3S2
27	A4O6S3	A4O4S3	AZO7S3	AZO1S3	A1O8S3	A1O1S3	A2O8S3	A2O2S3	A3O1S3	A3O2S3	A4O1S3	A4O3S3
28	AZO6S1	AZO4S1	A1O7S1	A1O2S1	A2O8S1	A2O1S1	A3O8S1	A3O2S1	A4O1S1	A4O2S1	AZO1S1	AZO3S1
29	AZO6S1	AZO4S2	A1O7S1	A1O2S2	A2O8S1	A2O1S2	A3O8S1	A3O2S2	A4O1S1	A4O2S2	AZO1S1	AZO3S2
30	AZO6S1	AZO4S3	A1O7S1	A1O2S3	A2O8S1	A2O1S3	A3O8S1	A3O2S3	A4O1S1	A4O2S3	AZO1S1	AZO3S3
31	AZO6S2	AZO4S1	A1O7S2	A1O2S1	A2O8S2	A2O1S1	A3O8S2	A3O2S1	A4O1S2	A4O2S1	AZO1S2	AZO3S1
32	AZO6S2	AZO4S2	A1O7S2	A1O2S2	A2O8S2	A2O1S2	A3O8S2	A3O2S2	A4O1S2	A4O2S2	AZO1S2	AZO3S2
33	AZO6S2	AZO4S3	A1O7S2	A1O2S3	A2O8S2	A2O1S3	A3O8S2	A3O2S3	A4O1S2	A4O2S3	AZO1S2	AZO3S3
34	AZO6S3	AZO4S1	A1O7S3	A1O2S1	A2O8S3	A2O1S1	A3O8S3	A3O2S1	A4O1S3	A4O2S1	AZO1S3	AZO3S1
35	AZO6S3	AZO4S2	A1O7S3	A1O2S2	A2O8S3	A2O1S2	A3O8S3	A3O2S2	A4O1S3	A4O2S2	AZO1S3	AZO3S2
36	AZO6S3	AZO4S3	A1O7S3	A1O2S3	A2O8S3	A2O1S3	A3O8S3	A3O2S3	A4O1S3	A4O2S3	AZO1S3	AZO3S3
37	A1O7S1	A1O1S1	A2O7S1	A2O2S1	A3O8S1	A3O1S1	A4O8S1	A4O2S1	AZO1S1	AZO2S1	A1O1S1	A1O4S1
38	A1O7S1	A1O1S2	A2O7S1	A2O2S2	A3O8S1	A3O1S2	A4O8S1	A4O2S2	AZO1S1	AZO2S2	A1O1S1	A1O4S2
39	A1O7S1	A1O1S3	A2O7S1	A2O2S3	A3O8S1	A3O1S3	A4O8S1	A4O2S3	AZO1S1	AZO2S3	A1O1S1	A1O4S3
40	A1O7S2	A1O1S1	A2O7S2	A2O2S1	A3O8S2	A3O1S1	A4O8S2	A4O2S1	AZO1S2	AZO2S1	A1O1S2	A1O4S1
41	A1O7S2	A1O1S2	A2O7S2	A2O2S2	A3O8S2	A3O1S2	A4O8S2	A4O2S2	AZO1S2	AZO2S2	A1O1S2	A1O4S2
42	A1O7S2	A1O1S3	A2O7S2	A2O2S3	A3O8S2	A3O1S3	A4O8S2	A4O2S3	AZO1S2	AZO2S3	A1O1S2	A1O4S3
43	A1O7S3	A1O1S1	A2O7S3	A2O2S1	A3O8S3	A3O1S1	A4O8S3	A4O2S1	AZO1S3	AZO2S1	A1O1S3	A1O4S1
44	A1O7S3	A1O1S2	A2O7S3	A2O2S2	A3O8S3	A3O1S2	A4O8S3	A4O2S2	AZO1S3	AZO2S2	A1O1S3	A1O4S2
45	A1O7S3	A1O1S3	A2O7S3	A2O2S3	A3O8S3	A3O1S3	A4O8S3	A4O2S3	AZO1S3	AZO2S3	A1O1S3	A1O4S3
46	A2O7S1	A2O1S1	A3O7S1	A3O2S1	A4O8S1	A4O1S1	AZO8S1	AZO2S1	A1O1S1	A1O3S1	A2O1S1	A2O4S1
47	A2O7S1	A2O1S2	A3O7S1	A3O2S2	A4O8S1	A4O1S2	AZO8S1	AZO2S2	A1O1S1	A1O3S2	A2O1S1	A2O4S2
48	A2O7S1	A2O1S3	A3O7S1	A3O2S3	A4O8S1	A4O1S3	AZO8S1	AZO2S3	A1O1S1	A1O3S3	A2O1S1	A2O4S3
49	A2O7S2	A2O1S1	A3O7S2	A3O2S1	A4O8S2	A4O1S1	AZO8S2	AZO2S1	A1O1S2	A1O3S1	A2O1S2	A2O4S1
50	A2O7S2	A2O1S2	A3O7S2	A3O2S2	A4O8S2	A4O1S2	AZO8S2	AZO2S2	A1O1S2	A1O3S2	A2O1S2	A2O4S2
51	A2O7S2	A2O1S3	A3O7S2	A3O2S3	A4O8S2	A4O1S3	AZO8S2	AZO2S3	A1O1S2	A1O3S3	A2O1S2	A2O4S3
52	A2O7S3	A2O1S1	A3O7S3	A3O2S1	A4O8S3	A4O1S1	AZO8S3	AZO2S1	A1O1S3	A1O3S1	A2O1S3	A2O4S1
53	A2O7S3	A2O1S2	A3O7S3	A3O2S2	A4O8S3	A4O1S2	AZO8S3	AZO2S2	A1O1S3	A1O3S2	A2O1S3	A2O4S2
54	A2O7S3	A2O1S3	A3O7S3	A3O2S3	A4O8S3	A4O1S3	AZO8S3	AZO2S3	A1O1S3	A1O3S3	A2O1S3	A2O4S3



**[Kitting combination - 5000K, 5700K and 6500K]**

-	RANK 1	RANK 2										
1	A3O1S1	A3O4S1	A4O2S1	A4O3S1	AZO2S1	AZO4S1	A1O1S1	A1O1S1	A2O2S1	A2O2S1	A3O3S1	A3O3S1
2	A3O1S1	A3O4S2	A4O2S1	A4O3S2	AZO2S1	AZO4S2	A1O1S1	A1O1S2	A2O2S1	A2O2S2	A3O3S1	A3O3S2
3	A3O1S1	A3O4S3	A4O2S1	A4O3S3	AZO2S1	AZO4S3	A1O1S1	A1O1S3	A2O2S1	A2O2S3	A3O3S1	A3O3S3
4	A3O1S2	A3O4S1	A4O2S2	A4O3S1	AZO2S2	AZO4S1	A1O1S2	A1O1S1	A2O2S2	A2O2S1	A3O3S2	A3O3S1
5	A3O1S2	A3O4S2	A4O2S2	A4O3S2	AZO2S2	AZO4S2	A1O1S2	A1O1S2	A2O2S2	A2O2S2	A3O3S2	A3O3S2
6	A3O1S2	A3O4S3	A4O2S2	A4O3S3	AZO2S2	AZO4S3	A1O1S2	A1O1S3	A2O2S2	A2O2S3	A3O3S2	A3O3S3
7	A3O1S3	A3O4S1	A4O2S3	A4O3S1	AZO2S3	AZO4S1	A1O1S3	A1O1S1	A2O2S3	A2O2S1	A3O3S3	A3O3S1
8	A3O1S3	A3O4S2	A4O2S3	A4O3S2	AZO2S3	AZO4S2	A1O1S3	A1O1S2	A2O2S3	A2O2S2	A3O3S3	A3O3S2
9	A3O1S3	A3O4S3	A4O2S3	A4O3S3	AZO2S3	AZO4S3	A1O1S3	A1O1S3	A2O2S3	A2O2S3	A3O3S3	A3O3S3
10	A4O1S1	A4O4S1	AZO2S1	AZO3S1	A1O3S1	A1O4S1	A2O1S1	A2O1S1	A3O2S1	A3O2S1	A4O3S1	A4O3S1
11	A4O1S1	A4O4S2	AZO2S1	AZO3S2	A1O3S1	A1O4S2	A2O1S1	A2O1S2	A3O2S1	A3O2S2	A4O3S1	A4O3S2
12	A4O1S1	A4O4S3	AZO2S1	AZO3S3	A1O3S1	A1O4S3	A2O1S1	A2O1S3	A3O2S1	A3O2S3	A4O3S1	A4O3S3
13	A4O1S2	A4O4S1	AZO2S2	AZO3S1	A1O3S2	A1O4S1	A2O1S2	A2O1S1	A3O2S2	A3O2S1	A4O3S2	A4O3S1
14	A4O1S2	A4O4S2	AZO2S2	AZO3S2	A1O3S2	A1O4S2	A2O1S2	A2O1S2	A3O2S2	A3O2S2	A4O3S2	A4O3S2
15	A4O1S2	A4O4S3	AZO2S2	AZO3S3	A1O3S2	A1O4S3	A2O1S2	A2O1S3	A3O2S2	A3O2S3	A4O3S2	A4O3S3
16	A4O1S3	A4O4S1	AZO2S3	AZO3S1	A1O3S3	A1O4S1	A2O1S3	A2O1S1	A3O2S3	A3O2S1	A4O3S3	A4O3S1
17	A4O1S3	A4O4S2	AZO2S3	AZO3S2	A1O3S3	A1O4S2	A2O1S3	A2O1S2	A3O2S3	A3O2S2	A4O3S3	A4O3S2
18	A4O1S3	A4O4S3	AZO2S3	AZO3S3	A1O3S3	A1O4S3	A2O1S3	A2O1S3	A3O2S3	A3O2S3	A4O3S3	A4O3S3
19	AZO1S1	AZO4S1	A1O2S1	A1O4S1	A2O3S1	A2O4S1	A3O1S1	A3O1S1	A4O2S1	A4O2S1	AZO3S1	AZO3S1
20	AZO1S1	AZO4S2	A1O2S1	A1O4S2	A2O3S1	A2O4S2	A3O1S1	A3O1S2	A4O2S1	A4O2S2	AZO3S1	AZO3S2
21	AZO1S1	AZO4S3	A1O2S1	A1O4S3	A2O3S1	A2O4S3	A3O1S1	A3O1S3	A4O2S1	A4O2S3	AZO3S1	AZO3S3
22	AZO1S2	AZO4S1	A1O2S2	A1O4S1	A2O3S2	A2O4S1	A3O1S2	A3O1S1	A4O2S2	A4O2S1	AZO3S2	AZO3S1
23	AZO1S2	AZO4S2	A1O2S2	A1O4S2	A2O3S2	A2O4S2	A3O1S2	A3O1S2	A4O2S2	A4O2S2	AZO3S2	AZO3S2
24	AZO1S2	AZO4S3	A1O2S2	A1O4S3	A2O3S2	A2O4S3	A3O1S2	A3O1S3	A4O2S2	A4O2S3	AZO3S2	AZO3S3
25	AZO1S3	AZO4S1	A1O2S3	A1O4S1	A2O3S3	A2O4S1	A3O1S3	A3O1S1	A4O2S3	A4O2S1	AZO3S3	AZO3S1
26	AZO1S3	AZO4S2	A1O2S3	A1O4S2	A2O3S3	A2O4S2	A3O1S3	A3O1S2	A4O2S3	A4O2S2	AZO3S3	AZO3S2
27	AZO1S3	AZO4S3	A1O2S3	A1O4S3	A2O3S3	A2O4S3	A3O1S3	A3O1S3	A4O2S3	A4O2S3	AZO3S3	AZO3S3
28	A1O2S1	A1O3S1	A2O2S1	A2O4S1	A3O3S1	A3O4S1	A4O1S1	A4O1S1	AZO2S1	AZO2S1	A1O4S1	A1O4S1
29	A1O2S1	A1O3S2	A2O2S1	A2O4S2	A3O3S1	A3O4S2	A4O1S1	A4O1S2	AZO2S1	AZO2S2	A1O4S1	A1O4S2
30	A1O2S1	A1O3S3	A2O2S1	A2O4S3	A3O3S1	A3O4S3	A4O1S1	A4O1S3	AZO2S1	AZO2S3	A1O4S1	A1O4S3
31	A1O2S2	A1O3S1	A2O2S2	A2O4S1	A3O3S2	A3O4S1	A4O1S2	A4O1S1	AZO2S2	AZO2S1	A1O4S2	A1O4S1
32	A1O2S2	A1O3S2	A2O2S2	A2O4S2	A3O3S2	A3O4S2	A4O1S2	A4O1S2	AZO2S2	AZO2S2	A1O4S2	A1O4S2
33	A1O2S2	A1O3S3	A2O2S2	A2O4S3	A3O3S2	A3O4S3	A4O1S2	A4O1S3	AZO2S2	AZO2S3	A1O4S2	A1O4S3
34	A1O2S3	A1O3S1	A2O2S3	A2O4S1	A3O3S3	A3O4S1	A4O1S3	A4O1S1	AZO2S3	AZO2S1	A1O4S3	A1O4S1
35	A1O2S3	A1O3S2	A2O2S3	A2O4S2	A3O3S3	A3O4S2	A4O1S3	A4O1S2	AZO2S3	AZO2S2	A1O4S3	A1O4S2
36	A1O2S3	A1O3S3	A2O2S3	A2O4S3	A3O3S3	A3O4S3	A4O1S3	A4O1S3	AZO2S3	AZO2S3	A1O4S3	A1O4S3
37	A2O2S1	A2O3S1	A3O2S1	A3O4S1	A4O3S1	A4O4S1	AZO1S1	AZO1S1	A1O3S1	A1O3S1	A2O4S1	A2O4S1
38	A2O2S1	A2O3S2	A3O2S1	A3O4S2	A4O3S1	A4O4S2	AZO1S1	AZO1S2	A1O3S1	A1O3S2	A2O4S1	A2O4S2
39	A2O2S1	A2O3S3	A3O2S1	A3O4S3	A4O3S1	A4O4S3	AZO1S1	AZO1S3	A1O3S1	A1O3S3	A2O4S1	A2O4S3
40	A2O2S2	A2O3S1	A3O2S2	A3O4S1	A4O3S2	A4O4S1	AZO1S2	AZO1S1	A1O3S2	A1O3S1	A2O4S2	A2O4S1
41	A2O2S2	A2O3S2	A3O2S2	A3O4S2	A4O3S2	A4O4S2	AZO1S2	AZO1S2	A1O3S2	A1O3S2	A2O4S2	A2O4S2
42	A2O2S2	A2O3S3	A3O2S2	A3O4S3	A4O3S2	A4O4S3	AZO1S2	AZO1S3	A1O3S2	A1O3S3	A2O4S2	A2O4S3
43	A2O2S3	A2O3S1	A3O2S3	A3O4S1	A4O3S3	A4O4S1	AZO1S3	AZO1S1	A1O3S3	A1O3S1	A2O4S3	A2O4S1
44	A2O2S3	A2O3S2	A3O2S3	A3O4S2	A4O3S3	A4O4S2	AZO1S3	AZO1S2	A1O3S3	A1O3S2	A2O4S3	A2O4S2
45	A2O2S3	A2O3S3	A3O2S3	A3O4S3	A4O3S3	A4O4S3	AZO1S3	AZO1S3	A1O3S3	A1O3S3	A2O4S3	A2O4S3
46	A3O2S1	A3O3S1	A4O2S1	A4O4S1	AZO3S1	AZO4S1	A1O2S1	A1O2S1	A2O3S1	A2O3S1	A3O4S1	A3O4S1
47	A3O2S1	A3O3S2	A4O2S1	A4O4S2	AZO3S1	AZO4S2	A1O2S1	A1O2S2	A2O3S1	A2O3S2	A3O4S1	A3O4S2
48	A3O2S1	A3O3S3	A4O2S1	A4O4S3	AZO3S1	AZO4S3	A1O2S1	A1O2S3	A2O3S1	A2O3S3	A3O4S1	A3O4S3
49	A3O2S2	A3O3S1	A4O2S2	A4O4S1	AZO3S2	AZO4S1	A1O2S2	A1O2S1	A2O3S2	A2O3S1	A3O4S2	A3O4S1
50	A3O2S2	A3O3S2	A4O2S2	A4O4S2	AZO3S2	AZO4S2	A1O2S2	A1O2S2	A2O3S2	A2O3S2	A3O4S2	A3O4S2
51	A3O2S2	A3O3S3	A4O2S2	A4O4S3	AZO3S2	AZO4S3	A1O2S2	A1O2S3	A2O3S2	A2O3S3	A3O4S2	A3O4S3
52	A3O2S3	A3O3S1	A4O2S3	A4O4S1	AZO3S3	AZO4S1	A1O2S3	A1O2S1	A2O3S3	A2O3S1	A3O4S3	A3O4S1
53	A3O2S3	A3O3S2	A4O2S3	A4O4S2	AZO3S3	AZO4S2	A1O2S3	A1O2S2	A2O3S3	A2O3S2	A3O4S3	A3O4S2
54	A3O2S3	A3O3S3	A4O2S3	A4O4S3	AZO3S3	AZO4S3	A1O2S3	A1O2S3	A2O3S3	A2O3S3	A3O4S3	A3O4S3



**[Kitting combination - 5000K, 5700K and 6500K]**

-	RANK 1	RANK 2										
1	A4O4S1	A4O4S1										
2	A4O4S1	A4O4S2										
3	A4O4S1	A4O4S3										
4	A4O4S2	A4O4S1										
5	A4O4S2	A4O4S2										
6	A4O4S2	A4O4S3										
7	A4O4S3	A4O4S1										
8	A4O4S3	A4O4S2										
9	A4O4S3	A4O4S3										
10	AZO4S1	AZO4S1										
11	AZO4S1	AZO4S2										
12	AZO4S1	AZO4S3										
13	AZO4S2	AZO4S1										
14	AZO4S2	AZO4S2										
15	AZO4S2	AZO4S3										
16	AZO4S3	AZO4S1										
17	AZO4S3	AZO4S2										
18	AZO4S3	AZO4S3										
19												
20												
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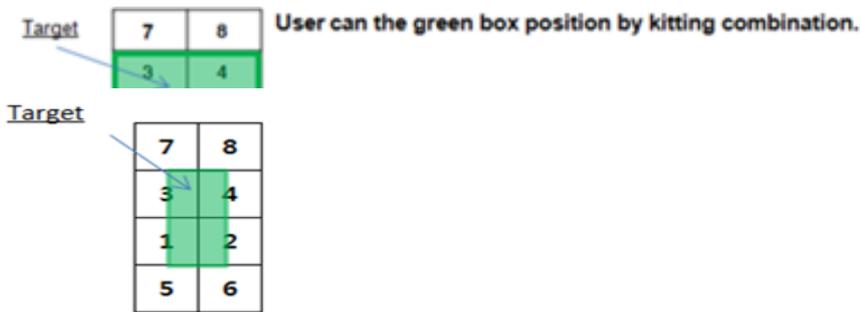


### 3) Kitting bin Concept – 5000K[SPMWHT223MD5WARD S0]

1. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin( $V_F$ , Color,  $l_m$ ).
2. A forward voltage( $V_F$ ) of kitting bin is combined by a pair of same  $V_F$  rank such as (A1+A1), (A2+A2), (A3+A3), (A4+A4) or (AZ+AZ).
3. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation)
4. A luminous flux( $l_m$ ) is average by kitting procedure.(below kitting simulation)

For example Kitting  $l_m$  is average S1 and S2 [ Kitting  $l_m = (S1+S2)/2$  ]

#### [Kitting example]



Kitting Combination :  + 

7	8	7	8	7	8	7	8	7	8	7	8	7	8	7	8
3	4	3	4	3	4	3	4	3	4	3	4	3	4	3	4
1	2	1	2	1	2	1	2	1	2	1	2	1	2	1	2
5	6	5	6	5	6	5	6	5	6	5	6	5	6	5	6



[Kitting combination - 5000K]

-	RANK 1	RANK 2										
1	AZR7S1	AZR2S1	AZR5S1	AZR8S1	A1R1S1	A1R8S1	A2R3S1	A2R2S1	A3R7S1	A3R2S1	A3R5S1	A3R8S1
2	AZR7S1	AZR2S2	AZR5S1	AZR8S2	A1R1S1	A1R8S2	A2R3S1	A2R2S2	A3R7S1	A3R2S2	A3R5S1	A3R8S2
3	AZR7S1	AZR2S3	AZR5S1	AZR8S3	A1R1S1	A1R8S3	A2R3S1	A2R2S3	A3R7S1	A3R2S3	A3R5S1	A3R8S3
4	AZR7S2	AZR2S1	AZR5S2	AZR8S1	A1R1S2	A1R8S1	A2R3S2	A2R2S1	A3R7S2	A3R2S1	A3R5S2	A3R8S1
5	AZR7S2	AZR2S2	AZR5S2	AZR8S2	A1R1S2	A1R8S2	A2R3S2	A2R2S2	A3R7S2	A3R2S2	A3R5S2	A3R8S2
6	AZR7S2	AZR2S3	AZR5S2	AZR8S3	A1R1S2	A1R8S3	A2R3S2	A2R2S3	A3R7S2	A3R2S3	A3R5S2	A3R8S3
7	AZR7S3	AZR2S1	AZR5S3	AZR8S1	A1R1S3	A1R8S1	A2R3S3	A2R2S1	A3R7S3	A3R2S1	A3R5S3	A3R8S1
8	AZR7S3	AZR2S2	AZR5S3	AZR8S2	A1R1S3	A1R8S2	A2R3S3	A2R2S2	A3R7S3	A3R2S2	A3R5S3	A3R8S2
9	AZR7S3	AZR2S3	AZR5S3	AZR8S3	A1R1S3	A1R8S3	A2R3S3	A2R2S3	A3R7S3	A3R2S3	A3R5S3	A3R8S3
10	AZR7S1	AZR6S1	AZR5S1	AZR4S1	A1R1S1	A1R4S1	A2R3S1	A2R6S1	A3R7S1	A3R6S1	A3R5S1	A3R4S1
11	AZR7S1	AZR6S2	AZR5S1	AZR4S2	A1R1S1	A1R4S2	A2R3S1	A2R6S2	A3R7S1	A3R6S2	A3R5S1	A3R4S2
12	AZR7S1	AZR6S3	AZR5S1	AZR4S3	A1R1S1	A1R4S3	A2R3S1	A2R6S3	A3R7S1	A3R6S3	A3R5S1	A3R4S3
13	AZR7S2	AZR6S1	AZR5S2	AZR4S1	A1R1S2	A1R4S1	A2R3S2	A2R6S1	A3R7S2	A3R6S1	A3R5S2	A3R4S1
14	AZR7S2	AZR6S2	AZR5S2	AZR4S2	A1R1S2	A1R4S2	A2R3S2	A2R6S2	A3R7S2	A3R6S2	A3R5S2	A3R4S2
15	AZR7S2	AZR6S3	AZR5S2	AZR4S3	A1R1S2	A1R4S3	A2R3S2	A2R6S3	A3R7S2	A3R6S3	A3R5S2	A3R4S3
16	AZR7S3	AZR6S1	AZR5S3	AZR4S1	A1R1S3	A1R4S1	A2R3S3	A2R6S1	A3R7S3	A3R6S1	A3R5S3	A3R4S1
17	AZR7S3	AZR6S2	AZR5S3	AZR4S2	A1R1S3	A1R4S2	A2R3S3	A2R6S2	A3R7S3	A3R6S2	A3R5S3	A3R4S2
18	AZR7S3	AZR6S3	AZR5S3	AZR4S3	A1R1S3	A1R4S3	A2R3S3	A2R6S3	A3R7S3	A3R6S3	A3R5S3	A3R4S3
19	AZR3S1	AZR2S1	A1R7S1	A1R2S1	A1R5S1	A1R8S1	A2R1S1	A2R8S1	A3R3S1	A3R2S1	A4R7S1	A4R2S1
20	AZR3S1	AZR2S2	A1R7S1	A1R2S2	A1R5S1	A1R8S2	A2R1S1	A2R8S2	A3R3S1	A3R2S2	A4R7S1	A4R2S2
21	AZR3S1	AZR2S3	A1R7S1	A1R2S3	A1R5S1	A1R8S3	A2R1S1	A2R8S3	A3R3S1	A3R2S3	A4R7S1	A4R2S3
22	AZR3S2	AZR2S1	A1R7S2	A1R2S1	A1R5S2	A1R8S1	A2R1S2	A2R8S1	A3R3S2	A3R2S1	A4R7S2	A4R2S1
23	AZR3S2	AZR2S2	A1R7S2	A1R2S2	A1R5S2	A1R8S2	A2R1S2	A2R8S2	A3R3S2	A3R2S2	A4R7S2	A4R2S2
24	AZR3S2	AZR2S3	A1R7S2	A1R2S3	A1R5S2	A1R8S3	A2R1S2	A2R8S3	A3R3S2	A3R2S3	A4R7S2	A4R2S3
25	AZR3S3	AZR2S1	A1R7S3	A1R2S1	A1R5S3	A1R8S1	A2R1S3	A2R8S1	A3R3S3	A3R2S1	A4R7S3	A4R2S1
26	AZR3S3	AZR2S2	A1R7S3	A1R2S2	A1R5S3	A1R8S2	A2R1S3	A2R8S2	A3R3S3	A3R2S2	A4R7S3	A4R2S2
27	AZR3S3	AZR2S3	A1R7S3	A1R2S3	A1R5S3	A1R8S3	A2R1S3	A2R8S3	A3R3S3	A3R2S3	A4R7S3	A4R2S3
28	AZR3S1	AZR6S1	A1R7S1	A1R6S1	A1R5S1	A1R4S1	A2R1S1	A2R4S1	A3R3S1	A3R6S1	A4R7S1	A4R6S1
29	AZR3S1	AZR6S2	A1R7S1	A1R6S2	A1R5S1	A1R4S2	A2R1S1	A2R4S2	A3R3S1	A3R6S2	A4R7S1	A4R6S2
30	AZR3S1	AZR6S3	A1R7S1	A1R6S3	A1R5S1	A1R4S3	A2R1S1	A2R4S3	A3R3S1	A3R6S3	A4R7S1	A4R6S3
31	AZR3S2	AZR6S1	A1R7S2	A1R6S1	A1R5S2	A1R4S1	A2R1S2	A2R4S1	A3R3S2	A3R6S1	A4R7S2	A4R6S1
32	AZR3S2	AZR6S2	A1R7S2	A1R6S2	A1R5S2	A1R4S2	A2R1S2	A2R4S2	A3R3S2	A3R6S2	A4R7S2	A4R6S2
33	AZR3S2	AZR6S3	A1R7S2	A1R6S3	A1R5S2	A1R4S3	A2R1S2	A2R4S3	A3R3S2	A3R6S3	A4R7S2	A4R6S3
34	AZR3S3	AZR6S1	A1R7S3	A1R6S1	A1R5S3	A1R4S1	A2R1S3	A2R4S1	A3R3S3	A3R6S1	A4R7S3	A4R6S1
35	AZR3S3	AZR6S2	A1R7S3	A1R6S2	A1R5S3	A1R4S2	A2R1S3	A2R4S2	A3R3S3	A3R6S2	A4R7S3	A4R6S2
36	AZR3S3	AZR6S3	A1R7S3	A1R6S3	A1R5S3	A1R4S3	A2R1S3	A2R4S3	A3R3S3	A3R6S3	A4R7S3	A4R6S3
37	AZR1S1	AZR8S1	A1R3S1	A1R2S1	A2R7S1	A2R2S1	A2R5S1	A2R8S1	A3R1S1	A3R8S1	A4R3S1	A4R2S1
38	AZR1S1	AZR8S2	A1R3S1	A1R2S2	A2R7S1	A2R2S2	A2R5S1	A2R8S2	A3R1S1	A3R8S2	A4R3S1	A4R2S2
39	AZR1S1	AZR8S3	A1R3S1	A1R2S3	A2R7S1	A2R2S3	A2R5S1	A2R8S3	A3R1S1	A3R8S3	A4R3S1	A4R2S3
40	AZR1S2	AZR8S1	A1R3S2	A1R2S1	A2R7S2	A2R2S1	A2R5S2	A2R8S1	A3R1S2	A3R8S1	A4R3S2	A4R2S1
41	AZR1S2	AZR8S2	A1R3S2	A1R2S2	A2R7S2	A2R2S2	A2R5S2	A2R8S2	A3R1S2	A3R8S2	A4R3S2	A4R2S2
42	AZR1S2	AZR8S3	A1R3S2	A1R2S3	A2R7S2	A2R2S3	A2R5S2	A2R8S3	A3R1S2	A3R8S3	A4R3S2	A4R2S3
43	AZR1S3	AZR8S1	A1R3S3	A1R2S1	A2R7S3	A2R2S1	A2R5S3	A2R8S1	A3R1S3	A3R8S1	A4R3S3	A4R2S1
44	AZR1S3	AZR8S2	A1R3S3	A1R2S2	A2R7S3	A2R2S2	A2R5S3	A2R8S2	A3R1S3	A3R8S2	A4R3S3	A4R2S2
45	AZR1S3	AZR8S3	A1R3S3	A1R2S3	A2R7S3	A2R2S3	A2R5S3	A2R8S3	A3R1S3	A3R8S3	A4R3S3	A4R2S3
46	AZR1S1	AZR4S1	A1R3S1	A1R6S1	A2R7S1	A2R6S1	A2R5S1	A2R4S1	A3R1S1	A3R4S1	A4R3S1	A4R6S1
47	AZR1S1	AZR4S2	A1R3S1	A1R6S2	A2R7S1	A2R6S2	A2R5S1	A2R4S2	A3R1S1	A3R4S2	A4R3S1	A4R6S2
48	AZR1S1	AZR4S3	A1R3S1	A1R6S3	A2R7S1	A2R6S3	A2R5S1	A2R4S3	A3R1S1	A3R4S3	A4R3S1	A4R6S3
49	AZR1S2	AZR4S1	A1R3S2	A1R6S1	A2R7S2	A2R6S1	A2R5S2	A2R4S1	A3R1S2	A3R4S1	A4R3S2	A4R6S1
50	AZR1S2	AZR4S2	A1R3S2	A1R6S2	A2R7S2	A2R6S2	A2R5S2	A2R4S2	A3R1S2	A3R4S2	A4R3S2	A4R6S2
51	AZR1S2	AZR4S3	A1R3S2	A1R6S3	A2R7S2	A2R6S3	A2R5S2	A2R4S3	A3R1S2	A3R4S3	A4R3S2	A4R6S3
52	AZR1S3	AZR4S1	A1R3S3	A1R6S1	A2R7S3	A2R6S1	A2R5S3	A2R4S1	A3R1S3	A3R4S1	A4R3S3	A4R6S1
53	AZR1S3	AZR4S2	A1R3S3	A1R6S2	A2R7S3	A2R6S2	A2R5S3	A2R4S2	A3R1S3	A3R4S2	A4R3S3	A4R6S2
54	AZR1S3	AZR4S3	A1R3S3	A1R6S3	A2R7S3	A2R6S3	A2R5S3	A2R4S3	A3R1S3	A3R4S3	A4R3S3	A4R6S3



**[Kitting combination - 5000K]**

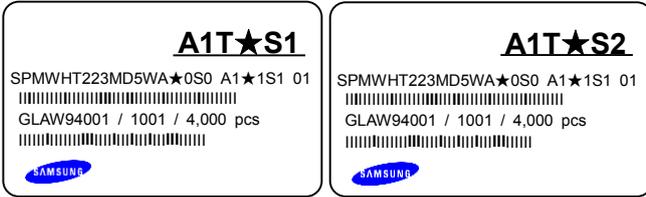
-	RANK 1	RANK 2										
1	A4R1S1	A4R8S1										
2	A4R1S1	A4R8S2										
3	A4R1S1	A4R8S3										
4	A4R1S2	A4R8S1										
5	A4R1S2	A4R8S2										
6	A4R1S2	A4R8S3										
7	A4R1S3	A4R8S1										
8	A4R1S3	A4R8S2										
9	A4R1S3	A4R8S3										
10	A4R1S1	A4R4S1										
11	A4R1S1	A4R4S2										
12	A4R1S1	A4R4S3										
13	A4R1S2	A4R4S1										
14	A4R1S2	A4R4S2										
15	A4R1S2	A4R4S3										
16	A4R1S3	A4R4S1										
17	A4R1S3	A4R4S2										
18	A4R1S3	A4R4S3										
19	A4R5S1	A4R8S1										
20	A4R5S1	A4R8S2										
21	A4R5S1	A4R8S3										
22	A4R5S2	A4R8S1										
23	A4R5S2	A4R8S2										
24	A4R5S2	A4R8S3										
25	A4R5S3	A4R8S1										
26	A4R5S3	A4R8S2										
27	A4R5S3	A4R8S3										
28	A4R5S1	A4R4S1										
29	A4R5S1	A4R4S2										
30	A4R5S1	A4R4S3										
31	A4R5S2	A4R4S1										
32	A4R5S2	A4R4S2										
33	A4R5S2	A4R4S3										
34	A4R5S3	A4R4S1										
35	A4R5S3	A4R4S2										
36	A4R5S3	A4R4S3										
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54												

## 5) Kitting bin Packing process

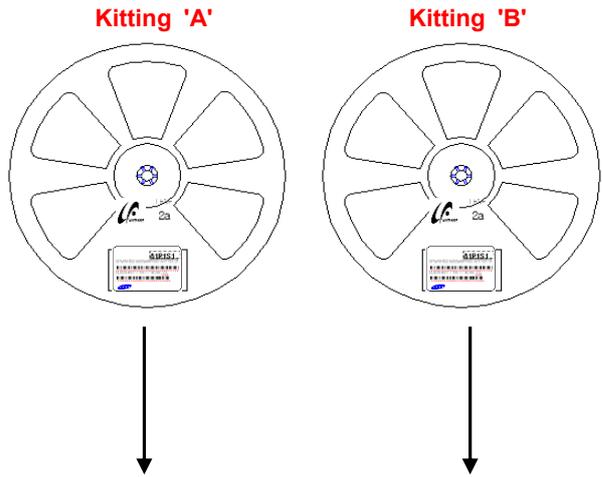
### Reel

Kitting 'A'

Kitting 'B'



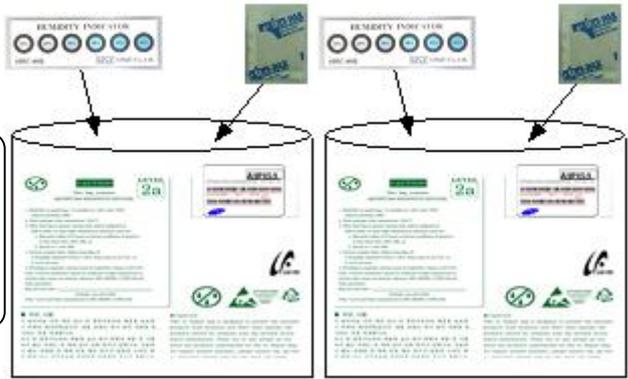
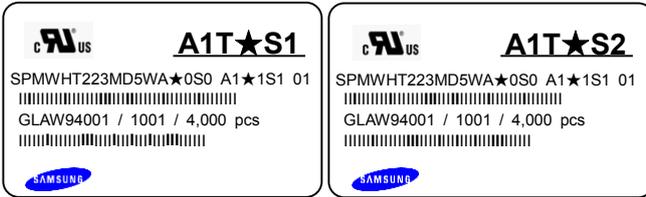
※ '★' means All kind of Chromaticity Coordinate Rank.



### Aluminum Vinyl Bag

Kitting 'A'

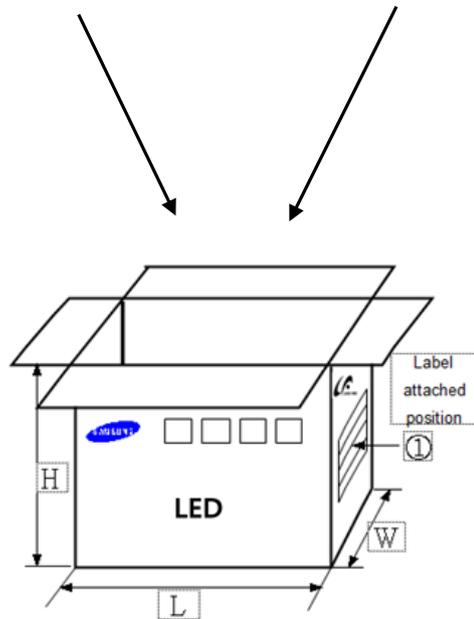
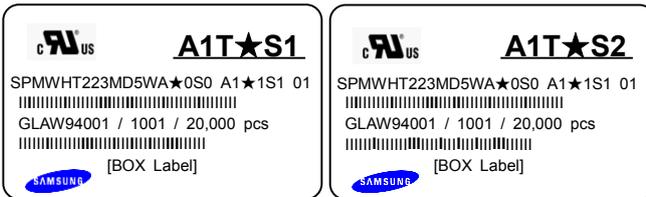
Kitting 'B'



### Outer Box

Kitting 'A'

Kitting 'B'



Material : Paper(SW3B(B))

TYPE	SIZE(mm)		
	L	W	H
7inch	245	220	182

## 12. Precaution for use

- 1) For over-current-proof function, customers are recommended to apply resistors to prevent sudden change of the current caused by slight shift of the voltage.

과전류 방지를 위해 전압의 미세한 이동에 의해 야기되는 전류의 순간 변화를 방지하기 위해 저항 등의 설치를 권장함.

- 2) This device should not be used in any type of fluid such as water, oil, organic solvent, etc. When washing is required, IPA is recommended to use.

제품은 물, 오일, 유기물과 같은 액체 타입에서의 사용은 제한되며, 세정이 필요할 시에는 IPA 사용을 권장함.

- 3) When the LEDs illuminate, operating current should be decided after considering the ambient maximum temperature.

LED의 발광 시, 동작 전류는 주변 최고온도를 고려하여 결정되어야 함.

- 4) LEDs must be stored in a clean environment.

If the LEDs are to be stored for 3 months or more after being shipped from Samsung Electronics, they should be packed by a sealed container with nitrogen gas injected.(Shelf life of sealed bags: 12 months, temp. ~40°C, ~90% RH)

LED의 보관은 청정한 환경에서 보존되어야 하며, 만약 삼성전자로부터 공급받는 후 3개월 또는 그 이상 보관이 필요하다면 질소 가스를 동봉한 보존용기에 보관되어야 함. (보존 bag의 수명 : 12 개월, 보존 온도 ~40°C, 습도 ~90% RH)

- 5) After storage bag is open, device subjected to soldering, solder reflow, or other high temperature processes must be:

보존 Bag이 개봉된 후에, 납땀이나 reflow등의 높은 온도에 노출되는 제품은 다음의 사항에 부합되어야 함.

- a. Mounted within 672 hours(28 days) at an assembly line with a condition of no more than 30°C/60%RH,

a. 제품은 30°C/60%RH보다 같거나 낮은 조립조건에서 672시간(28일)이내에 조립해야 함.

- b. Stored at <10%RH.

b. 10% 이하의 상대습도에서 보관되어야 함.

- 6) Repack unused Products with anti-moisture packing, fold to close any opening and then store in a dry place.

사용하지 않은 제품은 방습팩에 넣어 개봉 부위를 닫아서 다시 포장한 후, 건조한 장소에서 보관할 것을 권장함.

7) Devices require baking before mounting, if humidity card reading is  $>60\%$  at  $23\pm 5^{\circ}\text{C}$ .  
만약 습도표시카드의 수치가  $23\pm 5^{\circ}\text{C}$ 에서  $60\%$  이상이라면, 제품 실장 전 baking해야 함.

8) Devices must be baked for 10~24 hours at  $65\pm 5^{\circ}\text{C}$ , if baking is required.  
만약 baking이 필요하다면, 제품은  $65\pm 5^{\circ}\text{C}$ 에서 1시간 정도 baking 되어야 함.

9) The LEDs are sensitive to the static electricity and surge. It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs.

LED는 정전기 및 서지에 민감한 제품이므로, LED 제품을 다룰 시에는 정전기 방지장갑이나 손목밴드를 사용하기를 권장함.

If voltage exceeding the absolute maximum rating is applied to LEDs, it may cause damage or even destruction to LED devices.

만약 절대 허용치를 초과하는 전압이 LED에 가해지면, LED 소자는 파괴되거나 손상될 수 있음.

Damaged LEDs may show some unusual characteristics such as increase in leak current, lowered turn-on voltage, or abnormal lighting of LEDs at low current.

손상된 제품은 누설전류의 증가, Turn on 전압의 저하, 저 전류에서의 점등불량 등의 이상 거동을 보일 수 있음.

10) VOCs (volatile organic compounds) can be generated from adhesives, flux, hardener or organic additives used in luminaires (fixtures). Transparent LED silicone encapsulant is permeable to those chemicals and they may lead a discoloration of encapsulant when they expose to heat or light.

VOCs(휘발성 유기 화합물)는 등기구에 사용되는 접착제, Flux, 경화제, 유기물 첨가제에서 발생하여 LED 실리콘 봉지제를 투과하고, 빛 또는 열에 노출되었을 때 변색이 발생 할 수 있음.

This phenomenon can cause a significant loss of light emitted(output) from the luminaires(fixture).

이러한 현상은 등기구로부터 나오는 빛의 중대한 손실을 줄 수 있음.

In order to prevent these problems, we recommend you to know the physical properties of materials used in luminaires, They must be selected carefully.

이러한 문제 발생 방지를 위해서, 등기구에 사용되는 자재에 대한 물성을 알고 주의하여 선택 되어야함.

#### 11) Risk of Sulfurization (or Tarnishing)

The LED from Samsung Electronics uses a silver-plated lead frame and its surface color may change to black(or dark colored) when it is exposed to sulfur (S), chlorine (Cl) or other halogen compound

삼성전자의 LED는 Ag(은)을 도금한 리드프레임을 사용함. 이 리드프레임의 표면이 황(S), 염소(Cl), 또는 다른 할로겐 화합물들에 노출시 Ag(은)은 검정(또는 어두운색)으로 바뀔 수 있음.

Sulfurization of the lead frame may cause intensity degradation, change of chromaticity coordinates and, in extreme cases. open circuit. It requires caution.

리드 프레임의 황화(Sulfurization)는 광량 저하, 색좌표 변화 및 심한 경우 회로내의 LED 무등(Open) 불량을 일으킬 수도 있으니 주의가 필요함.

Due to possible sulfurization of lead frame, LED should not be used and stored together with oxidizing substances made of materials in a following list,

: Rubber, Plain paper, lead solder cream and so on.

리드 프레임 황화(Sulfurization)의 근원이 될 수 있으니 LED는 아래의 목록으로 만들어진 산화성 물질들과 함께 저장, 사용이 불가함 : 고무, 일반 종이, 납땜 크림 등

## Revision History

Date	NO	Revision History	Writer																																																																							
			Drawn	Approved																																																																						
2013.02.14	001	New version	T.J KIM	Y.T KIM																																																																						
2013.04.08	002	6500K, 5700K, 5000K CIE 8분할 적용	T.J KIM	Y.T KIM																																																																						
2013.07.27	003	<p style="text-align: center;">저 CCT IV spec 변경 (IM231A와 통일)</p> <table style="width: 100%; border-collapse: collapse;"> <tr> <th colspan="3" style="text-align: center; border: 1px solid black;">기존</th> <th style="font-size: 2em; vertical-align: middle;">➔</th> <th colspan="3" style="text-align: center; border: 1px solid black;">변경</th> </tr> <tr> <td style="border: 1px solid black;">bin</td><td style="border: 1px solid black;">Min</td><td style="border: 1px solid black;">Max</td> <td></td> <td style="border: 1px solid black;">bin</td><td style="border: 1px solid black;">Min</td><td style="border: 1px solid black;">Max</td> </tr> <tr> <td style="border: 1px solid black;">SY</td><td style="border: 1px solid black;">22.00</td><td style="border: 1px solid black;">24.50</td> <td></td> <td style="border: 1px solid black;">S1</td><td style="border: 1px solid black;">20.55</td><td style="border: 1px solid black;">23.56</td> </tr> <tr> <td style="border: 1px solid black;">SZ</td><td style="border: 1px solid black;">24.50</td><td style="border: 1px solid black;">27.00</td> <td></td> <td style="border: 1px solid black;">S2</td><td style="border: 1px solid black;">23.56</td><td style="border: 1px solid black;">27.09</td> </tr> <tr> <td style="border: 1px solid black;">S1</td><td style="border: 1px solid black;">27.00</td><td style="border: 1px solid black;">29.00</td> <td></td> <td style="border: 1px solid black;">S3</td><td style="border: 1px solid black;">27.09</td><td style="border: 1px solid black;">31.16</td> </tr> </table> <table style="width: 100%; border-collapse: collapse;"> <tr> <th colspan="3" style="text-align: center; border: 1px solid black;">기존</th> <th style="font-size: 2em; vertical-align: middle;">➔</th> <th colspan="3" style="text-align: center; border: 1px solid black;">변경</th> </tr> <tr> <td style="border: 1px solid black;">bin</td><td style="border: 1px solid black;">Min</td><td style="border: 1px solid black;">Max</td> <td></td> <td style="border: 1px solid black;">bin</td><td style="border: 1px solid black;">Min</td><td style="border: 1px solid black;">Max</td> </tr> <tr> <td style="border: 1px solid black;">SY</td><td style="border: 1px solid black;">19.00</td><td style="border: 1px solid black;">21.50</td> <td></td> <td style="border: 1px solid black;">S1</td><td style="border: 1px solid black;">19.81</td><td style="border: 1px solid black;">22.78</td> </tr> <tr> <td style="border: 1px solid black;">SZ</td><td style="border: 1px solid black;">21.50</td><td style="border: 1px solid black;">24.00</td> <td></td> <td style="border: 1px solid black;">S2</td><td style="border: 1px solid black;">22.78</td><td style="border: 1px solid black;">26.20</td> </tr> <tr> <td style="border: 1px solid black;">S1</td><td style="border: 1px solid black;">24.00</td><td style="border: 1px solid black;">26.00</td> <td></td> <td style="border: 1px solid black;">S3</td><td style="border: 1px solid black;">26.20</td><td style="border: 1px solid black;">30.13</td> </tr> </table>	기존			➔	변경			bin	Min	Max		bin	Min	Max	SY	22.00	24.50		S1	20.55	23.56	SZ	24.50	27.00		S2	23.56	27.09	S1	27.00	29.00		S3	27.09	31.16	기존			➔	변경			bin	Min	Max		bin	Min	Max	SY	19.00	21.50		S1	19.81	22.78	SZ	21.50	24.00		S2	22.78	26.20	S1	24.00	26.00		S3	26.20	30.13	T.J KIM	Y.T KIM
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2013.09.23	004	Addition of UL Authentication	J.H.YUN	Y.T KIM																																																																						
2013.10.28	005	Addition of Kitting combination Modification of CIE tolerance	T.J KIM	Y.T KIM																																																																						
2014.01.21	006	Addition of Quarter of color binning Remove UL mark of reel Addition of Kitting combination	T.J KIM	Y.T KIM																																																																						
2014.03.07	007	Change of Kitting bin Packing Process Addition of Half of color binning	T.J KIM	Y.T KIM																																																																						
2015.04.15	008	Change of Out box name : CHIP LED-> LED	G.E CHO	M.Y SON																																																																						