

# SQM48T/S20 DC-DC Converter Data Sheet

## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs



### Applications

- Telecommunications
- Data communications
- Wireless communications
- Servers

### Benefits

- High efficiency – no heat sink required
- Higher current capability at elevated temperatures than most competitors' 20-25 A quarter-bricks
- Extremely small footprint: 0.896" x 2.30" (2.06 in<sup>2</sup>), 38% smaller than conventional quarter-bricks

### Description

The **20A SemiQ™ Family** of DC-DC converters provide a high efficiency single output in a size that is only 60% of industry-standard quarter bricks, while preserving the same pinout and functionality.

The **20A SQM48 Series** converters of the **SemiQ™ Family** provide thermal performance in high temperature environments that exceeds most competitors' 20-25 A quarter-bricks. This is accomplished through the use of patent pending circuit, packaging and processing techniques to achieve ultra-high efficiency, excellent thermal management, and a very low body profile.

Low body profile and the preclusion of heat sinks minimize airflow shadowing, thus enhancing cooling for downstream devices. The use of 100% automation for assembly, coupled with advanced electronic circuits and thermal design, results in a product with extremely high reliability.

Operating from a 36-75 V input, the **20A SQM48 Series** converters provide any standard output voltage from 3.3 V down to 1.2 V. Outputs can be trimmed from -20% to +10% of the nominal output voltage ( $\pm 10\%$  for output voltage 1.2 V), thus providing outstanding design flexibility.

With a standard pinout and trim equations, the **SQM48 Series** converters are perfect drop-in replacements for existing 20 A quarter brick designs. Inclusion of this converter in new designs can result in significant board space and cost savings. The device is also available in a surface mount package.

In both cases the designer can expect reliability improvement over other available converters because of the **SQM48 Series'** optimized thermal efficiency.

### Features

- RoHS lead-free solder and lead-solder-exempted products are available
- Delivers up to 20 A
- Industry-standard, quarter-brick pinout
- Outputs available in 3.3, 2.5, 2.0, 1.8, 1.5, and 1.2 V
- Available in through-hole and SM packages
- Low profile: 0.28" (7.1 mm)
- Low weight: 0.66 oz (18.5 g)
- Onboard input differential LC-filter for the low input ripple current
- Start-up into pre-biased output
- No minimum load required
- Meets Basic insulation requirements of EN60950
- Withstands 100 V input transient for 100 ms
- Fixed-frequency operation
- Fully protected
- Remote output sense
- Positive or negative logic ON/OFF option
- Output voltage trim range: +10%/-20% with industry-standard trim equations (except 1.2 V output)
- High reliability: MTBF = 3.4 million hours, calculated per Telcordia TR-332, Method I Case 1
- UL60950 recognized in US and Canada and DEMKO certified per IEC/EN60950
- Designed to meet Class B conducted emissions per FCC and EN55022 when used with external filter
- All materials meet UL94, V-0 flammability rating

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### Electrical Specifications (common for all versions)

Conditions:  $T_A=25^\circ\text{C}$ , Airflow=300 LFM (1.5 m/s),  $V_{in}=48\text{ VDC}$ , All output voltages, unless otherwise specified.

Parameter	Notes	Min	Typ	Max	Units
<b>Absolute Maximum Ratings</b>					
Input Voltage	Continuous	0		80	VDC
Operating Ambient Temperature		-40		85	$^\circ\text{C}$
Storage Temperature		-55		125	$^\circ\text{C}$
<b>Input Characteristics</b>					
Operating Input Voltage Range		36	48	75	VDC
Input Under Voltage Lockout	Non-latching				
Turn-on Threshold		33	34	35	VDC
Turn-off Threshold		31	32	33	VDC
Input Voltage Transient	100 ms			100	VDC
<b>Isolation Characteristics</b>					
I/O Isolation		2000			VDC
Isolation Capacitance			160		pF
Isolation Resistance		10			M $\Omega$
<b>Feature Characteristics</b>					
Switching Frequency			415		kHz
Output Voltage Trim Range <sup>1</sup>	Industry-std. equations (3.3 - 1.5 V)	-20		+10	%
	Use trim equation on Page 4 (1.2 V)	-10		+10	%
Remote Sense Compensation <sup>1</sup>	Percent of $V_{OUT(NOM)}$			+10	%
Output Over-Voltage Protection	Non-latching (3.3 – 1.5 V)	117	122	127	%
	Non-latching (1.2 V)	124	132	140	%
Auto-Restart Period	Applies to all protection features		100		ms
Turn-On Time	See Figs. F, G and H		3		ms
<b>ON/OFF Control (Positive Logic)</b>					
Converter Off (logic low)		-20		0.8	VDC
Converter On (logic high)		2.4		20	VDC
<b>ON/OFF Control (Negative Logic)</b>					
Converter Off (logic high)		2.4		20	VDC
Converter On (logic low)		-20		0.8	VDC

#### Additional Notes:

- Vout can be increased up to 10% via the sense leads or up to 10% via the trim function. However, the total output voltage trim from all sources should not exceed 10% of  $V_{OUT(NOM)}$ , in order to insure specified operation of overvoltage protection circuitry.

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### Operations

#### Input and Output Impedance

These power converters have been designed to be stable with no external capacitors when used in low inductance input and output circuits.

In many applications, the inductance associated with the distribution from the power source to the input of the converter can affect the stability of the converter. The addition of a 33  $\mu$ F electrolytic capacitor with an ESR < 1 $\Omega$  across the input helps ensure stability of the converter. In many applications, the user has to use decoupling capacitance at the load. The power converter will exhibit stable operation with external load capacitance up to 20,000  $\mu$ F on 3.3 – 1.2 V outputs.

Additionally, see the EMC section of this data sheet for discussion of other external components which may be required for control of conducted emissions

#### ON/OFF (Pin 2)

The ON/OFF pin is used to turn the power converter on or off remotely via a system signal. There are two remote control options available, positive logic and negative logic and both are referenced to Vin(-). A typical connection is shown in Fig. A.

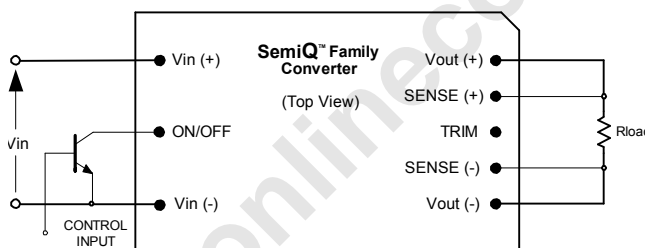


Fig. A: Circuit configuration for ON/OFF function.

The positive logic version turns on when the ON/OFF pin is at a logic high and turns off when at a logic low. The converter is on when the ON/OFF pin is left open. See table, page 2 for logic high/low definitions.

The negative logic version turns on when the pin is at a logic low and turns off when the pin is at a logic high. The ON/OFF pin can be hard wired directly to Vin(-) to enable automatic power up of the converter without the need of an external control signal.

The ON/OFF pin is internally pulled-up to 5V through a resistor. A properly debounced mechanical switch, open collector transistor, or FET can be used to drive the input of the ON/OFF pin. The device must be capable of sinking up to 0.2mA at a low level voltage

of  $\leq 0.8$  V. An external voltage source ( $\pm 20$  V maximum) may be connected directly to the ON/OFF input, in which case it must be capable of sourcing or sinking up to 1 mA depending on the signal polarity. See the Start-up Information section for system timing waveforms associated with use of the ON/OFF pin.

#### Remote Sense (Pins 5 and 7)

The remote sense feature of the converter compensates for voltage drops occurring between the output pins of the converter and the load. The SENSE(-) (Pin 5) and SENSE(+) (Pin 7) pins should be connected at the load or at the point where regulation is required (see Fig. B).

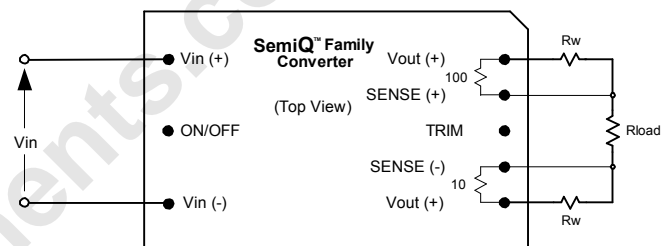


Fig. B: Remote sense circuit configuration.

If remote sensing is not utilized, the SENSE(-) pin must be connected to the Vout(-) pin (Pin 4), and the SENSE(+) pin must be connected to the Vout(+) pin (Pin 8) to ensure the converter will regulate at the specified output voltage. If these connections are not made, the converter will deliver an output voltage that is slightly higher than the specified data sheet value.

Because the sense leads carry minimal current, large traces on the end-user board are not required. However, sense traces should be run side by side and located close to a ground plane to minimize system noise and insure optimum performance.

When using the remote sense function, the converter's output overvoltage protection (OVP) senses the voltage across Vout(+) and Vout(-), and not across the sense lines, so the resistance (and resulting voltage drop) between the output pins of the converter and the load should be minimized to prevent unwanted triggering of the OVP.

When utilizing the remote sense feature, care must be taken not to exceed the maximum allowable output power capability of the converter, equal to the product of the nominal output voltage and the allowable output current for the given conditions.

When using remote sense, the output voltage at the converter can be increased by as much as 10% above the nominal rating in order to maintain the required voltage across the load. Therefore, the designer must, if necessary, decrease the maximum



current (originally obtained from the derating curves) by the same percentage to ensure the converter's actual output power remains at or below the maximum allowable output power.

### Output Voltage Adjust /TRIM (Pin 6)

The output voltage can be adjusted up 10% or down 20% for  $V_{out} \geq 1.5V$ , and  $\pm 10\%$  for  $V_{out} = 1.2V$  relative to the rated output voltage by the addition of an externally connected resistor. For output voltage 3.3V, trim up to 10% is guaranteed only at  $V_{in} \geq 40V$ , and it is marginal (8% to 10%) at  $V_{in} = 36V$ .

The TRIM pin should be left open if trimming is not being used. To minimize noise pickup, a  $0.1\mu F$  capacitor is connected internally between the TRIM and SENSE(-) pins.

To increase the output voltage, refer to Fig. C. A trim resistor,  $R_{T-INCR}$ , should be connected between the TRIM (Pin 6) and SENSE(+) (Pin 7), with a value of:

$$R_{T-INCR} = \frac{5.11(100 + \Delta)V_{O-NOM} - 626}{1.225\Delta} - 10.22 \quad [k\Omega],$$

for 3.3 – 1.5V.

$$R_{T-INCR} = \frac{84.6}{\Delta} - 7.2 \quad [k\Omega] \quad (1.2V)$$

where,

$R_{T-INCR}$  = Required value of trim-up resistor  $[k\Omega]$

$V_{O-NOM}$  = Nominal value of output voltage  $[V]$

$$\Delta = \left| \frac{(V_{O-REQ} - V_{O-NOM})}{V_{O-NOM}} \right| \times 100 \quad [\%]$$

$V_{O-REQ}$  = Desired (trimmed) output voltage  $[V]$ .

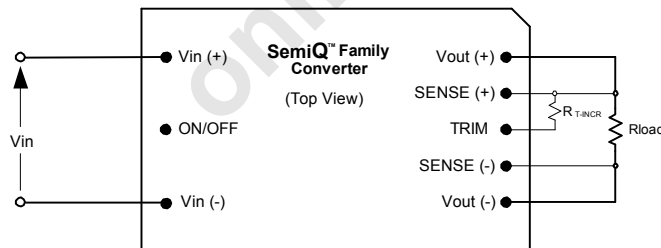


Fig. C: Configuration for increasing output voltage.

When trimming up, care must be taken not to exceed the converter's maximum allowable output power. See previous section for a complete discussion of this requirement.

To decrease the output voltage (Fig. D), a trim resistor,  $R_{T-DECR}$ , should be connected between the TRIM (Pin 6) and SENSE(-) (Pin 5), with a value of:

$$R_{T-DECR} = \frac{511}{\Delta} - 10.22 \quad [k\Omega] \quad (3.3 - 1.5V)$$

$$R_{T-DECR} = \frac{700}{\Delta} - 15 \quad [k\Omega] \quad (1.2V)$$

where,

$R_{T-DECR}$  = Required value of trim-down resistor  $[k\Omega]$

and  $\Delta$  is defined above.

Note: The above equations for calculation of trim resistor values match those typically used in conventional industry-standard quarter bricks and one-eighth bricks (except for 1.2V output).

Converters with output voltages 1.2V is available with alternative trim feature to provide the customers with the flexibility of second sourcing. For these converters, the last character in the part number is "T". The trim equations of "T" version of converters and more information can be found in Application Note 103.

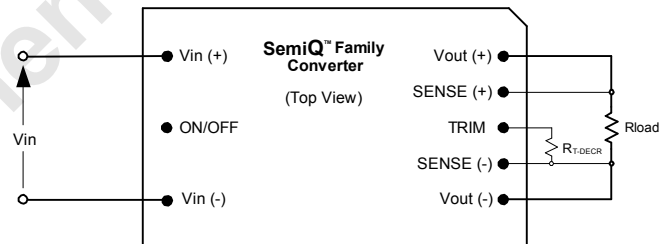


Fig. D: Configuration for decreasing output voltage.

Trimming/sensing beyond 110% of the rated output voltage is not an acceptable design practice, as this condition could cause unwanted triggering of the output over-voltage protection (OVP) circuit. The designer should ensure that the difference between the voltages across the converter's output pins and its sense pins does not exceed 10% of  $V_{OUT(NOM)}$ , or:

$$[V_{OUT(+)} - V_{OUT(-)}] - [V_{SENSE(+)} - V_{SENSE(-)}] \leq V_{O-NOM} \times 10\% \quad [V]$$

This equation is applicable for any condition of output sensing and/or output trim.

## Protection Features

### Input Undervoltage Lockout

Input undervoltage lockout is standard with this converter. The converter will shut down when the input voltage drops below a pre-determined voltage.

The input voltage must be typically 34 V for the converter to turn on. Once the converter has been turned on, it will shut off when the input voltage drops typically below 32 V. This feature is beneficial in preventing deep discharging of batteries used in telecom applications.





### Output Protections

All output circuit protection features are non-latching and operate in a “hiccup” mode. After an output protection event occurs, the converter will be turned off, and held off for approximately 100 ms after which, the protection circuit will reset and the converter will attempt to restart. If the fault is still present, the converter will repeat the above action. Once the fault is removed, the converter will start normally.

### Output Overcurrent Protection (OCP)

The converter is protected against overcurrent or short-circuit conditions. Upon sensing an overcurrent condition, the converter will switch to constant current operation and thereby begin to reduce output voltage. When the output voltage drops below 40-50% of the nominal value of output voltage, the converter will shut down (Fig. x.11).

Once the converter has shut down, it will attempt to restart nominally every 100 ms with a typical 1-2% duty cycle (Fig. x.12). The attempted restart will continue indefinitely until the overload or short circuit conditions are removed or the output voltage rises above 40-50% of its nominal value.

Once the output current is brought back into its specified range, the converter automatically exits the hiccup mode and continues normal operation.

### Output Over-Voltage Protection (OVP)

The converter will shut down if the output voltage across Vout(+) (Pin 8) and Vout(-) (Pin 4) exceeds the threshold of the OVP circuitry. The OVP circuitry contains its own reference, independent of the output voltage regulation loop. Once the converter has shut down, it will attempt to restart every 100 ms until the OVP condition is removed.

### Over-Temperature Protection (OTP)

The converter will shut down under an over-temperature condition to protect itself from overheating caused by operation outside the thermal derating curves, or operation in abnormal conditions such as system fan failure. After the converter has cooled to a safe operating temperature, it will automatically restart.

### Safety Requirements

The converters meet North American and International safety regulatory requirements per UL60950 and EN60950. Basic Insulation is provided between input and output.

To comply with safety agencies requirements, an input line fuse must be used external to the converter. The Table below provides the recommended fuse rating for use with this family of products.

Output Voltage	Fuse Rating
3.3V	4A
2.5 -1.8V	3A
1.5 - 1.2V	2A

All SQM converters are UL approved for maximum fuse rating of 15 A. To protect a group of converters with a single fuse, the rating can be increased from the recommended values above.

### Electromagnetic Compatibility (EMC)

EMC requirements must be met at the end-product system level, as no specific standards dedicated to EMC characteristics of board mounted component dc-dc converters exist. However, Power-One tests its converters to several system level standards, primary of which is the more stringent EN55022, *Information technology equipment - Radio disturbance characteristics - Limits and methods of measurement*.

Effective internal LC differential filter significantly reduces input reflected ripple current (Fig. x.9), and improves EMC.

With the addition of a simple external filter, all versions of the **SQM48 Series** converters pass the requirements of Class B conducted emissions per EN55022 and FCC requirements. Please contact Power-One Applications Engineering for details of this testing.

### Characterization

#### General Information

The converter has been characterized for many operational aspects, to include thermal derating (maximum load current as a function of ambient temperature and airflow) for vertical and horizontal mounting, efficiency, start-up and shutdown parameters, output ripple and noise, transient response to load step-change, overload and short circuit.

The figures are numbered as Fig. x.y, where x indicates the different output voltages, and y associates with specific plots (y = 1 for the vertical thermal derating, ...). For example, Fig. x.1 will refer to the vertical thermal derating for all the output voltages in general.

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## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs



The following pages contain specific plots or waveforms associated with the converter. Additional comments for specific data are provided below.

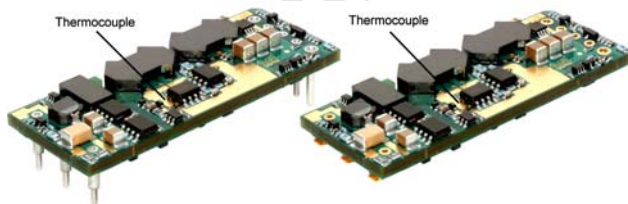
### Test Conditions

All data presented were taken with the converter soldered to a test board, specifically a 0.060" thick printed wiring board (PWB) with four layers. The top and bottom layers were not metalized. The two inner layers, comprising two-ounce copper, were used to provide traces for connectivity to the converter.

The lack of metalization on the outer layers as well as the limited thermal connection ensured that heat transfer from the converter to the PWB was minimized. This provides a worst-case but consistent scenario for thermal derating purposes.

All measurements requiring airflow were made in the vertical and horizontal wind tunnel using Infrared (IR) thermography and thermocouples for thermometry.

Ensuring components on the converter do not exceed their ratings is important to maintaining high reliability. If one anticipates operating the converter at or close to the maximum loads specified in the derating curves, it is prudent to check actual operating temperatures in the application. Thermographic imaging is preferable; if this capability is not available, then thermocouples may be used. It is recommended the use of AWG #40 gauge thermocouples to ensure measurement accuracy. Careful routing of the thermocouple leads will further minimize measurement error. Refer to Fig. E for optimum measuring thermocouple locations.



**Fig. E:** Locations of the thermocouples for thermal testing.

### Thermal Derating

Load current vs. ambient temperature and airflow rates are given in Fig. x.1 for through-hole version. Ambient temperature was varied between 25°C and 85°C, with airflow rates from 30 to 500 LFM (0.15 to 2.5 m/s), and vertical converter mounting.

For each set of conditions, the maximum load current was defined as the lowest of:

(i) The output current at which any FET junction temperature does not exceed a maximum specified temperature (120 °C) as indicated by the thermographic image, or

(ii) The nominal rating of the converter (20 A on 3.3 – 1.2 V).

During normal operation, derating curves with maximum FET temperature less or equal to 120 °C should not be exceeded. Temperature on the PCB at thermocouple location shown in Fig. E should not exceed 120 °C in order to operate inside the derating curves.

### Efficiency

Efficiency vs. load current plot is shown in Fig. x.2 for ambient temperature of 25 °C, airflow rate of 300 LFM (1.5 m/s), vertical converter mounting, and input voltages of 36 V, 48 V and 72 V.

### Start-up

Output voltage waveforms, during the turn-on transient using the ON/OFF pin for full rated load currents (resistive load) are shown without and with external load capacitance in Fig. x.3 and Fig. x.4, respectively.

### Ripple and Noise

Fig. x.7 shows the output voltage ripple waveform, measured at full rated load current with a 10 µF tantalum and 1 µF ceramic capacitor across the output. Note that all output voltage waveforms are measured across a 1 µF ceramic capacitor.

The input reflected ripple current waveforms are obtained using the test setup shown in Fig x.8. The corresponding waveforms are shown in Fig. x.9 and Fig. x.10.

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### Start-up Information (using negative ON/OFF)

#### Scenario #1: Initial Start-up From Bulk Supply

ON/OFF function enabled, converter started via application of  $V_{IN}$ . See Figure F.

Time	Comments
$t_0$	ON/OFF pin is ON; system front end power is toggled on, $V_{IN}$ to converter begins to rise.
$t_1$	$V_{IN}$ crosses Under-Voltage Lockout protection circuit threshold; converter enabled.
$t_2$	Converter begins to respond to turn-on command (converter turn-on delay).
$t_3$	Converter $V_{OUT}$ reaches 100% of nominal value.

For this example, the total converter start-up time ( $t_3 - t_1$ ) is typically 3 ms.

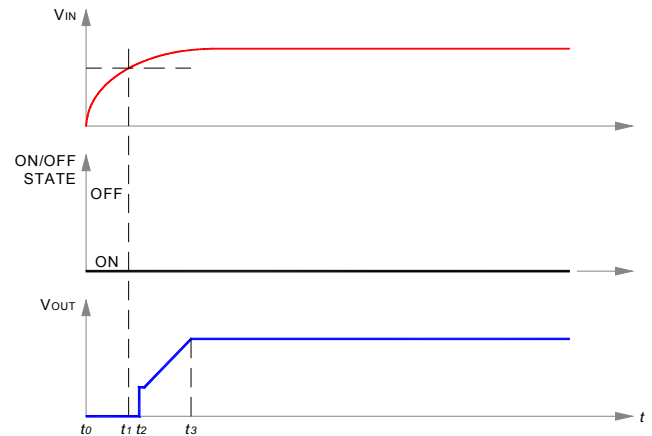


Fig. F: Start-up scenario #1.

#### Scenario #2: Initial Start-up Using ON/OFF Pin

With  $V_{IN}$  previously powered, converter started via ON/OFF pin. See Figure G.

Time	Comments
$t_0$	$V_{INPUT}$ at nominal value.
$t_1$	Arbitrary time when ON/OFF pin is enabled (converter enabled).
$t_2$	End of converter turn-on delay.
$t_3$	Converter $V_{OUT}$ reaches 100% of nominal value.

For this example, the total converter start-up time ( $t_3 - t_1$ ) is typically 3 ms.

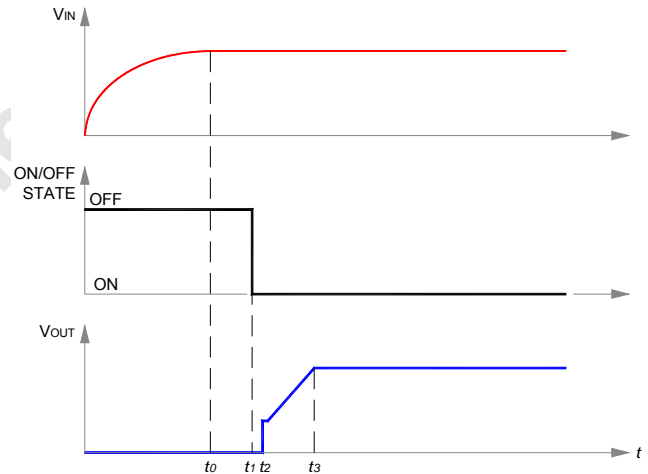


Fig. G: Start-up scenario #2.

#### Scenario #3: Turn-off and Restart Using ON/OFF Pin

With  $V_{IN}$  previously powered, converter is disabled and then enabled via ON/OFF pin. See Figure H.

Time	Comments
$t_0$	$V_{IN}$ and $V_{OUT}$ are at nominal values; ON/OFF pin ON.
$t_1$	ON/OFF pin arbitrarily disabled; converter output falls to zero; turn-on inhibit delay period (100 ms typical) is initiated, and ON/OFF pin action is internally inhibited.
$t_2$	ON/OFF pin is externally re-enabled. If $(t_2 - t_1) \leq 100$ ms, external action of ON/OFF pin is locked out by start-up inhibit timer. If $(t_2 - t_1) > 100$ ms, ON/OFF pin action is internally enabled.
$t_3$	Turn-on Inhibit delay period ends. If ON/OFF pin is ON, converter begins turn-on; if off, converter awaits ON/OFF pin ON signal; see Figure G.
$t_4$	End of converter turn-on delay.
$t_5$	Converter $V_{OUT}$ reaches 100% of nominal value.

For the condition,  $(t_2 - t_1) \leq 100$  ms, the total converter start-up time ( $t_5 - t_2$ ) is typically 103 ms. For  $(t_2 - t_1) > 100$  ms, start-up will be typically 3 ms after release of ON/OFF pin.

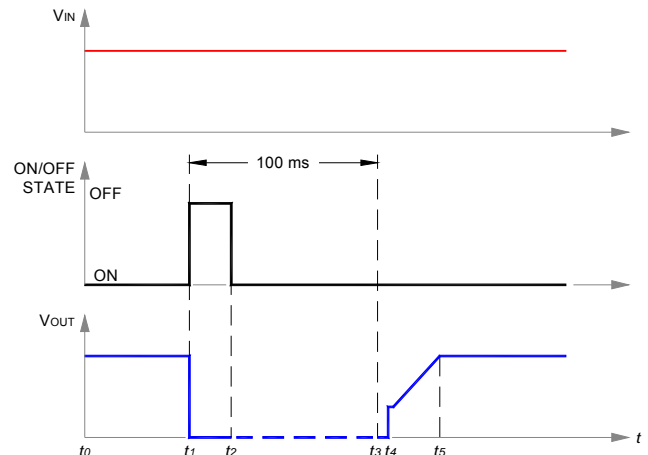


Fig. H: Start-up scenario #3.

# SQM48T/S20 DC-DC Converter Data Sheet

## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs

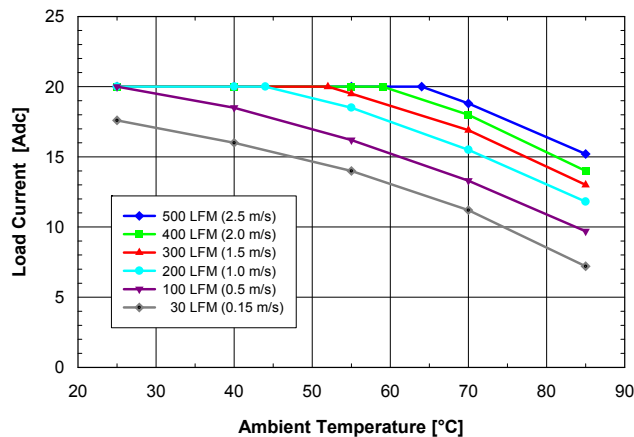


### Electrical Specifications: SQM48T/S20033 (3.3 Volt Out)

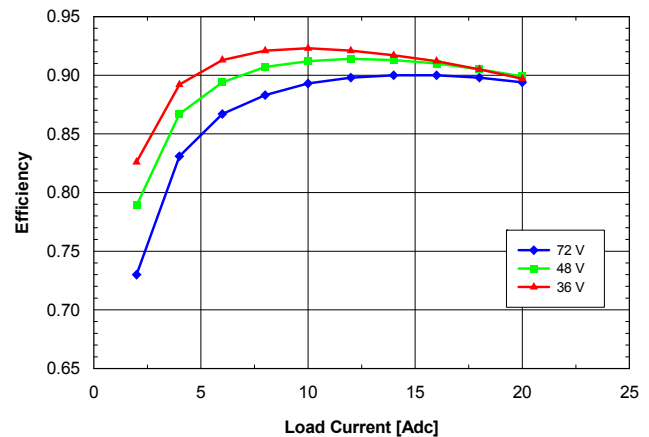
Conditions:  $T_A=25^\circ\text{C}$ , Airflow=300 LFM (1.5 m/s),  $V_{in}=48\text{ VDC}$ ,  $V_{out}=3.3\text{ VDC}$ , unless otherwise specified.

Parameter	Notes	Min	Typ	Max	Units
<b>Input Characteristics</b>					
Maximum Input Current	20 ADC, 3.3 VDC Out @ 36 VDC In			2.1	ADC
Input Stand-by Current	$V_{in} = 48\text{ V}$ , converter disabled		3		mADC
Input No Load Current (0 load on the output)	$V_{in} = 48\text{ V}$ , converter enabled		45		mADC
Input Reflected-Ripple Current	25MHz bandwidth		6		$\text{mA}_{\text{PK-PK}}$
Input Voltage Ripple Rejection	120Hz		TBD		dB
<b>Output Characteristics</b>					
Output Voltage Set Point (no load)		3.267	3.300	3.333	VDC
Output Regulation					
Over Line			$\pm 2$	$\pm 5$	mV
Over Load			$\pm 2$	$\pm 5$	mV
Output Voltage Range	Over line, load and temperature <sup>2</sup>	3.250		3.350	VDC
Output Ripple and Noise - 25MHz bandwidth	Full load + 10 $\mu\text{F}$ tantalum + 1 $\mu\text{F}$ ceramic		30	50	$\text{mV}_{\text{PK-PK}}$
External Load Capacitance	Plus full load (resistive)			20,000	$\mu\text{F}$
Output Current Range		0		20	ADC
Current Limit Inception	Non-latching	21	24	27.5	ADC
Peak Short-Circuit Current	Non-latching. Short=10m $\Omega$ .		30	44	A
RMS Short-Circuit Current	Non-latching			6.7	Arms
<b>Dynamic Response</b>					
Load Change 25% of $I_{out\text{ Max}}$ , $di/dt = 0.1\text{ A}/\mu\text{s}$	$C_o = 1\text{ }\mu\text{F}$ ceramic		80		mV
$di/dt = 5\text{ A}/\mu\text{s}$	$C_o = 450\text{ }\mu\text{F}$ POS + 1 $\mu\text{F}$ ceramic		140		mV
Settling Time to 1%			100		$\mu\text{s}$
<b>Efficiency</b>					
100% Load			90		%
50% Load			91		%

Additional Notes: 2.  $-40^\circ\text{C}$  to  $85^\circ\text{C}$ .



**Fig. 3.3V.1:** Available load current vs. ambient air temperature and airflow rates for **SQM48T20033** converter with D height pins mounted vertically with air flowing from pin 3 to pin 1, MOSFET temperature  $\leq 120^\circ\text{C}$ ,  $V_{in} = 48$

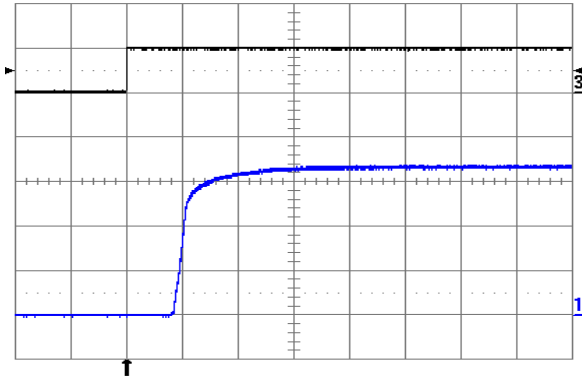


**Fig. 3.3V.2:** Efficiency vs. load current and input voltage for **SQM48T/S20033** converter mounted vertically with air flowing from pin 3 to pin 1 at a rate of 300 LFM (1.5 m/s) and  $T_A = 25^\circ\text{C}$ .

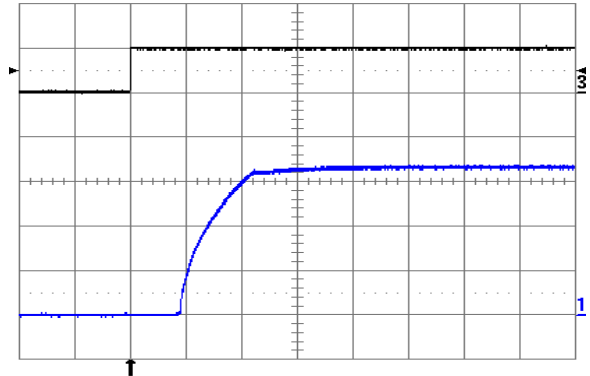




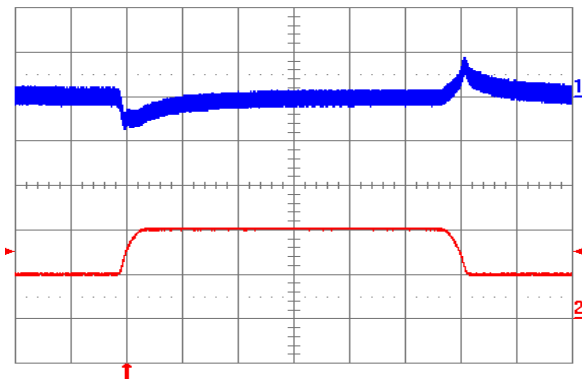
**SQM48T/S20033 (3.3 Volt Out)**



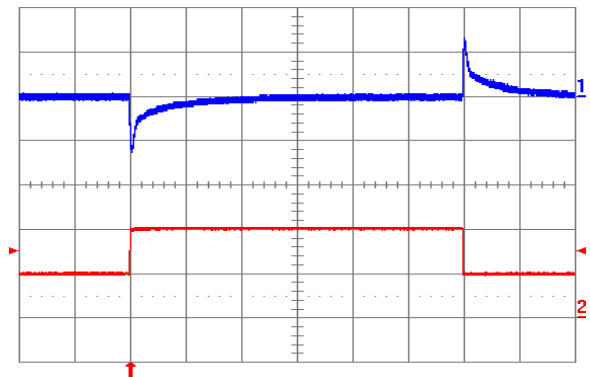
**Fig. 3.3V.3:** Turn-on transient at full rated load current (resistive) with no output capacitor at  $V_{in} = 48$  V, triggered via ON/OFF pin. Top trace: ON/OFF signal (5V/div.). Bottom trace: output voltage (1 V/div.). Time scale: 2 ms/div.



**Fig. 3.3V.4:** Turn-on transient at full rated load current (resistive) plus 10,000  $\mu$ F at  $V_{in} = 48$  V, triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (1 V/div.). Time scale: 2 ms/div.



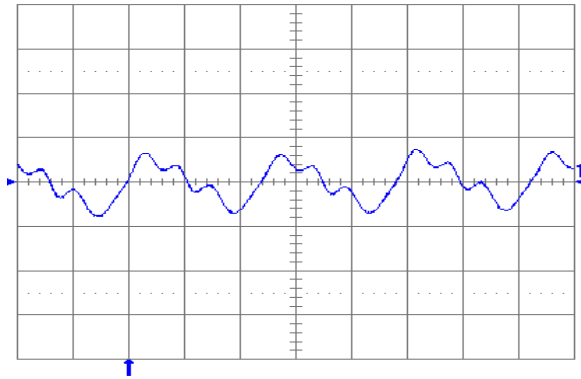
**Fig. 3.3V.5:** Output voltage response to load current step-change (5A – 10A – 5A ) at  $V_{in} = 48$  V. Top trace: output voltage (100 mV/div.). Bottom trace: load current (5A/div.). Current slew rate: 0.1 A/ $\mu$ s.  $C_o = 1$   $\mu$ F ceramic. Time scale: 0.2 ms/div.



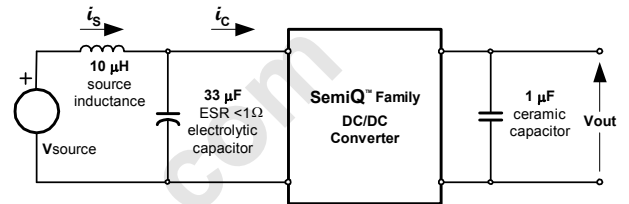
**Fig. 3.3V.6:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48$  V. Top trace: output voltage (100 mV/div.). Bottom trace: load current (5 A/div.). Current slew rate: 5 A/ $\mu$ s.  $C_o = 450$   $\mu$ F tantalum + 1  $\mu$ F ceramic. Time scale: 0.2 ms/div.



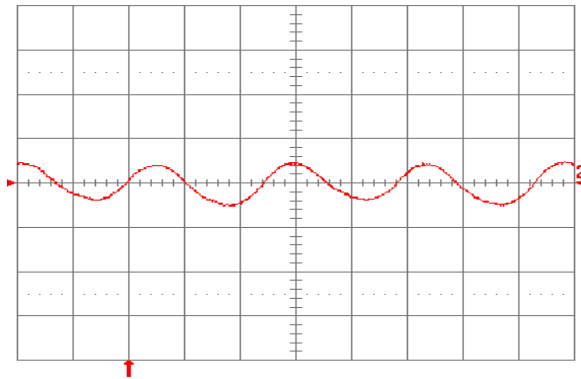
### SQM48T/S20033 (3.3 Volt Out)



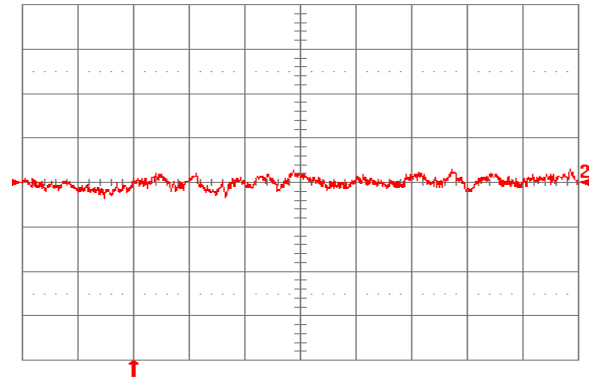
**Fig. 3.3V.7:** Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with  $C_o = 10\ \mu\text{F}$  tantalum + 1  $\mu\text{F}$  ceramic and  $V_{in} = 48\ \text{V}$ . Time scale: 1  $\mu\text{s}/\text{div}$ .



**Fig. 3.3V.8:** Test setup for measuring input reflected ripple currents,  $i_c$  and  $i_s$ .



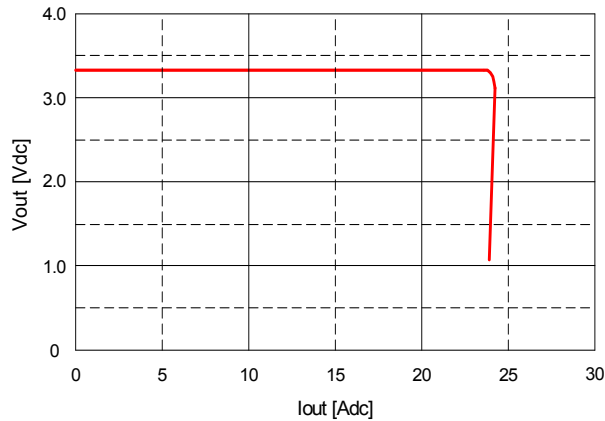
**Fig. 3.3V.9:** Input reflected ripple current,  $i_c$  (100 mA/div.), measured at input terminals at full rated load current and  $V_{in} = 48\ \text{V}$ . Refer to Fig. 3.3V.8 for test setup. Time scale: 1  $\mu\text{s}/\text{div}$ .



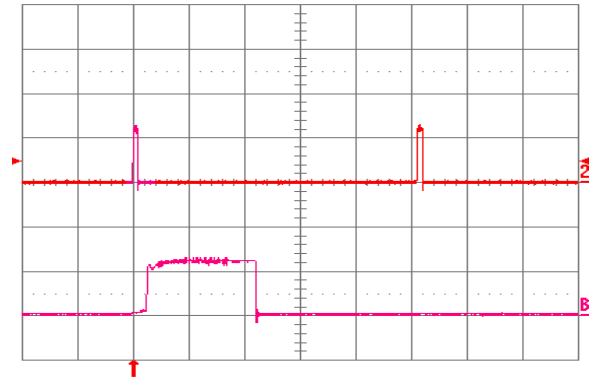
**Fig. 3.3V.10:** Input reflected ripple current,  $i_s$  (10 mA/div.), measured through 10  $\mu\text{H}$  at the source at full rated load current and  $V_{in} = 48\ \text{V}$ . Refer to Fig. 3.3V.8 for test setup. Time scale: 1  $\mu\text{s}/\text{div}$ .



**SQM48T/S20033 (3.3 Volt Out)**



**Fig. 3.3V.11:** Output voltage vs. load current showing current limit point and converter shutdown point. Input voltage has almost no effect on current limit characteristic.



**Fig. 3.3V.12:** Load current (top trace, 20 A/div., 20ms/div.) into a 10 mΩ short circuit during restart, at Vin = 48 V. Bottom trace (20 A/div., 1ms/div.) is an expansion of the on-time portion of the top trace.

# SQM48T/S20 DC-DC Converter Data Sheet

## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs

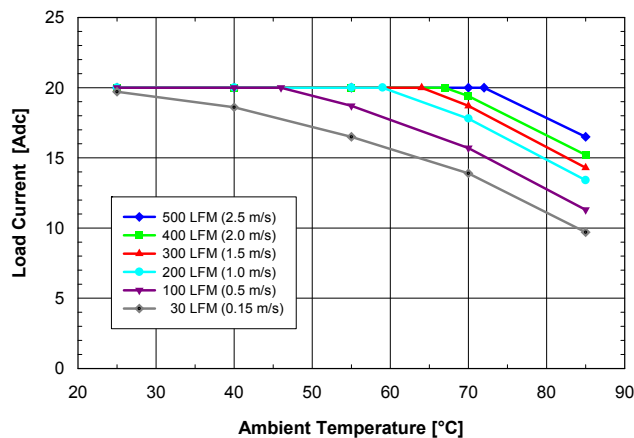


### Electrical Specifications: SQM48T/S20025 (2.5 Volt Out)

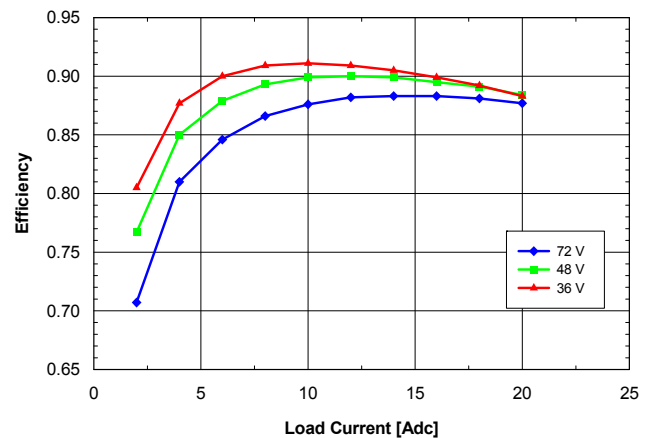
Conditions:  $T_A=25^\circ\text{C}$ , Airflow=300 LFM (1.5 m/s),  $V_{in}=48\text{ VDC}$ ,  $V_{out}=2.5\text{ VDC}$ , unless otherwise specified.

Parameter	Notes	Min	Typ	Max	Units
<b>Input Characteristics</b>					
Maximum Input Current	20 ADC, 2.5 VDC Out @ 36 VDC In			1.6	ADC
Input Stand-by Current	$V_{in} = 48\text{ V}$ , converter disabled		3		mADC
Input No Load Current (0 load on the output)	$V_{in} = 48\text{ V}$ , converter enabled		35		mADC
Input Reflected-Ripple Current	25MHz bandwidth		6		mA <sub>PK-PK</sub>
Input Voltage Ripple Rejection	120Hz		TBD		dB
<b>Output Characteristics</b>					
Output Voltage Set Point (no load)	-40 °C to 85 °C	2.475	2.500	2.525	VDC
Output Regulation					
Over Line			±2	±5	mV
Over Load			±2	±5	mV
Output Voltage Range	Over line, load and temperature <sup>2</sup>	2.462		2.538	VDC
Output Ripple and Noise - 25MHz bandwidth	Full load + 10 $\mu\text{F}$ tantalum + 1 $\mu\text{F}$ ceramic		35	50	mV <sub>PK-PK</sub>
External Load Capacitance	Plus full load (resistive)			20,000	$\mu\text{F}$
Output Current Range		0		20	ADC
Current Limit Inception	Non-latching	21	24	27.5	ADC
Peak Short-Circuit Current	Non-latching. Short=10m $\Omega$ .		30	44	A
RMS Short-Circuit Current	Non-latching			6.7	Arms
<b>Dynamic Response</b>					
Load Change 25% of Iout Max, di/dt = 0.1A/ $\mu\text{S}$	Co = 1 $\mu\text{F}$ ceramic		80		mV
di/dt = 5A/ $\mu\text{S}$	Co = 450 $\mu\text{F}$ POS + 1 $\mu\text{F}$ ceramic		140		mV
Settling Time to 1%			100		$\mu\text{s}$
<b>Efficiency</b>					
100% Load			88.5		%
50% Load			90		%

Additional Notes: 2. -40 °C to 85 °C.



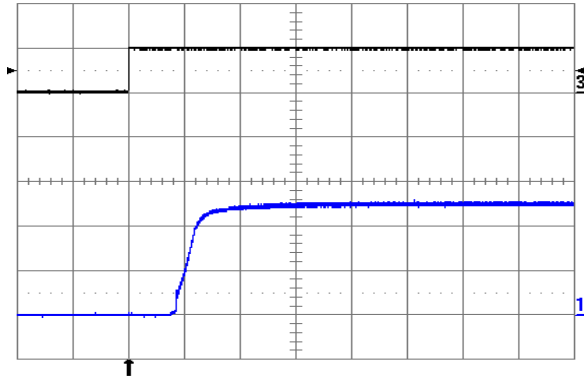
**Fig. 2.5V.1:** Available load current vs. ambient air temperature and airflow rates for **SQM48T20025** converter with D height pins mounted vertically with air flowing from pin 3 to pin 1, MOSFET temperature  $\leq 120^\circ\text{C}$ ,  $V_{in} = 48\text{ V}$ .



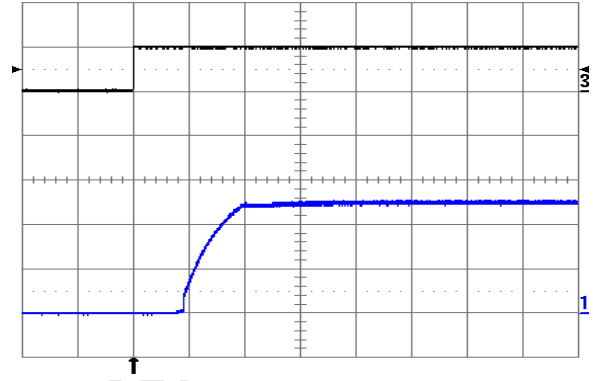
**Fig. 2.5V.2:** Efficiency vs. load current and input voltage for **SQM48T/S20025** converter mounted vertically with air flowing from pin 3 to pin 1 at a rate of 300 LFM (1.5 m/s) and  $T_A = 25^\circ\text{C}$ .



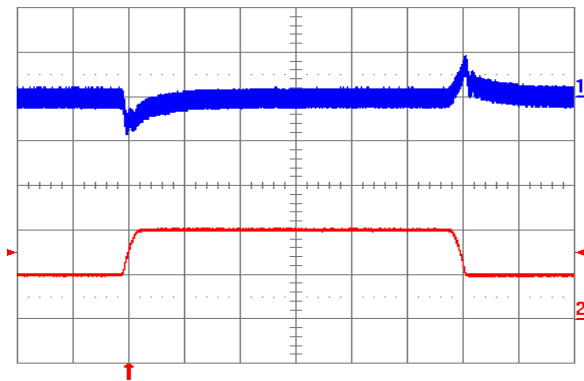
**SQM48T/S20025 (2.5 Volt Out)**



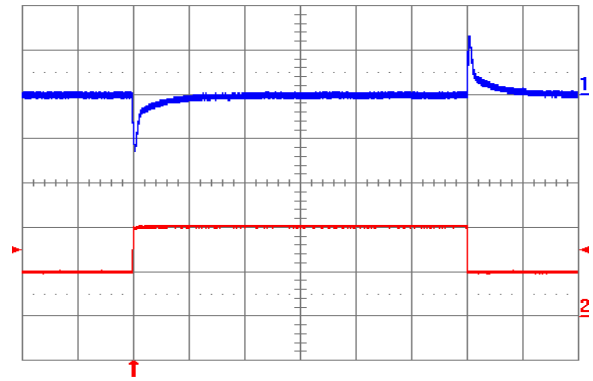
**Fig. 2.5V.3:** Turn-on transient at full rated load current (resistive) with no output capacitor at  $V_{in} = 48$  V, triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (1 V/div.). Time scale: 2 ms/div.



**Fig. 2.5V.4:** Turn-on transient at full rated load current (resistive) plus 10,000  $\mu$ F at  $V_{in} = 48$  V, triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (1 V/div.). Time scale: 2 ms/div.



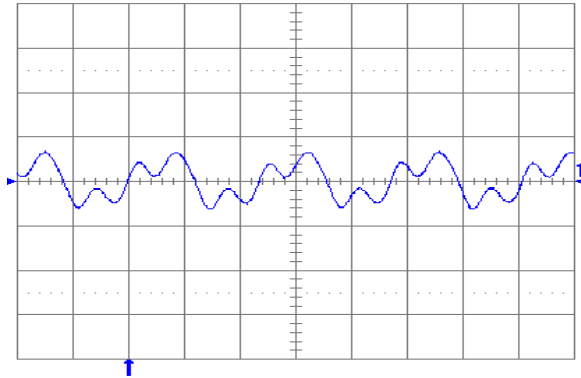
**Fig. 2.5V.5:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48$  V. Top trace: output voltage (100 mV/div.). Bottom trace: load current (5 A/div.). Current slew rate: 0.1 A/ $\mu$ s.  $C_o = 1$   $\mu$ F ceramic. Time scale: 0.2 ms/div.



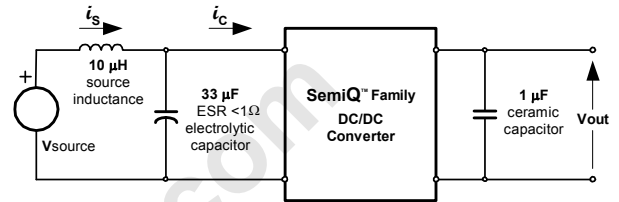
**Fig. 2.5V.6:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48$  V. Top trace: output voltage (100 mV/div.). Bottom trace: load current (5 A/div.). Current slew rate: 5 A/ $\mu$ s.  $C_o = 450$   $\mu$ F tantalum + 1  $\mu$ F ceramic. Time scale: 0.2 ms/div.



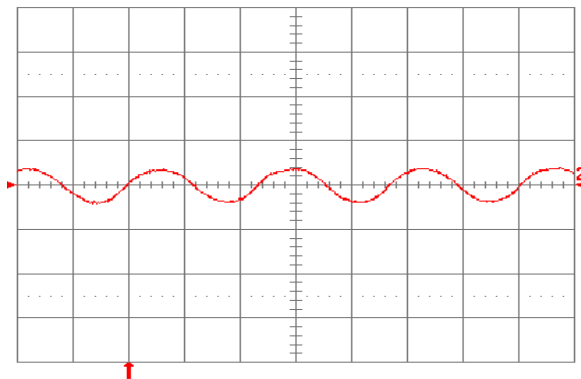
**SQM48T/S20025 (2.5 Volt Out)**



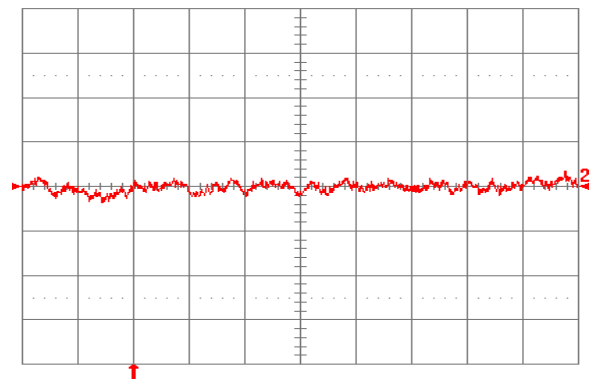
**Fig. 2.5V.7:** Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with  $C_o = 10 \mu\text{F}$  tantalum +  $1\mu\text{F}$  ceramic and  $V_{in} = 48 \text{ V}$ . Time scale:  $1 \mu\text{s}/\text{div}$ .



**Fig. 2.5V.8:** Test setup for measuring input reflected ripple currents,  $i_c$  and  $i_s$ .



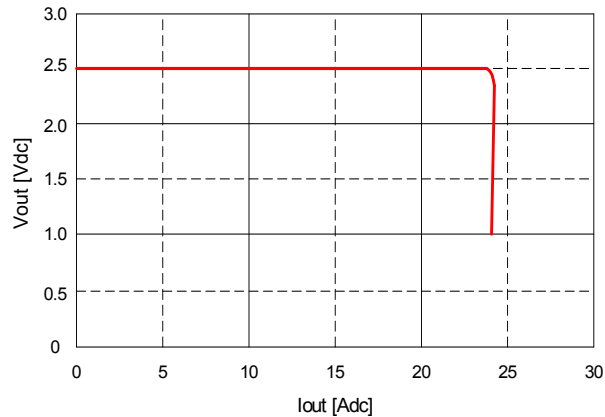
**Fig. 2.5V.9:** Input reflected ripple current,  $i_c$  (100 mA/div.), measured at input terminals at full rated load current and  $V_{in} = 48 \text{ V}$ . Refer to Fig. 2.5V.8 for test setup. Time scale:  $1 \mu\text{s}/\text{div}$ .



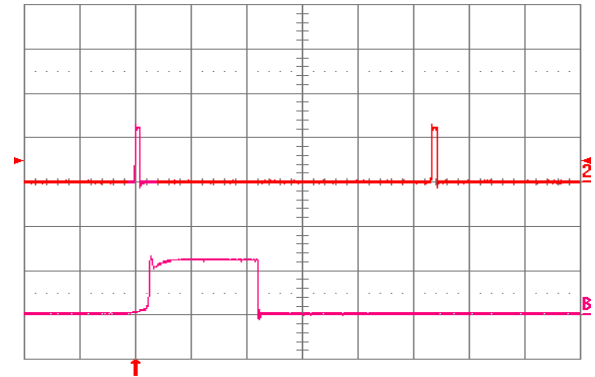
**Fig. 2.5V.10:** Input reflected ripple current,  $i_s$  (10 mA/div.), measured through  $10 \mu\text{H}$  at the source at full rated load current and  $V_{in} = 48 \text{ V}$ . Refer to Fig. 2.5V.8 for test setup. Time scale:  $1 \mu\text{s}/\text{div}$ .



**SQM48T/S20025 (2.5 Volt Out)**



**Fig. 2.5V.11:** Output voltage vs. load current showing current limit point and converter shutdown point. Input voltage has almost no effect on current limit characteristic.



**Fig. 2.5V.12:** Load current (top trace, 20 A/div., 20 ms/div.) into a 10 mΩ short circuit during restart, at Vin = 48 V. Bottom trace (20 A/div., 1 ms/div.) is an expansion of the on-time portion of the top trace.

# SQM48T/S20 DC-DC Converter Data Sheet

## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs

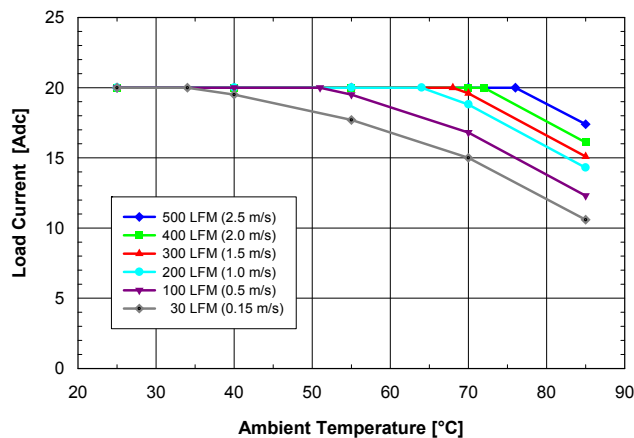


### Electrical Specifications: SQM48T/S20020 (2.0 Volt Out)

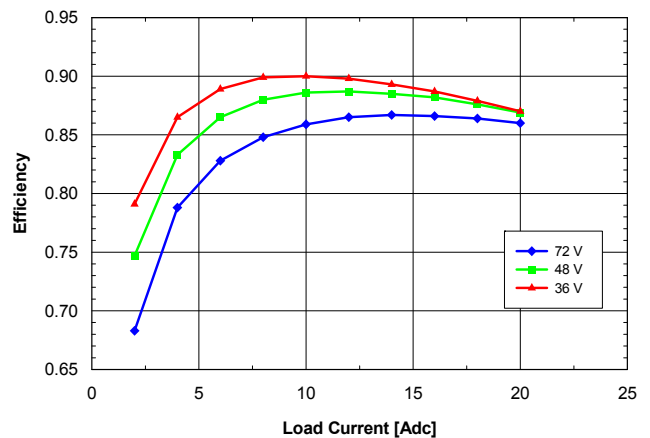
Conditions:  $T_A=25^\circ\text{C}$ , Airflow=300 LFM (1.5 m/s),  $V_{in}=48\text{ VDC}$ ,  $V_{out}=2.0\text{ VDC}$ , unless otherwise specified.

Parameter	Notes	Min	Typ	Max	Units
<b>Input Characteristics</b>					
Maximum Input Current	20 ADC, 2.0 VDC Out @ 36 VDC In			1.3	ADC
Input Stand-by Current	$V_{in} = 48\text{ V}$ , converter disabled		3		mADC
Input No Load Current (0 load on the output)	$V_{in} = 48\text{ V}$ , converter enabled		32		mADC
Input Reflected-Ripple Current	25 MHz bandwidth		6		mA <sub>PK-PK</sub>
Input Voltage Ripple Rejection	120 Hz		TBD		dB
<b>Output Characteristics</b>					
Output Voltage Set Point (no load)	-40 °C to 85 °C	1.98	2.000	2.02	VDC
Output Regulation					
Over Line			±2	±5	mV
Over Load			±2	±5	mV
Output Voltage Range	Over line, load and temperature <sup>2</sup>	1.970		2.030	VDC
Output Ripple and Noise - 25 MHz bandwidth	Full load + 10 $\mu\text{F}$ tantalum + 1 $\mu\text{F}$ ceramic		30	50	mV <sub>PK-PK</sub>
External Load Capacitance	Plus full load (resistive)			20,000	$\mu\text{F}$
Output Current Range		0		20	ADC
Current Limit Inception	Non-latching	21	24	27.5	ADC
Peak Short-Circuit Current	Non-latching. Short=10 m $\Omega$ .		30	44	A
RMS Short-Circuit Current	Non-latching			6.7	Arms
<b>Dynamic Response</b>					
Load Change 25% of Iout Max, di/dt = 0.1A/ $\mu\text{S}$	Co = 1 $\mu\text{F}$ ceramic		80		mV
	di/dt = 5 A/ $\mu\text{S}$		140		mV
Settling Time to 1%	Co = 450 $\mu\text{F}$ POS + 1 $\mu\text{F}$ ceramic		100		$\mu\text{s}$
<b>Efficiency</b>					
100% Load			87		%
50% Load			88.5		%

Additional Notes: 2. -40 °C to 85 °C.



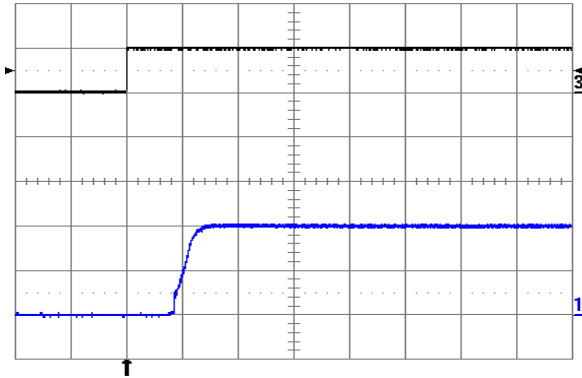
**Fig. 2.0V.1:** Available load current vs. ambient air temperature and airflow rates for **SQM48T20020** converter with D height pins mounted vertically with air flowing from pin 3 to pin 1, MOSFET temperature  $\leq 120^\circ\text{C}$ ,  $V_{in} = 48\text{ V}$ .



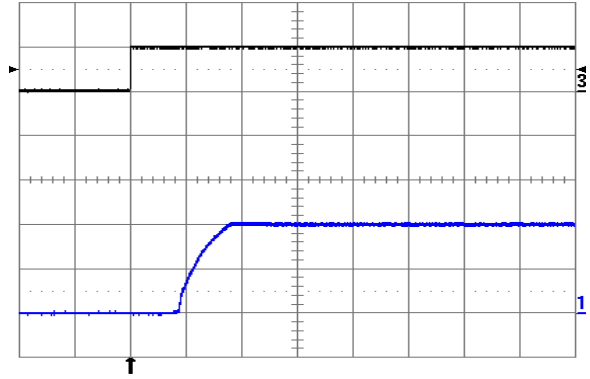
**Fig. 2.0V.2:** Efficiency vs. load current and input voltage for **SQM48T/S20020** converter mounted vertically with air flowing from pin 3 to pin 1 at a rate of 300 LFM (1.5 m/s) and  $T_A = 25^\circ\text{C}$ .



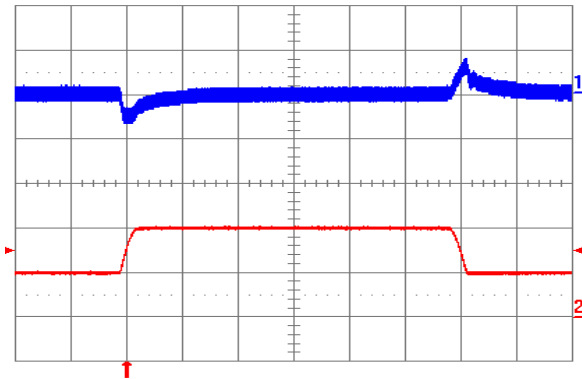
**SQM48T/S20020 (2.0 Volt Out)**



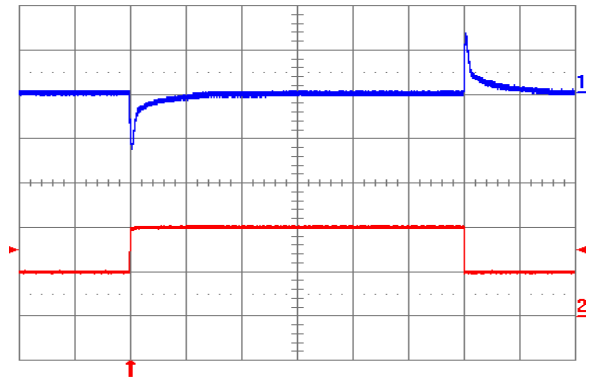
**Fig. 2.0V.3:** Turn-on transient at full rated load current (resistive) with no output capacitor at  $V_{in} = 48\text{ V}$ , triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (1 V/div.). Time scale: 2 ms/div.



**Fig. 2.0V.4:** Turn-on transient at full rated load current (resistive) plus 10,000  $\mu\text{F}$  at  $V_{in} = 48\text{ V}$ , triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (1 V/div.). Time scale: 2 ms/div.



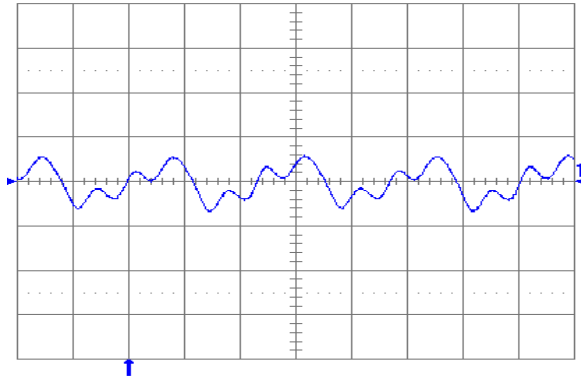
**Fig. 2.0V.5:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48\text{ V}$ . Top trace: output voltage (100 mV/div.). Bottom trace: load current (5A/div.). Current slew rate: 0.1 A/ $\mu\text{s}$ .  $C_o = 1\text{ }\mu\text{F}$  ceramic. Time scale: 0.2 ms/div.



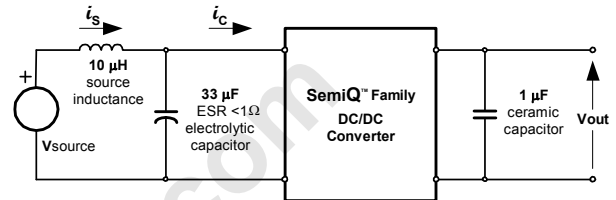
**Fig. 2.0V.6:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48\text{ V}$ . Top trace: output voltage (100 mV/div.). Bottom trace: load current (5 A/div.). Current slew rate: 5 A/ $\mu\text{s}$ .  $C_o = 450\text{ }\mu\text{F}$  tantalum + 1  $\mu\text{F}$  ceramic. Time scale: 0.2 ms/div.



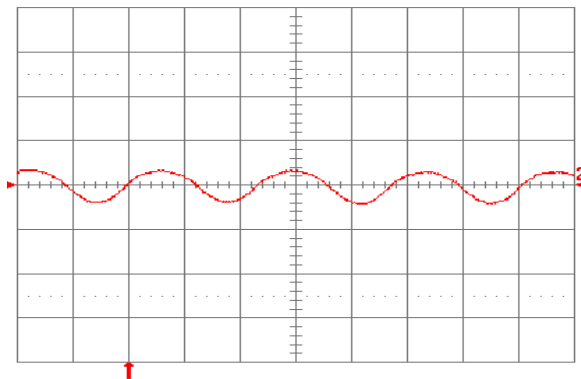
### SQM48T/S20020 (2.0 Volt Out)



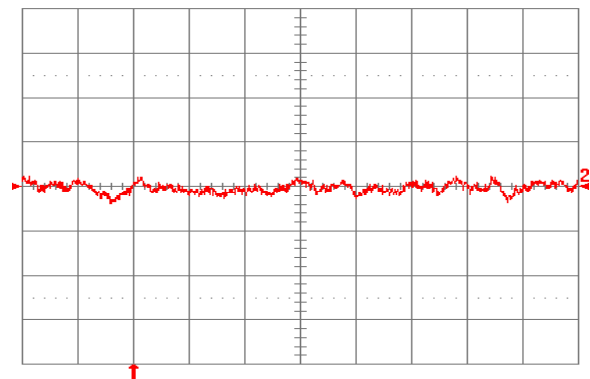
**Fig. 2.0V.7:** Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with  $C_o = 10 \mu\text{F}$  tantalum + 1  $\mu\text{F}$  ceramic and  $V_{in} = 48 \text{ V}$ . Time scale: 1  $\mu\text{s}/\text{div}$ .



**Fig. 2.0V.8:** Test setup for measuring input reflected ripple currents,  $i_c$  and  $i_s$ .



**Fig. 2.0V.9:** Input reflected ripple current,  $i_c$  (100 mA/div.), measured at input terminals at full rated load current and  $V_{in} = 48 \text{ V}$ . Refer to Fig. 2.0V.8 for test setup. Time scale: 1  $\mu\text{s}/\text{div}$ .

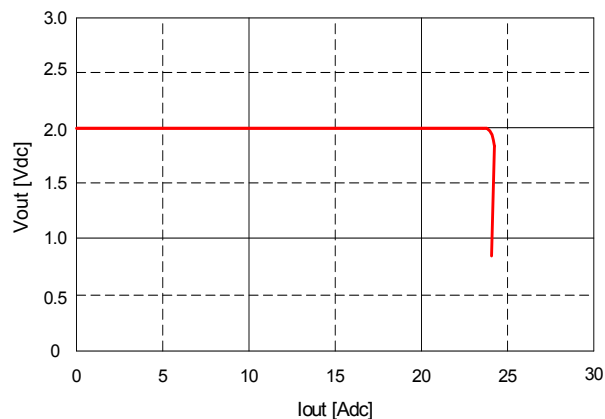


**Fig. 2.0V.10:** Input reflected ripple current,  $i_s$  (10 mA/div.), measured through 10  $\mu\text{H}$  at the source at full rated load current and  $V_{in} = 48 \text{ V}$ . Refer to Fig. 2.0V.8 for test setup. Time scale: 1  $\mu\text{s}/\text{div}$ .

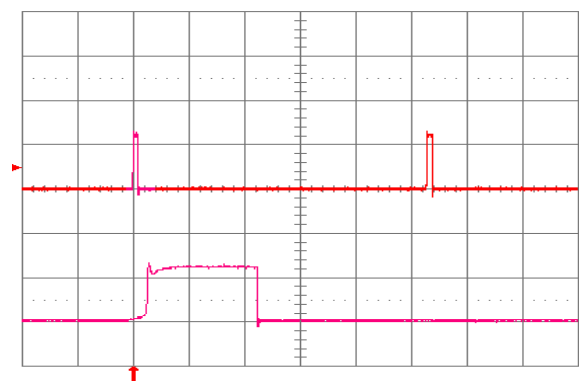




**SQM48T/S20020 (2.0 Volt Out)**



**Fig. 2.0V.11:** Output voltage vs. load current showing current limit point and converter shutdown point. Input voltage has almost no effect on current limit characteristic.



**Fig. 2.0V.12:** Load current (top trace, 20 A/div., 20 ms/div.) into a 10 mΩ short circuit during restart, at  $V_{in} = 48$  V. Bottom trace (20 A/div., 1 ms/div.) is an expansion of the on-time portion of the top trace.

# SQM48T/S20 DC-DC Converter Data Sheet

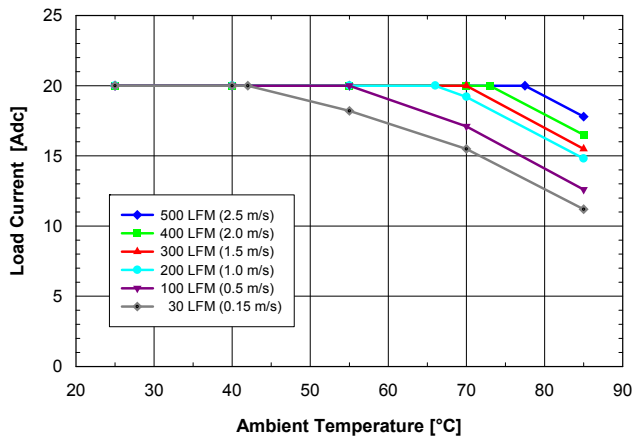
## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs

### Electrical Specifications: SQM48T/S20018 (1.8 Volt Out)

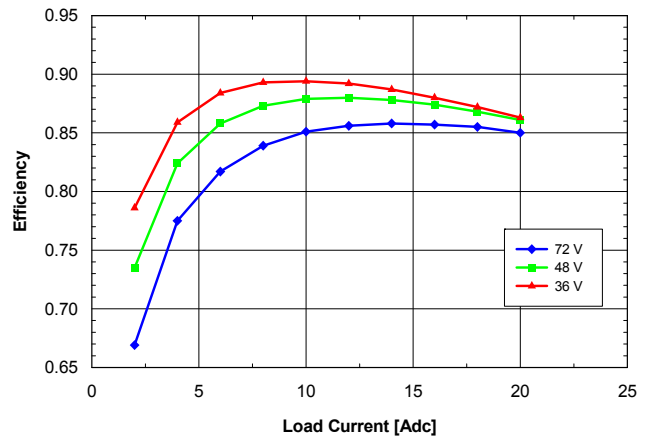
Conditions:  $T_A=25^\circ\text{C}$ , Airflow=300 LFM (1.5 m/s),  $V_{in}=48\text{ VDC}$ ,  $V_{out}=1.8\text{ VDC}$ , unless otherwise specified.

Parameter	Notes	Min	Typ	Max	Units
<b>Input Characteristics</b>					
Maximum Input Current	20 ADC, 1.8 VDC Out @ 36 VDC In			1.2	ADC
Input Stand-by Current	$V_{in} = 48\text{V}$ , converter disabled		3		mADC
Input No Load Current (0 load on the output)	$V_{in} = 48\text{V}$ , converter enabled		30		mADC
Input Reflected-Ripple Current	25 MHz bandwidth		6		mA <sub>PK-PK</sub>
Input Voltage Ripple Rejection	120 Hz		TBD		dB
<b>Output Characteristics</b>					
Output Voltage Set Point (no load)	$-40^\circ\text{C}$ to $85^\circ\text{C}$	1.782	1.800	1.818	VDC
Output Regulation					
Over Line			$\pm 2$	$\pm 4$	mV
Over Load			$\pm 2$	$\pm 5$	mV
Output Voltage Range	Over line, load and temperature <sup>2</sup>	1.773		1.827	VDC
Output Ripple and Noise - 25 MHz bandwidth	Full load + 10 $\mu\text{F}$ tantalum + 1 $\mu\text{F}$ ceramic		30	50	mV <sub>PK-PK</sub>
External Load Capacitance	Plus full load (resistive)			20,000	$\mu\text{F}$
Output Current Range		0		20	ADC
Current Limit Inception	Non-latching	21	24	27.5	ADC
Peak Short-Circuit Current	Non-latching. Short=10m $\Omega$ .		30	44	A
RMS Short-Circuit Current	Non-latching			6.7	Arms
<b>Dynamic Response</b>					
Load Change 25% of $I_{out\text{ Max}}$ , $di/dt = 0.1\text{A}/\mu\text{s}$	$C_o = 1\text{ }\mu\text{F}$ ceramic		80		mV
$di/dt = 5\text{A}/\mu\text{s}$	$C_o = 450\text{ }\mu\text{F}$ POS + 1 $\mu\text{F}$ ceramic		140		mV
Settling Time to 1%			100		$\mu\text{s}$
<b>Efficiency</b>					
100% Load			86		%
50% Load			88		%

Additional Notes: 2.  $-40^\circ\text{C}$  to  $85^\circ\text{C}$ .



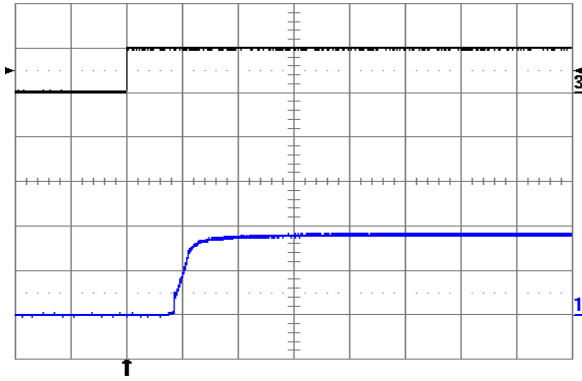
**Fig. 1.8V.1:** Available load current vs. ambient air temperature and airflow rates for **SQM48T20018** converter with D height pins mounted vertically with air flowing from pin 3 to pin 1, MOSFET temperature  $\leq 120^\circ\text{C}$ ,  $V_{in} = 48\text{ V}$ .



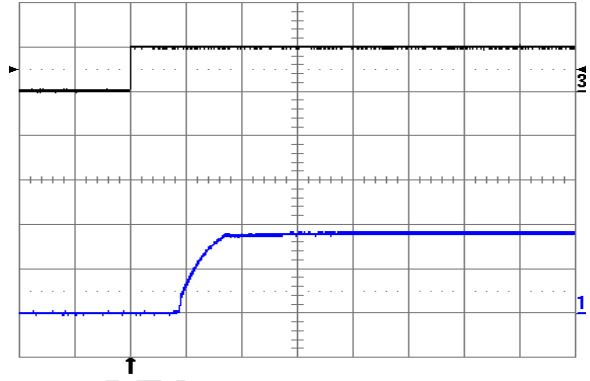
**Fig. 1.8V.2:** Efficiency vs. load current and input voltage for **SQM48T/S20018** converter mounted vertically with air flowing from pin 3 to pin 1 at a rate of 300 LFM (1.5 m/s) and  $T_A = 25^\circ\text{C}$ .



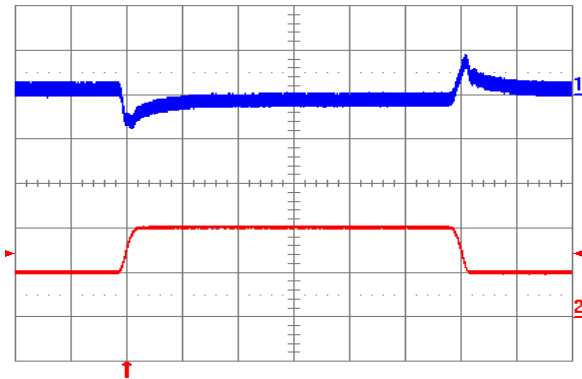
**SQM48T/S20018 (1.8 Volt Out)**



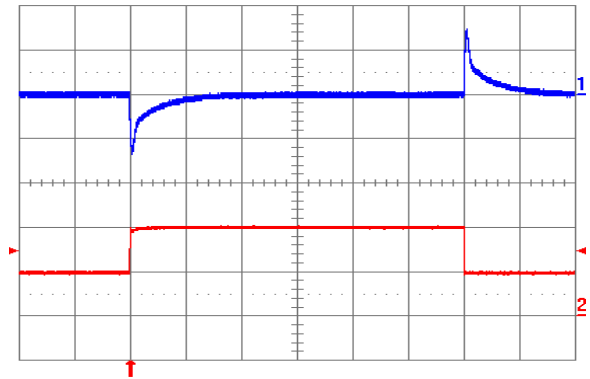
**Fig. 1.8V.3:** Turn-on transient at full rated load current (resistive) with no output capacitor at  $V_{in} = 48\text{ V}$ , triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (1 V/div.). Time scale: 2 ms/div.



**Fig. 1.8V.4:** Turn-on transient at full rated load current (resistive) plus 10,000  $\mu\text{F}$  at  $V_{in} = 48\text{ V}$ , triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (1 V/div.). Time scale: 2 ms/div.



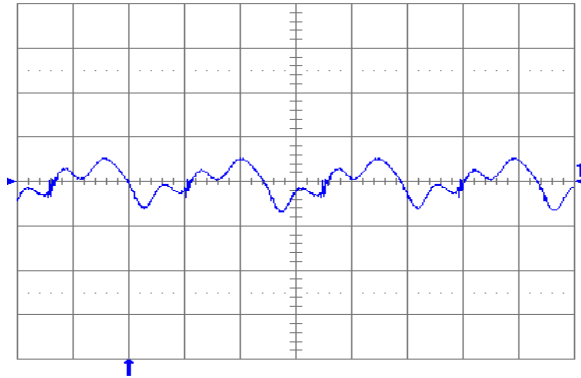
**Fig. 1.8V.5:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48\text{ V}$ . Top trace: output voltage (100 mV/div.). Bottom trace: load current (5 A/div.). Current slew rate: 0.1A/ $\mu\text{s}$ .  $C_o = 1\text{ }\mu\text{F}$  ceramic. Time scale: 0.2 ms/div.



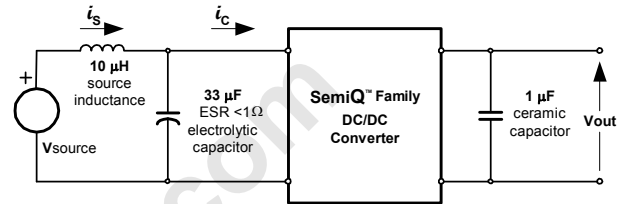
**Fig. 1.8V.6:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48\text{ V}$ . Top trace: output voltage (100 mV/div.). Bottom trace: load current (5 A/div.). Current slew rate: 5 A/ $\mu\text{s}$ .  $C_o = 450\text{ }\mu\text{F}$  tantalum + 1  $\mu\text{F}$  ceramic. Time scale: 0.2 ms/div.



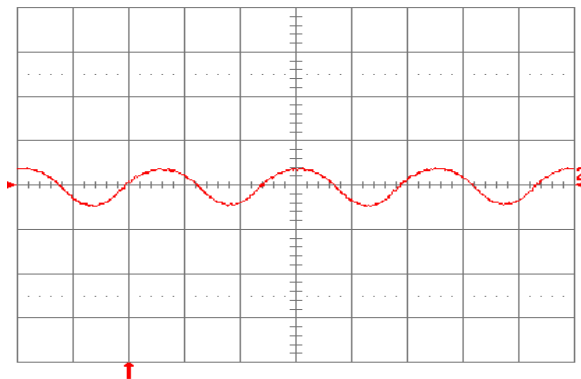
### SQM48T/S20018 (1.8 Volt Out)



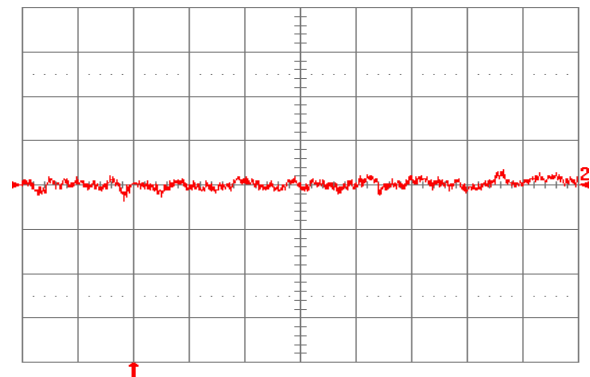
**Fig. 1.8V.7:** Output voltage ripple (20mV/div.) at full rated load current into a resistive load with  $C_o = 10 \mu\text{F}$  tantalum + 1  $\mu\text{F}$  ceramic and  $V_{in} = 48 \text{ V}$ . Time scale: 1  $\mu\text{s}/\text{div}$ .



**Fig. 1.8V.8:** Test setup for measuring input reflected ripple currents,  $i_c$  and  $i_s$ .



**Fig. 1.8V.9:** Input reflected ripple current,  $i_c$  (100 mA/div.), measured at input terminals at full rated load current and  $V_{in} = 48 \text{ V}$ . Refer to Fig. 1.8V.8 for test setup. Time scale: 1  $\mu\text{s}/\text{div}$ .



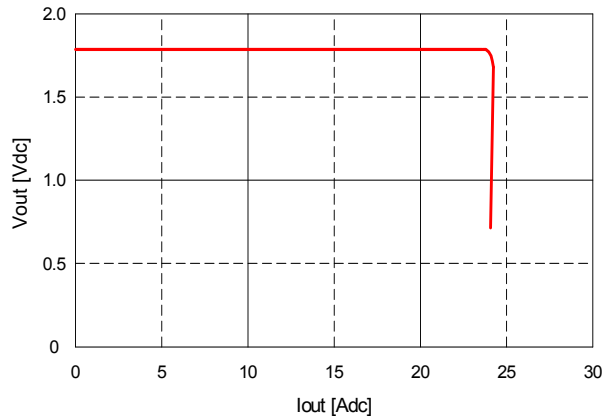
**Fig. 1.8V.10:** Input reflected ripple current,  $i_s$  (10 mA/div.), measured through 10  $\mu\text{H}$  at the source at full rated load current and  $V_{in} = 48 \text{ V}$ . Refer to Fig. 1.8V.8 for test setup. Time scale: 1  $\mu\text{s}/\text{div}$ .

# SQM48T/S20 DC-DC Converter Data Sheet

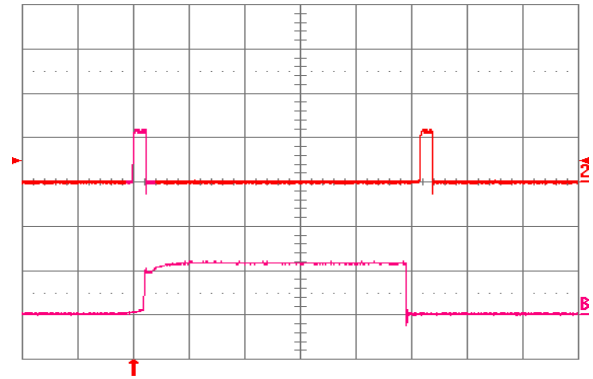
## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs



### SQM48T/S20018 (1.8 Volt Out)



**Fig. 1.8V.11:** Output voltage vs. load current showing current limit point and converter shutdown point. Input voltage has almost no effect on current limit characteristic.



**Fig. 1.8V.12:** Load current (top trace, 20 A/div., 20 ms/div.) into a 10 mΩ short circuit during restart, at  $V_{in} = 48$  V. Bottom trace (20 A/div., 1 ms/div.) is an expansion of the on-time portion of the top trace.



# SQM48T/S20 DC-DC Converter Data Sheet

## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs

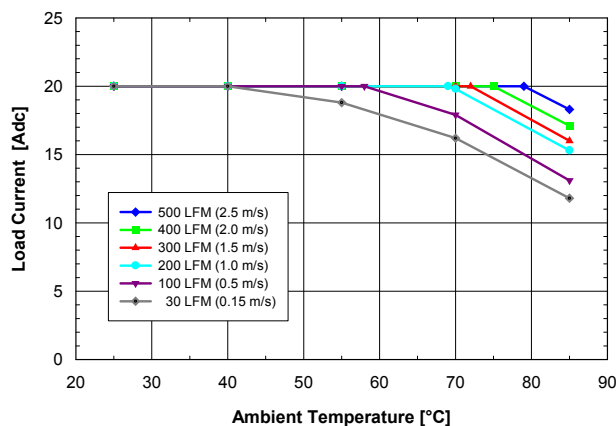


### Electrical Specifications: SQM48T/S20015 (1.5 Volt Out)

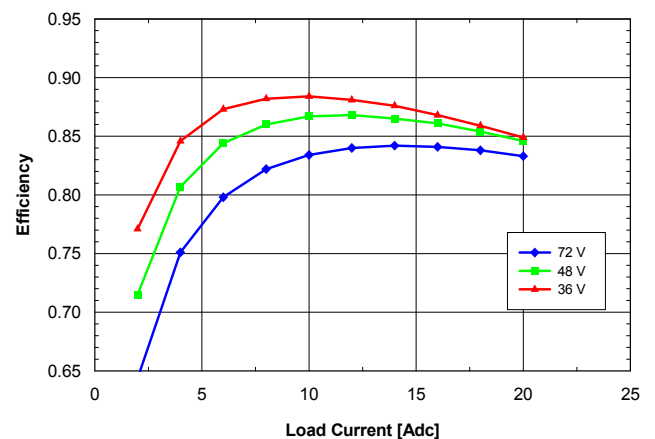
Conditions:  $T_A=25^\circ\text{C}$ , Airflow=300 LFM (1.5 m/s),  $V_{in}=48\text{ VDC}$ ,  $V_{out}=1.5\text{ VDC}$ , unless otherwise specified.

Parameter	Notes	Min	Typ	Max	Units
<b>Input Characteristics</b>					
Maximum Input Current	20 ADC, 1.5 VDC Out @ 36 VDC $I_{in}$			1.0	ADC
Input Stand-by Current	$V_{in} = 48\text{ V}$ , converter disabled		3		mADC
Input No Load Current (0 load on the output)	$V_{in} = 48\text{ V}$ , converter enabled		27		mADC
Input Reflected-Ripple Current	25 MHz bandwidth		6		$\text{mA}_{\text{PK-PK}}$
Input Voltage Ripple Rejection	120 Hz		TBD		dB
<b>Output Characteristics</b>					
Output Voltage Set Point (no load)	$-40^\circ\text{C}$ to $85^\circ\text{C}$	1.485	1.500	1.515	VDC
Output Regulation					
Over Line			$\pm 2$	$\pm 4$	mV
Over Load			$\pm 2$	$\pm 4$	mV
Output Voltage Range	Over line, load and temperature <sup>2</sup>	1.477		1.523	VDC
Output Ripple and Noise - 25 MHz bandwidth	Full load + 10 $\mu\text{F}$ tantalum + 1 $\mu\text{F}$ ceramic		30	50	$\text{mV}_{\text{PK-PK}}$
External Load Capacitance	Plus full load (resistive)			20,000	$\mu\text{F}$
Output Current Range		0		20	ADC
Current Limit Inception	Non-latching	21	24	27.5	ADC
Peak Short-Circuit Current	Non-latching. Short=10m $\Omega$ .		30	44	A
RMS Short-Circuit Current	Non-latching			6.7	Arms
<b>Dynamic Response</b>					
Load Change 25% of $I_{out}\text{ Max}$ , $di/dt = 0.1\text{ A}/\mu\text{s}$	$C_o = 1\text{ }\mu\text{F}$ ceramic		80		mV
$di/dt = 5\text{ A}/\mu\text{s}$	$C_o = 450\text{ }\mu\text{F}$ POS + 1 $\mu\text{F}$ ceramic		140		mV
Settling Time to 1%			100		$\mu\text{s}$
<b>Efficiency</b>					
100% Load			84.5		%
50% Load			86.5		%

Additional Notes: 2.  $-40^\circ\text{C}$  to  $85^\circ\text{C}$ .



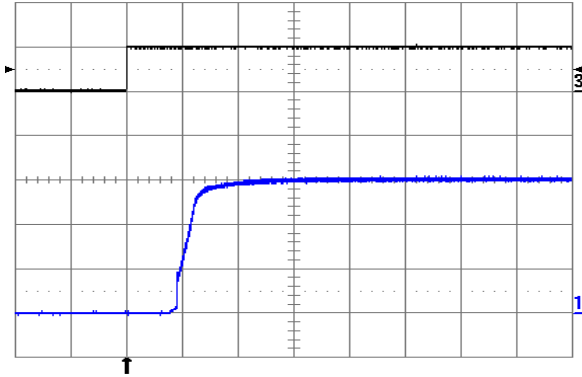
**Fig. 1.5V.1:** Available load current vs. ambient air temperature and airflow rates for **SQM48T20015** converter with D height pins mounted vertically with air flowing from pin 3 to pin 1, MOSFET temperature  $\leq 120^\circ\text{C}$ ,  $V_{in} = 48\text{ V}$ .



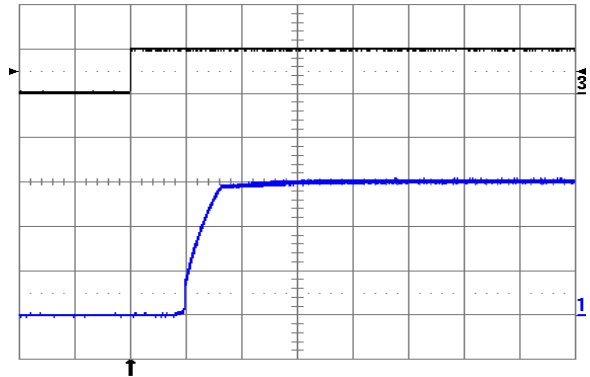
**Fig. 1.5V.2:** Efficiency vs. load current and input voltage for **SQM48T/S20015** converter mounted vertically with air flowing from pin 3 to pin 1 at a rate of 300 LFM (1.5 m/s) and  $T_a = 25^\circ\text{C}$ .



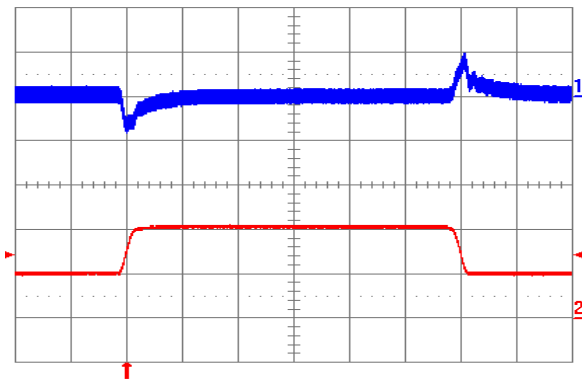
**SQM48T/S20015 (1.5 Volt Out)**



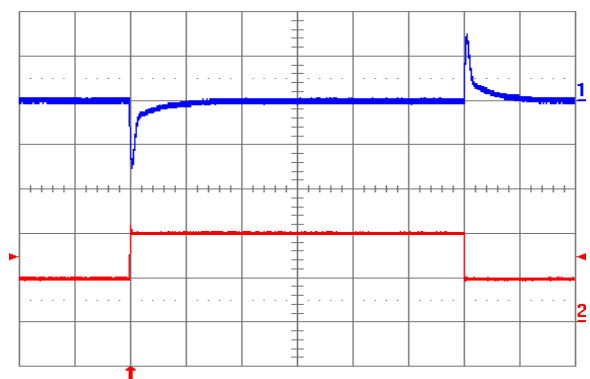
**Fig. 1.5V.3:** Turn-on transient at full rated load current (resistive) with no output capacitor at  $V_{in} = 48$  V, triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (0.5 V/div.). Time scale: 2 ms/div.



**Fig. 1.5V.4:** Turn-on transient at full rated load current (resistive) plus 10,000  $\mu$ F at  $V_{in} = 48$  V, triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (0.5 V/div.). Time scale: 2 ms/div.

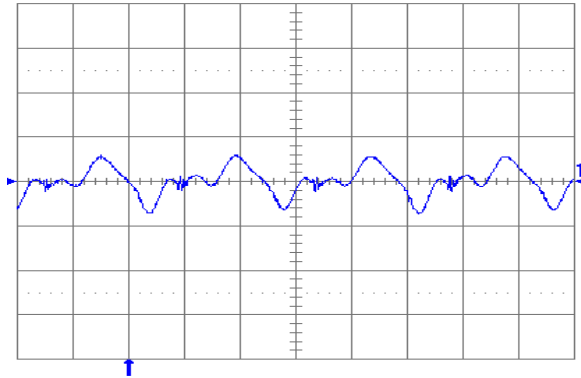


**Fig. 1.5V.5:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48$  V. Top trace: output voltage (100 mV/div.). Bottom trace: load current (5 A/div.). Current slew rate: 0.1 A/ $\mu$ s.  $C_o = 1$   $\mu$ F ceramic. Time scale: 0.2 ms/div.

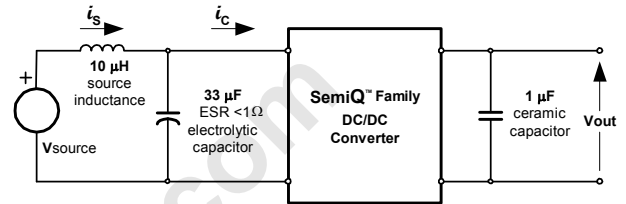


**Fig. 1.5V.6:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48$  V. Top trace: output voltage (100 mV/div.). Bottom trace: load current (5 A/div.). Current slew rate: 5 A/ $\mu$ s.  $C_o = 450$   $\mu$ F tantalum + 1  $\mu$ F ceramic. Time scale: 0.2 ms/div.

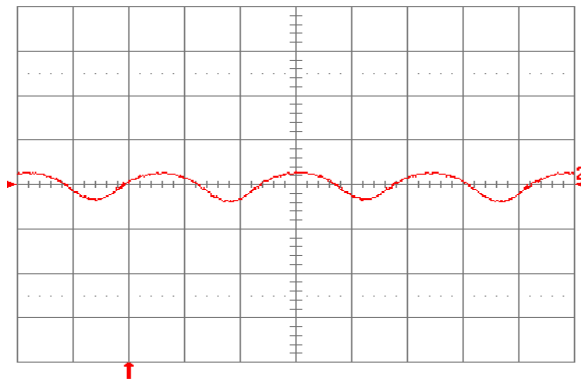
**SQM48T/S20015 (1.5 Volt Out)**



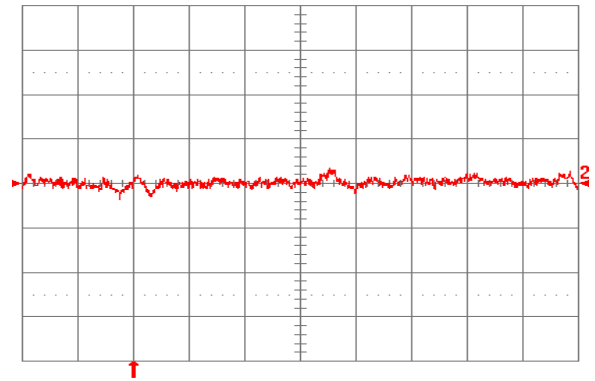
**Fig. 1.5V.7:** Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with  $C_o = 10 \mu\text{F}$  tantalum + 1  $\mu\text{F}$  ceramic and  $V_{in} = 48 \text{ V}$ . Time scale: 1  $\mu\text{s}/\text{div}$ .



**Fig. 1.5V.8:** Test setup for measuring input reflected ripple currents,  $i_c$  and  $i_s$ .



**Fig. 1.5V.9:** Input reflected ripple current,  $i_c$  (100 A/div.), measured at input terminals at full rated load current and  $V_{in} = 48 \text{ V}$ . Refer to Fig. 1.5V.8 for test setup. Time scale: 1  $\mu\text{s}/\text{div}$ .



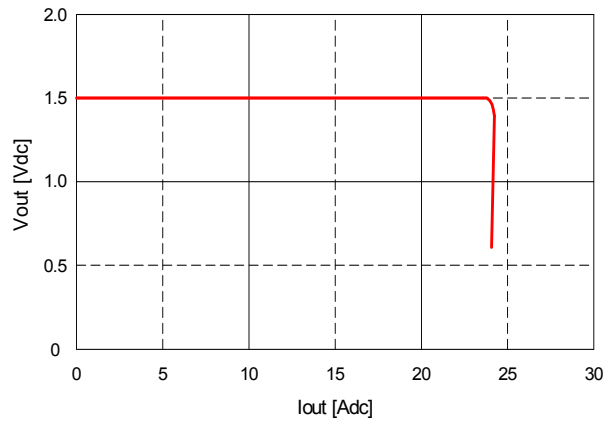
**Fig. 1.5V.10:** Input reflected ripple current,  $i_s$  (10 mA/div.), measured through 10  $\mu\text{H}$  at the source at full rated load current and  $V_{in} = 48 \text{ V}$ . Refer to Fig. 1.5V.8 for test setup. Time scale: 1  $\mu\text{s}/\text{div}$ .

# SQM48T/S20 DC-DC Converter Data Sheet

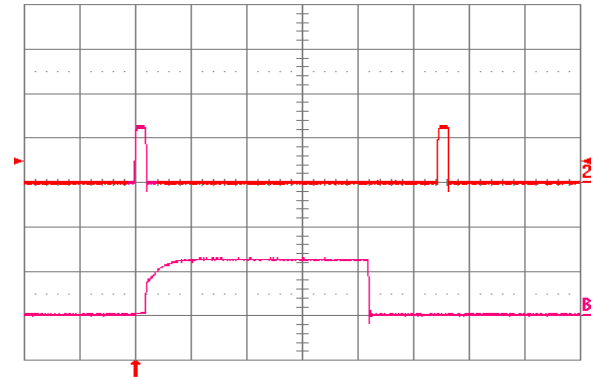
## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs



### SQM48T/S20015 (1.5 Volt Out)



**Fig. 1.5V.11:** Output voltage vs. load current showing current limit point and converter shutdown point. Input voltage has almost no effect on current limit characteristic.



**Fig. 1.5V.12:** Load current (top trace, 20 A/div., 20 ms/div.) into a 10 mΩ short circuit during restart, at Vin = 48 V. Bottom trace (20 A/div., 1 ms/div.) is an expansion of the on-time portion of the top trace.

# SQM48T/S20 DC-DC Converter Data Sheet

## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs

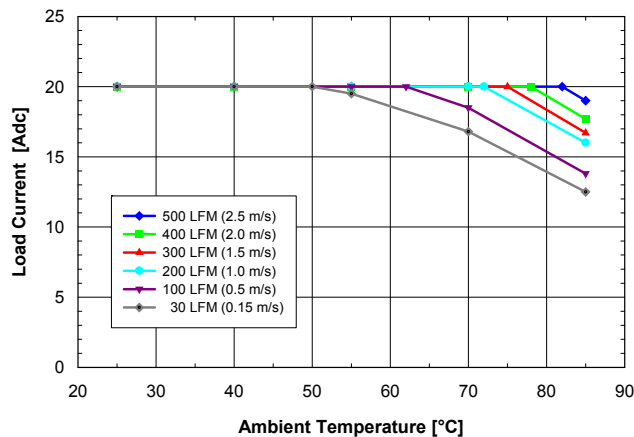


### Electrical Specifications: SQM48T/S20012 (1.2 Volt Out)

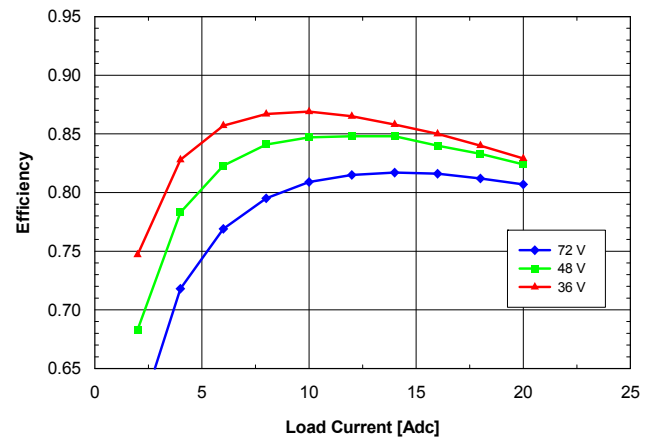
Conditions:  $T_A=25^\circ\text{C}$ , Airflow=300 LFM (1.5 m/s),  $V_{in}=48\text{ VDC}$ ,  $V_{out}=1.2\text{ VDC}$ , unless otherwise specified.

Parameter	Notes	Min	Typ	Max	Units
<b>Input Characteristics</b>					
Maximum Input Current	20 ADC, 1.2 VDC Out @ 36 VDC In			0.85	ADC
Input Stand-by Current	$V_{in} = 48\text{ V}$ , converter disabled		3		mADC
Input No Load Current (0 load on the output)	$V_{in} = 48\text{ V}$ , converter enabled		24		mADC
Input Reflected-Ripple Current	25 MHz bandwidth		6		mA <sub>PK-PK</sub>
Input Voltage Ripple Rejection	120 Hz		TBD		dB
<b>Output Characteristics</b>					
Output Voltage Set Point (no load)	$-40^\circ\text{C}$ to $85^\circ\text{C}$	1.188	1.200	1.212	VDC
Output Regulation					
Over Line			$\pm 1$	$\pm 3$	mV
Over Load			$\pm 1$	$\pm 3$	mV
Output Voltage Range	Over line, load and temperature <sup>2</sup>	1.182		1.218	VDC
Output Ripple and Noise - 25 MHz bandwidth	Full load + 10 $\mu\text{F}$ tantalum + 1 $\mu\text{F}$ ceramic		30	50	mV <sub>PK-PK</sub>
External Load Capacitance	Plus full load (resistive)			20,000	$\mu\text{F}$
Output Current Range		0		20	ADC
Current Limit Inception	Non-latching	21	24	27.5	ADC
Peak Short-Circuit Current	Non-latching. Short=10m $\Omega$ .		30	44	A
RMS Short-Circuit Current	Non-latching			6.7	Arms
<b>Dynamic Response</b>					
Load Change 25% of $I_{out\text{ Max}}$ , $di/dt = 0.1\text{ A}/\mu\text{s}$	$C_o = 1\text{ }\mu\text{F}$ ceramic		80		mV
$di/dt = 5\text{ A}/\mu\text{s}$	$C_o = 450\text{ }\mu\text{F}$ POS + 1 $\mu\text{F}$ ceramic		140		mV
Settling Time to 1%			100		$\mu\text{s}$
<b>Efficiency</b>					
100% Load			82.5		%
50% Load			84.5		%

Additional Notes: 2.  $-40^\circ\text{C}$  to  $85^\circ\text{C}$ .



**Fig. 1.2V.1:** Available load current vs. ambient air temperature and airflow rates for **SQM48T20012** converter with D height pins mounted vertically with air flowing from pin 3 to pin 1, MOSFET temperature  $\leq 120^\circ\text{C}$ ,  $V_{in} = 48\text{ V}$ .



**Fig. 1.2V.2:** Efficiency vs. load current and input voltage for **SQM48T/S20012** converter mounted vertically with air flowing from pin 3 to pin 1 at a rate of 300 LFM (1.5 m/s) and  $T_a = 25^\circ\text{C}$ .

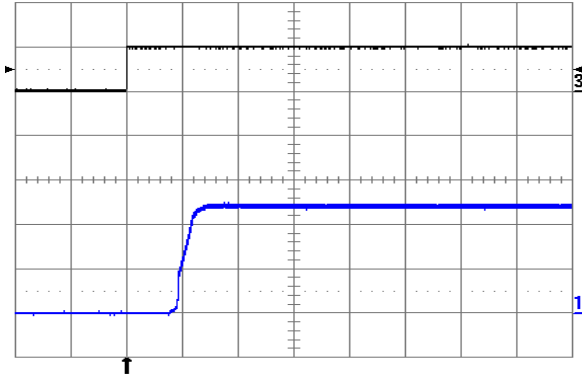


# SQM48T/S20 DC-DC Converter Data Sheet

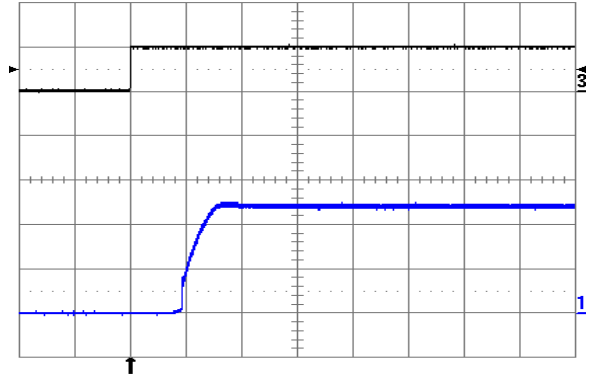
## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs



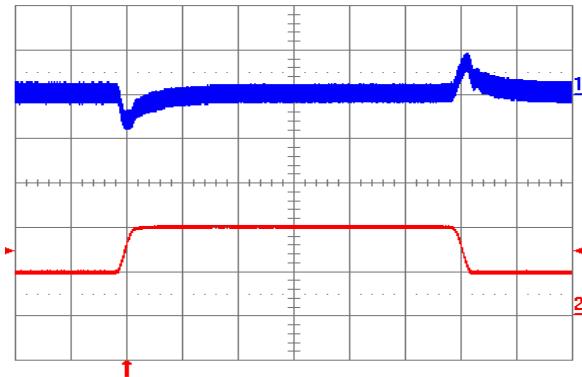
### SQM48T/S20012 (1.2 Volt Out)



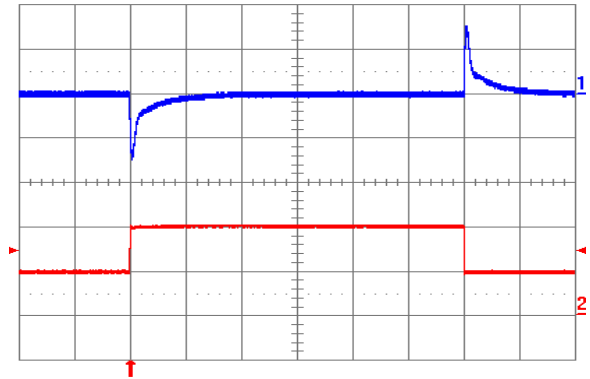
**Fig. 1.2V.3:** Turn-on transient at full rated load current (resistive) with no output capacitor at  $V_{in} = 48$  V, triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (0.5 V/div.). Time scale: 2 ms/div.



**Fig. 1.2V.4:** Turn-on transient at full rated load current (resistive) plus 10,000  $\mu$ F at  $V_{in} = 48$  V, triggered via ON/OFF pin. Top trace: ON/OFF signal (5 V/div.). Bottom trace: output voltage (0.5 V/div.). Time scale: 2 ms/div.

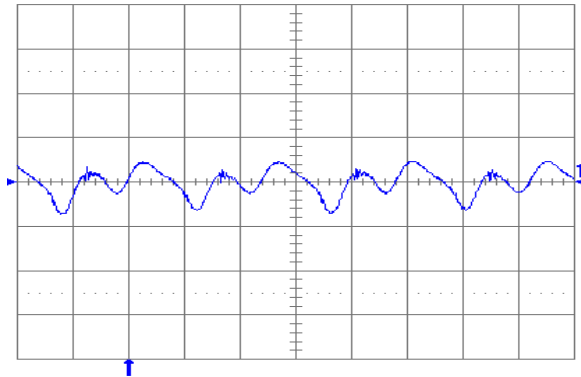


**Fig. 1.2V.5:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48$  V. Top trace: output voltage (100 mV/div.). Bottom trace: load current (5 A/div.). Current slew rate: 0.1A/ $\mu$ s.  $C_o = 1$   $\mu$ F ceramic. Time scale: 0.2 ms/div.

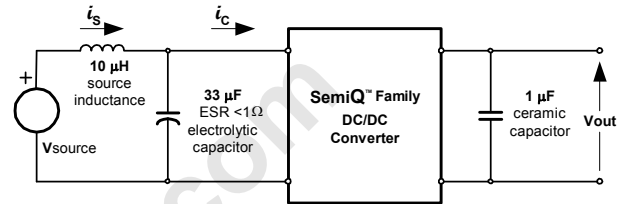


**Fig. 1.2V.6:** Output voltage response to load current step-change (5A – 10A – 5A) at  $V_{in} = 48$  V. Top trace: output voltage (100 mV/div.). Bottom trace: load current (5 A/div.). Current slew rate: 5 A/ $\mu$ s.  $C_o = 450$   $\mu$ F tantalum + 1  $\mu$ F ceramic. Time scale: 0.2 ms/div.

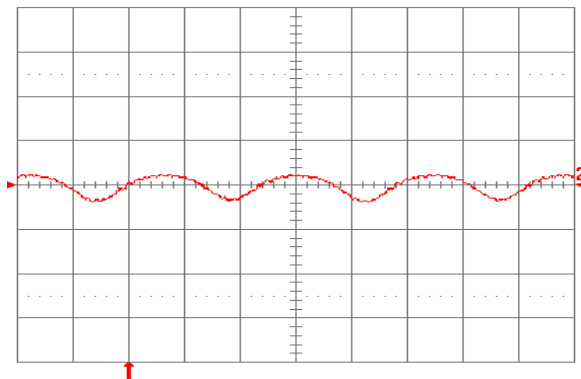
**SQM48T/S20012 (1.2 Volt Out)**



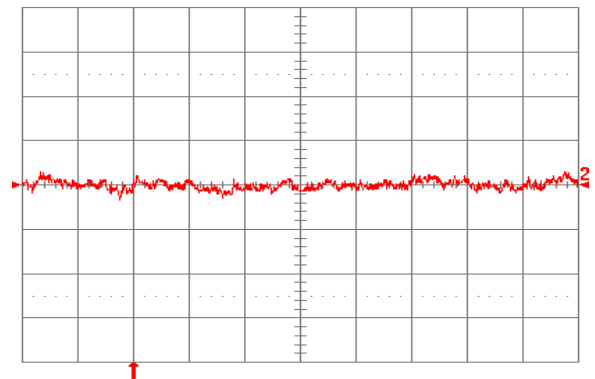
**Fig. 1.2V.7:** Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with  $C_o = 10 \mu\text{F}$  tantalum + 1  $\mu\text{F}$  ceramic and  $V_{in} = 48 \text{ V}$ . Time scale: 1  $\mu\text{s}/\text{div}$ .



**Fig. 1.2V.8:** Test setup for measuring input reflected ripple currents,  $i_c$  and  $i_s$



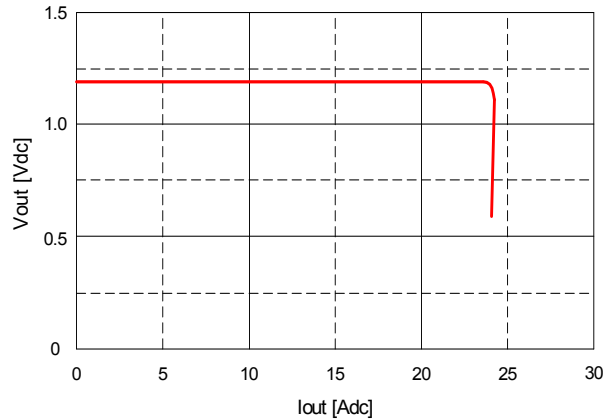
**Fig. 1.2V.9:** Input reflected ripple current,  $i_c$  (100mA/div.), measured at input terminals at full rated load current and  $V_{in} = 48 \text{ V}$ . Refer to Fig. 1.2V.8 for test setup. Time scale: 1  $\mu\text{s}/\text{div}$ .



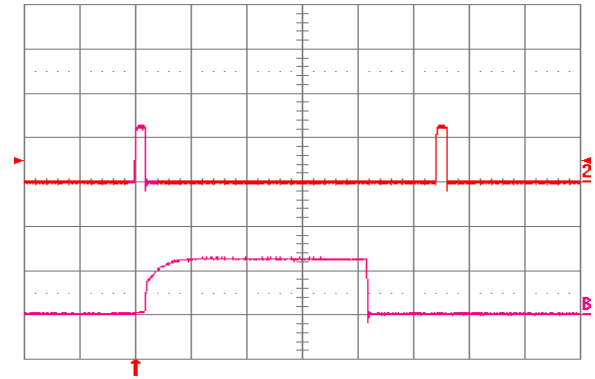
**Fig. 1.2V.10:** Input reflected ripple current,  $i_s$  (10 mA/div.), measured through 10  $\mu\text{H}$  at the source at full rated load current and  $V_{in} = 48 \text{ V}$ . Refer to Fig. 1.2V.8 for test setup. Time scale: 1  $\mu\text{s}/\text{div}$ .



**SQM48T/S20012 (1.2 Volt Out)**



**Fig. 1.2V.11:** Output voltage vs. load current showing current limit point and converter shutdown point. Input voltage has almost no effect on current limit characteristic.



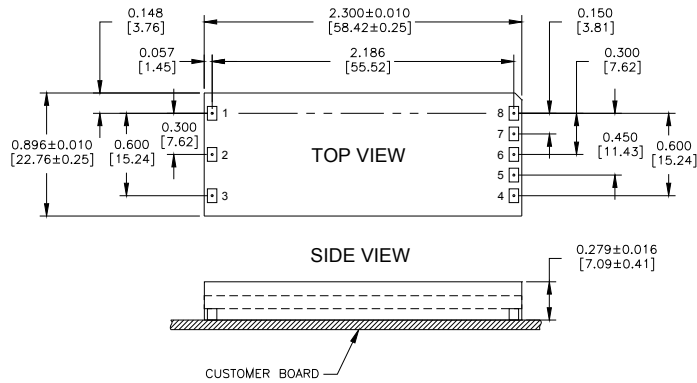
**Fig. 1.2V.16:** Load current (top trace, 20 A/div., 50 ms/div.) into a 10 mΩ short circuit during restart, at  $V_{in} = 48$  V. Bottom trace (20 A/div., 5 ms/div.) is an expansion of the on-time portion of the top trace.

# SQM48T/S20 DC-DC Converter Data Sheet

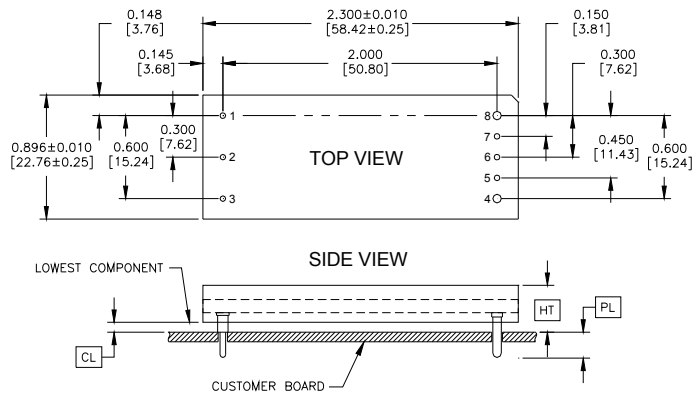
## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs



### Physical Information



**SQM48S Pinout (Surface Mount)**



**SQM48T Pinout (Through-hole)**

### SQM48S Platform Notes

- All dimensions are in inches [mm]
- Connector Material: Copper
- Connector Finish: Gold over Nickel
- Converter Weight: 0.66 oz [18.5 g]
- Recommended Surface-Mount Pads:  
Min. 0.080" X 0.112" [2.03 x 2.84]  
Max. 0.092" X 0.124" [2.34 x 3.15]

### Pad/Pin Connections

Pad/Pin #	Function
1	Vin (+)
2	ON/OFF
3	Vin (-)
4	Vout (-)
5	SENSE(-)
6	TRIM
7	SENSE(+)
8	Vout (+)

### SQM48T Platform Notes

- All dimensions are in inches [mm]
- Pins 1-3 and 5-7 are Ø 0.040" [1.02] with Ø 0.078" [1.98] shoulder
- Pins 4 and 8 are Ø 0.062" [1.57] without shoulder
- Pin material: Brass
- Pin Finish: Tin/Lead over Nickel
- Converter Weight: 0.66 oz [18.5 g]

Height Option	HT (Max. Height)	CL (Min. Clearance)	Pin Option	PL Pin Length
	+0.000 [+0.00] -0.038 [- 0.97]	+0.016 [+0.41] -0.000 [- 0.00]		±0.005 [±0.13]
A	0.325 [8.26]	0.030 [0.77]	A	0.188 [4.77]
B	0.358 [9.09]	0.063 [1.60]	B	0.145 [3.68]
C	0.522 [13.26]	0.227 [5.77]	C	0.110 [2.79]
D	0.422 [10.72]	0.127 [3.23]		
E	0.304 [7.72]	0.09 [0.237]		

# SQM48T/S20 DC-DC Converter Data Sheet

## 36-75 VDC Input; 1.2-3.3 VDC @ 20A Outputs



### Converter Part Numbering Ordering Information

Product Series	Input Voltage	Mounting Scheme	Rated Load Current	Output Voltage		ON/OFF Logic	Maximum Height [HT]	Pin Length [PL]	Special Features	RoHS
<b>SQM</b>	<b>48</b>	<b>S</b>	<b>20</b>	<b>018</b>	-	<b>N</b>	<b>B</b>	<b>A</b>	<b>0</b>	
1/8 <sup>th</sup> Brick Format	36-75 V	S ⇒ Surface Mount	20A	012 ⇒ 1.2V 015 ⇒ 1.5V 018 ⇒ 1.8V 020 ⇒ 2.0V 025 ⇒ 2.5V 033 ⇒ 3.3V		N ⇒ Negative  P ⇒ Positive	<u>SMT</u> S ⇒ 0.295"	<u>SMT</u> 0 ⇒ 0.00"	0 ⇒ STD  T ⇒ Alternative Trim Option (For 1.2V)	No Suffix ⇒ RoHS lead-solder-exemption compliant  G ⇒ RoHS compliant for all six substances

Product Series	Input Voltage	Mounting Scheme	Rated Load Current	Output Voltage		ON/OFF Logic	Maximum Height [HT]	Pin Length [PL]	Special Features	RoHS
<b>SQM</b>	<b>48</b>	<b>T</b>	<b>20</b>	<b>018</b>	-	<b>N</b>	<b>B</b>	<b>A</b>	<b>0</b>	
1/8 <sup>th</sup> Brick Format	36-75 V	T ⇒ Through-hole	20A	012 ⇒ 1.2V 015 ⇒ 1.5V 018 ⇒ 1.8V 020 ⇒ 2.0V 025 ⇒ 2.5V 033 ⇒ 3.3V		N ⇒ Negative  P ⇒ Positive	<u>Through hole</u> A ⇒ 0.325" B ⇒ 0.358" C ⇒ 0.522" D ⇒ 0.422" E ⇒ 0.304"	<u>Through hole</u> A ⇒ 0.188" B ⇒ 0.145" C ⇒ 0.110"	0 ⇒ STD  T ⇒ Alternative Trim Option (For 1.2V)	No Suffix ⇒ RoHS lead-solder-exemption compliant  G ⇒ RoHS compliant for all six substances

The example above describes P/N SQM48T20018-NBA0: 36-75 V input, through-hole mounting, 20 A @ 1.8 V output, negative ON/OFF logic, a maximum height of 0.358", a through the board pin length of 0.188", and RoHS lead-solder-exemption compliancy.

Model numbers highlighted in yellow or shaded are not recommended for new designs.

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