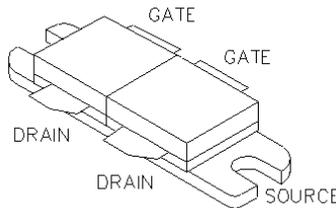




General Description

Silicon VDMOS and LDMOS transistors designed specifically for broadband RF applications. Suitable for Military Radios, Cellular and Paging Amplifier Base Stations, Broadcast FM/AM, MRI, Laser Driver and others.

"Polyfet"TM process features low feedback and output capacitances resulting in high F_t transistors with high input impedance and high efficiency.



SILICON GATE ENHANCEMENT MODE

RF POWER VDMOS TRANSISTOR

300.0 Watts Push - Pull

Package Style AR

**HIGH EFFICIENCY, LINEAR
HIGH GAIN, LOW NOISE**

ABSOLUTE MAXIMUM RATINGS (T = 25 °C)

Total Device Dissipation	Junction to Case Thermal Resistance	Maximum Junction Temperature	Storage Temperature	DC Drain Current	Drain to Gate Voltage	Drain to Source Voltage	Gate to Source Voltage
500 Watts	0.30 °C/W	200 °C	-65 °C to 150 °C	25.0 A	125V	125V	20 V

RF CHARACTERISTICS (300.0 WATTS OUTPUT)

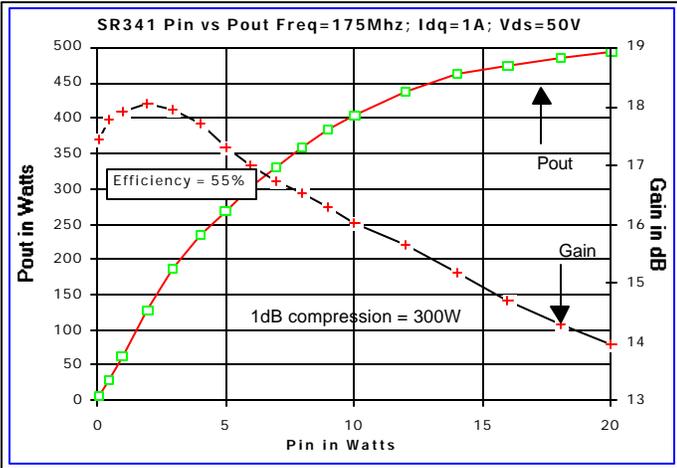
SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Gps	Common Source Power Gain	16			dB	$I_{dq} = 0.80 \text{ A}$, $V_{ds} = 50.0 \text{ V}$, $F = 175 \text{ MHz}$
η	Drain Efficiency		65		%	$I_{dq} = 0.80 \text{ A}$, $V_{ds} = 50.0 \text{ V}$, $F = 175 \text{ MHz}$
VSWR	Load Mismatch Tolerance			20:1	Relative	$I_{dq} = 0.80 \text{ A}$, $V_{ds} = 50.0 \text{ V}$, $F = 175 \text{ MHz}$

ELECTRICAL CHARACTERISTICS (EACH SIDE)

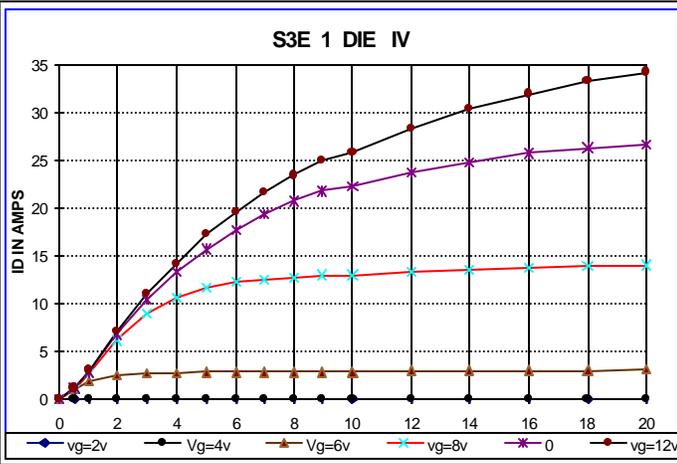
SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Bvdss	Drain Breakdown Voltage	125			V	$I_{ds} = 40.00 \text{ mA}$, $V_{gs} = 0 \text{ V}$
Idss	Zero Bias Drain Current			5.0	mA	$V_{ds} = 50.0 \text{ V}$, $V_{gs} = 0 \text{ V}$
Igss	Gate Leakage Current			1	uA	$V_{ds} = 0 \text{ V}$, $V_{gs} = 30 \text{ V}$
Vgs	Gate Bias for Drain Current	1		7	V	$I_{ds} = 0.30 \text{ A}$, $V_{gs} = V_{ds}$
gM	Forward Transconductance		5.5		Mho	$V_{ds} = 10 \text{ V}$, $V_{gs} = 5 \text{ V}$
Rdson	Saturation Resistance		0.30		Ohm	$V_{gs} = 20 \text{ V}$, $I_{ds} = 6.00 \text{ A}$
I _{dsat}	Saturation Current		35.00		Amp	$V_{gs} = 20 \text{ V}$, $V_{ds} = 10 \text{ V}$
Ciss	Common Source Input Capacitance		400.0		pF	$V_{ds} = 50.0 \text{ V}$, $V_{gs} = 0 \text{ V}$, $F = 1 \text{ MHz}$
Crss	Common Source Feedback Capacitance		15.0		pF	$V_{ds} = 50.0 \text{ V}$, $V_{gs} = 0 \text{ V}$, $F = 1 \text{ MHz}$
Coss	Common Source Output Capacitance		200.0		pF	$V_{ds} = 50.0 \text{ V}$, $V_{gs} = 0 \text{ V}$, $F = 1 \text{ MHz}$

SR341

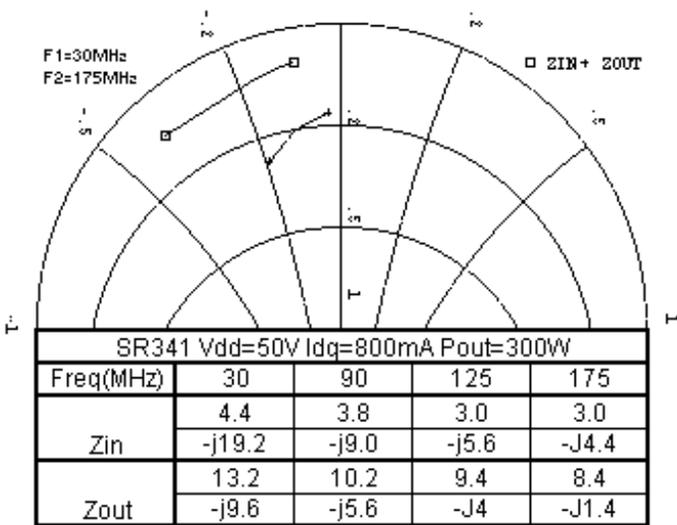
POUT VS PIN GRAPH



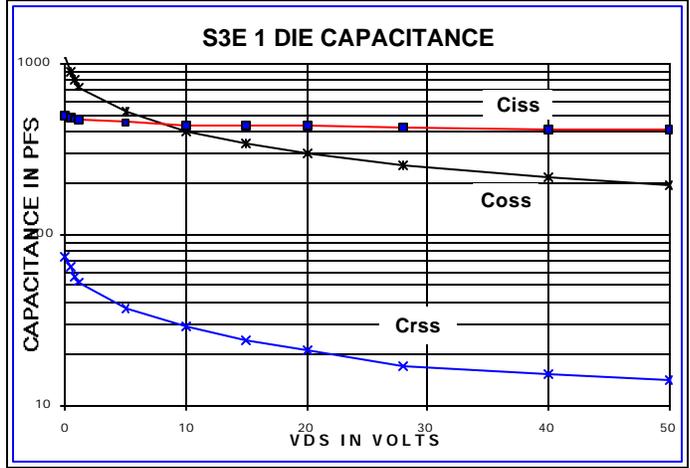
IV CURVE



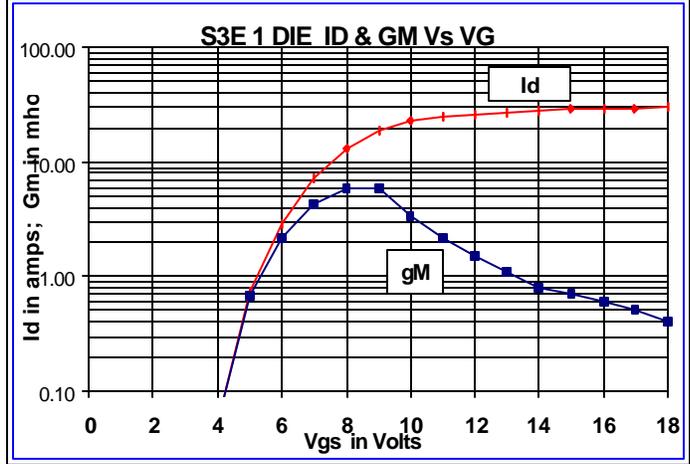
Zin Zout



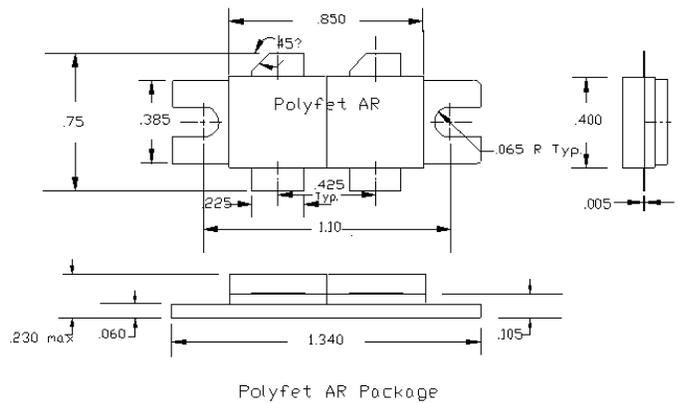
CAPACITANCE VS VOLTAGE



ID & GM VS VGS



PACKAGE DIMENSIONS IN INCHES



Tolerance .XX +/-0.01 .XXX +/- .005 inches