

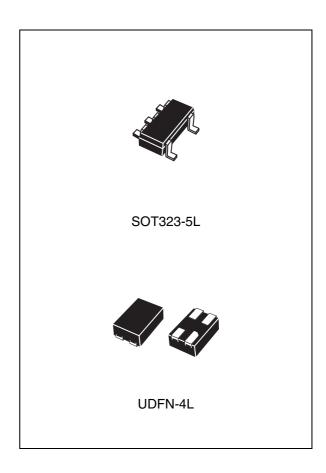
Ultra-low current 2.4 V precision analog temperature sensor

Features

- Precision analog voltage output temperature sensor
- ±1.5 °C maximum temperature accuracy at 25 °C (±0.5 °C typical)
- Ultra-low quiescent supply current: 4.8 µA (typ) and 8.0 µA (max)
- Operating voltage range: 2.4 V to 5.5 V
- Operating temperature range: -55 °C to 130 °C (grade 7) -40 °C to 85 °C (grade 9)
- SOT323-5L 5-lead package
- UDFN-4L 4-lead package

Applications

- Smartphones
- Multimedia PDA devices
- GPS devices
- Portable medical instruments
- Voltage-controlled crystal oscillator temperature monitors
- RF power transistor monitor



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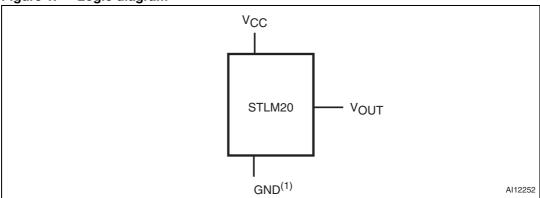
STLM20 Description

1 Description

The STLM20 is a precision analog output temperature sensor for low current applications where maximizing battery life is important. It operates over a -55 °C to 130 °C (grade 7) or -40 °C to 85 °C (grade 9) temperature range. The power supply operating range is 2.4 V to 5.5 V. The maximum temperature accuracy of the STLM20 is \pm 1.5 °C (\pm 0.5 °C typ) at an ambient temperature of 25 °C and V_{CC} of 2.7 V. The temperature error increases linearly and reaches a maximum of \pm 2.5 °C at the temperature range extremes. The temperature range is affected by the power supply voltage. For the temperature grade 7 device, a power supply voltage of 2.7 V to 5.5 V, the temperature range extremes are +130 °C and -55 °C (decreasing the power supply voltage from 2.7 V to 2.4 V changes the low end of the operating temperature range from -55 °C to -30 °C, while the positive remains at +130 °C).

The STLM20 has a maximum quiescent supply current of 8 μ A. Therefore, self-heating is negligible.

Figure 1. Logic diagram



 Pin 2 GND may be grounded or left floating (SOT323-5L only). For optimum thermal conductivity to the PC board ground plane, it should be grounded.

Table 1. Signal names

V _{CC}	Supply voltage
GND	Ground
V _{OUT}	Output voltage
NC	No connect

Figure 2. Connections (top view)



- 1. Pin 1 NC should be left floating or grounded.
- Pin 2 GND may be grounded or left floating. For optimum thermal conductivity to the PC board ground plane, it should be grounded.

Transfer function STLM20

2 Transfer function

The STLM20's transfer function can be described in different ways, with varying levels of precision. A simple linear transfer function, with good accuracy near 25 °C is expressed as:

Equation 1 (first order linear equation)

$$V_O = (-11.69 \text{mV})/^{\circ}\text{C} \times \text{T} + 1.8663 \text{V}$$

Over the specified operating temperature range, the best accuracy can be obtained by using the parabolic transfer function:

Equation 2 (second order parabolic equation)

$$V_{O} = (-3.88 \times 10^{-6} \times T^{2}) + (-1.15 \times 10^{-2} \times T) + 1.8639$$

and solving for T:

$$T = -1481.96 + \sqrt{2.1962 \times 10^6 + \frac{\left(1.8639 - V_O\right)}{3.88 \times 10^{-6}}}$$

The best fit linear transfer function for many popular temperature ranges was calculated in *Table 2*, where the error introduced by the linear transfer function increases with wider temperature ranges.

Table 2. First order equations optimized for different temperature ranges

Temperature range		Linear equation	Maximum deviation of linear equation from parabolic equation		
T _{min} (°C)	T _{max} (°C)	V _O =	(°C)		
- 55	130	-11.79 mV/°C * T + 1.8528 V	±1.41		
-40	110	-11.77 mV/°C * T + 1.8577 V	±0.93		
-30	100	-11.77 mV/°C * T + 1.8605 V	±0.70		
-40	85	-11.67 mV/°C * T + 1.8583 V	±0.65		
-10	65	-11.71 mV/°C * T + 1.8641 V	±0.23		
35	45	-11.81 mV/°C * T + 1.8701 V	±0.004		
20	30	-11.69 mV/°C * T + 1.8663 V	±0.004		

STLM20 Transfer function

Table 3. Second order parabolic equation ($V_{CC} = 2.7 \text{ V}$)

Parameter	Conditions	Min	Тур	Max	Unit
	T _A = -55 °C	2.457	2.485	2.512	
	T _A = -40 °C	2.292	2.318	2.343	
	T _A = -30 °C	2.181	2.205	2.230	
Temperature error based on:	T _A = -20 °C	2.069	2.092	2.116	V
$V_{OUT} = (-3.88 \times 10^{-6} \times T^2) + (-1.15 \times 10^{-2} \times T) + (-1.15 \times T) + (-1.15$	T _A = 0 °C	1.842	1.864	1.886	
1.8639 where T is the temperature	T _A = 25 °C	1.556	1.574	1.592	
	T _A = 50 °C	1.255	1.279	1.303	
	T _A = 85 °C	0.833	0.859	0.884	
	T _A = 130 °C	0.272	0.303	0.335	

Table 4. Second order parabolic equation for operation over the full voltage range $(V_{CC} = 2.4 \text{ V to } 5.5 \text{ V})$

Parameter ⁽¹⁾	Conditions	Min	Max	Unit
	$T_A = -55 {}^{\circ}C^{(2)(3)}$	2.457	2.531	
	$T_A = -40 {}^{\circ}C^{(2)}$	2.292	2.362	
Temperature error based on:	T _A = -30 °C	2.180	2.249	
	T _A = -20 °C	2.068	2.135	
$V_{OUT} = (-3.88 \times 10^{-6} \times T^2) + (-1.15 \times 10^{-2} \times T) +$	T _A = 0 °C	1.841	1.904	V
1.8639, where T is the temperature	T _A = 25 °C	1.555	1.610	
	T _A = 50 °C	1.254	1.322	
	T _A = 85 °C	0.832	0.903	
	T _A = 130 °C ⁽³⁾	0.271	0.353	

^{1.} V_{OUT} tolerance is ±4% (temperature grade 9 only).

^{2.} Valid for V_{CC} min = 2.7 V.

^{3.} Valid for temperature grade 7 only.

Maximum ratings STLM20

3 Maximum ratings

Stressing the device above the rating listed in the absolute maximum ratings table may cause permanent damage to the device. These are stress ratings only and operation of the device at these or any other conditions above those indicated in the operating sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Table 5. Absolute maximum ratings

Symbol	Parameter	Value	Unit	
T _{STG}	Storage temperature		-65 to +150	°C
T _{SLD} ⁽¹⁾	Lead solder temperature for 10 sec	conds	260	°C
V _O	Output voltage		-0.3 to V _{CC} + 0.3	V
V _{CC}	Supply voltage	-0.2 to 6.5	V	
Io	Output current	10	mA	
T _{J(max)}	Maximum junction temperature		150	°C
Δ	Thermal resistance	SOT323-5L	331.4	°C/W
θ_{JA}	Thermal resistance	UDFN-4L	160.2	°C/W
T _A ⁽²⁾	Ambient operating temperature	Grade 7	-55 to 130	°C
'A` ′	Ambient operating temperature	Grade 9	-40 to 85	°C

^{1.} Reflow at peak temperature of 260 °C. The time above 255 °C must not exceed 30 seconds.

^{2.} Grade 7: STLM20W87F Grade 9: STLM20DD9F

4 DC and AC characteristics

This section summarizes the DC and AC characteristics of the device. The parameters in the DC and AC characteristics table that follows are derived from tests performed under the test conditions. Designers should check that the operating conditions in their circuit match the operating conditions when relying on the quoted parameters.

Table 6. DC and AC characteristics

Sym	Description	Test condition ⁽¹⁾	Min	Typ ⁽²⁾	Max	Unit
		$T_A = -30 ^{\circ}\text{C} \text{ to } 130 ^{\circ}\text{C}$	2.4		5.5	V
V _{CC}	Supply voltage	T _A = -55 °C to 130 °C or -40 °C to 85 °C	2.7		5.5	V
V _O	Output voltage	T _A = 0 °C		1.8639		V
		T _A = 125 °C to 130 °C			±2.5	°C
	Second order parabolic equation	T _A = 80 °C to 85 °C		±1.00	±2.1	°C
	Temperature to voltage error ⁽³⁾	T _A = 25 °C to 30 °C		±0.5	±1.5	°C
	$V_O = (-3.88 \times 10^{-6} * T^2) + (-1.15 \times 10^{-2} * T) + 1.8639 V$	T _A = 0 °C		±0.75	±1.9	°C
	(-1.15 x 10 1) + 1.8639 V	T _A = -40 °C			±2.3	°C
		T _A = -55 °C			±2.5	°C
IQ	Quiescent current	2.4 V ≤ V _{CC} ≤ 5.5 V		4.8	8	μΑ
	Sensor gain (temperature sensitivity or average slope), V _O = -11.77 mV/°C * T + 1.860 V	-30 °C ≤ T _A ≤ 100 °C	-11.4	-11.77	-12.2	mV/°C
	Non-linearity	-20 °C ≤ T _A ≤ 80 °C		±0.4		%
ΔI_{Q}	Change of quiescent current	2.4 V ≤ V _{CC} ≤ 5.5 V		0.7		μΑ
T _C V _O	Temperature coefficient of quiescent current			-11		nA/°C
I _{SD}	Shutdown current	V _{CC} ≤ 0.8 V		0.02		μΑ
Z _O	Output impedance	$0 \mu A \le I_L \le 16 \mu A^{(4)(5)}$			160	Ω
RegL	Load regulation ⁽⁶⁾	υ μΑ ≤ Ι <u>Γ</u> ≤ 10 μΑ ^{. Α. Α}			-2.5	mV
Regl1	Line regulation	$2.4 \text{ V} \le \text{V}_{\text{CC}} \le 5.0 \text{ V}$			3.3	mV/V
Regl2	Tenie regulation	5.0 V ≤ V _{CC} ≤ 5.5 V			11	mV

^{1.} Valid for ambient operating temperature: $T_A = -55$ to 130 °C or $T_A = -40$ to 85 °C; $V_{CC} = 2.7$ V (except where noted).

^{2.} $T_J = T_A = 25$ °C, unless otherwise noted under test condition.

Accuracy is defined as the error between the measured and calculated output voltage at specified conditions of voltage, current, and temperature.

Positive load currents flow out of the device (sourcing) and can typically be 16 μA. For negative load currents (sinking), the
device can sink less than 1 μA.

^{5.} Over the supply range of 2.4 to 5.5 V.

Measured at constant junction temperature, with pulse testing and low duty cycle. Output changes due to heating may be calculated by multiplying internal dissipation by thermal resistance.

Capacitive load STLM20

5 **Capacitive load**

The STLM20 will handle capacitive loads of up to 300 pF. Over the specified temperature range, the STLM20 has a maximum output impedance of 160 Ω .

In a noisy environment, it may be advisable to add some filtering to minimize noise in the output voltage. A 0.1 µF capacitor added between the supply voltage and ground is recommended.

In an extremely noisy environment, it may be necessary to add a low-pass filter network to the output of the device. A 1 µF capacitor, in addition to the output impedance of the device, and a 200 Ω series resistor, will provide a low-pass filter that will pass the slow thermal time constant of the STLM20, while filtering the higher frequency noise.

 $0.1 \mu F$ $0.1 \mu F$ **Bypass Capacitor Bypass Capacitor** GND GND CFILTER : 2 CFILTER : R_{FILTER} ≶ C_L C_{L} Al12259 AI12260

Figure 3. Filter network for noisy environments or capacitive loads > 300 pF

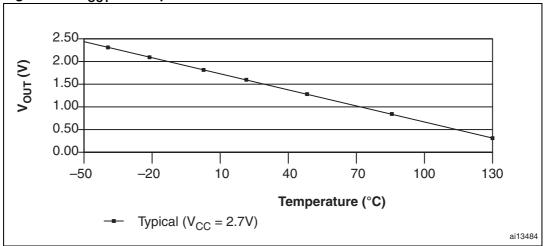
Resistor/capacitor combinations for the filter network Table 7.

R _{FILTER}	C _{FILTER}
200 Ω	1 μF
470 Ω	0.1 μF
680 Ω	0.01 μF
1000 Ω	1000 pF
10 kΩ	100 pF
100 kΩ	10 pF

6 Typical operating characteristics

The graph shown in *Figure 4* represents V_{OUT} according to temperature.

Figure 4. V_{OUT} vs. temperature



7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

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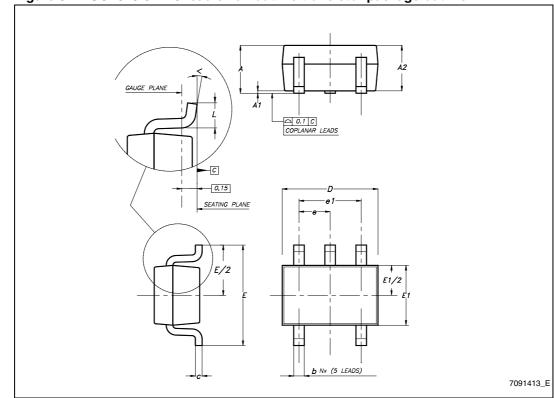


Figure 5. SOT323-5L – 5-lead small outline transistor package outline

1. Drawing is not to scale.

Table 8. SOT323-5L – 5-lead small outline transistor package mechanical data

Symbol		mm			inches	
Symbol	Тур	Min	Max	Тур	Min	Max
Α	-	0.80	1.10	-	0.031	0.043
A1	_	0	0.10	_	0.000	0.004
A2	0.90	0.80	1.00	0.035	0.031	0.039
b	_	0.15	0.30	_	0.006	0.012
С	_	0.10	0.22	_	0.004	0.009
D	2.00	1.80	2.20	0.079	0.071	0.087
Е	2.10	1.80	2.40	0.083	0.071	0.094
E1	1.25	1.15	1.35	0.049	0.045	0.053
е	0.65	_	_	0.026	-	_
e1	1.30	-	-	0.051	-	_
L	0.36	0.26	0.46	0.014	0.010	0.018
<	_	0°	8°	_	0°	8°

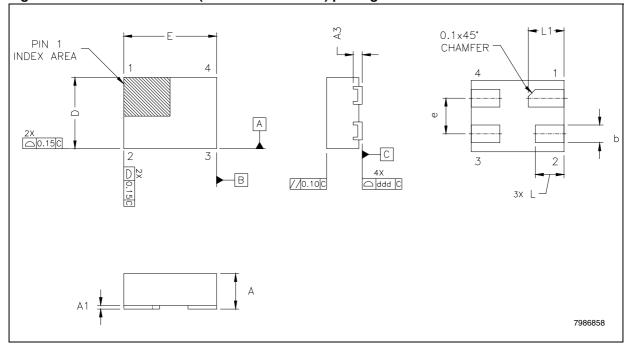


Figure 6. UDFN-4L - 4-lead (1.00 mm x 1.30 mm) package outline

Note: Drawing is not to scale.

Table 9. UDFN-4L - 4-lead (1.00 mm x 1.30 mm) package mechanical data

Cumbal		mm			inches	
Symbol	Min	Тур	Max	Min	Тур	Max
А	0.45	0.50	0.55	0.018	0.020	0.022
A1	0	0.025	0.05	0	0.001	0.002
A3	0.119	0.127	0.177	0.0046	0.0050	0.0069
b	0.20	0.25	0.30	0.008	0.010	0.012
D	0.95	1.00	1.05	0.037	0.039	0.041
E	1.25	1.30	1.35	0.049	0.051	0.053
е	0.45	0.50	0.55	0.018	0.020	0.022
L	0.35	0.40	0.45	0.014	0.016	0.018
L1	0.45	0.50	0.55	0.018	0.020	0.022
ddd	0	0.04	0.08	0	0.0016	0.0031

TOP COVER TAPE

CENTER LINES OF CAVITY

USER DIRECTION OF FEED

AM03073v1

Figure 7. Carrier tape for SOT323-5L and UDFN-4L packages

Table 10. Carrier tape dimensions for SOT323-5L and UDFN-4L packages

Package	W	D	E	P ₀	P ₂	F	A ₀	B ₀	K ₀	P ₁	Т	Unit	Bulk Qty
SOT323-5L	8.00 +0.30 -0.10	1.50 +0.10/ -0.00	1.75 ±0.10	4.00 ±0.10	2.00 ±0.10	3.50 ±0.05	2.25 ±0.10	2.40 ±0.10	1.22 ±0.10	4.00 ±0.10	0.30 ±0.05	mm	3000
UDFN-4L	8.00 +0.30 -0.10	1.50 +0.10/ -0.00	1.75 ±0.10	4.00 ±0.10	2.00 ±0.10	3.50 ±0.05	1.25 ±0.10	1.55 ±0.10	0.65 ±0.05	4.00 ±0.10	0.250 ±0.05	mm	3000

Figure 8. Reel schematic

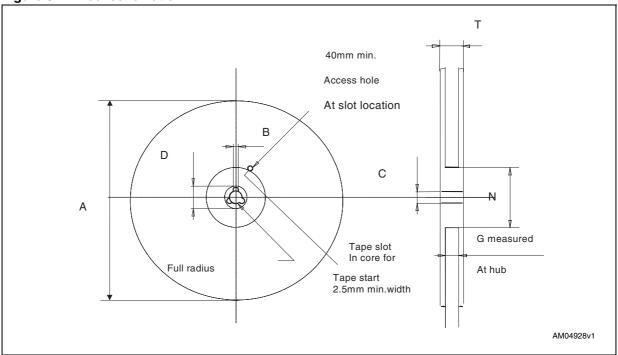


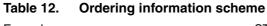
Table 11. Reel dimensions for 8 mm carrier tape - SOT323-5L and UDFN-4L packages

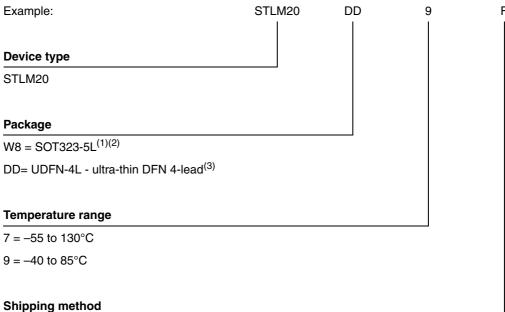
A (max)	B (min)	С	D (min)	N (min)	G	T (max)
180 mm (7-inch)	1.5 mm	13 mm ± 0.2 mm	20.2 mm	60 mm	8.4 mm + 2/–0 mm	14.4 mm

Note: The dimensions given in Table 11 incorporate tolerances that cover all variations on critical parameters.

STLM20 Part numbering

8 Part numbering





F = ECOPACK® package, tape & reel

- 1. Available in temperature grade 7 (-55 to 130 °C) only
- 2. SOT323-5L also referred to as SC70-5L
- 3. Available in temperature grade 9 (-40 to 85 °C) only

Table 13. Marking description

Part number	Package	Marking
STLM20W87F	SOT323-5L	M20
STLM20DD9F	UDFN-4L	20

For other options, or for more information on any aspect of this device, please contact the ST sales office nearest you.

Revision history STLM20

9 Revision history

Table 14. Document revision history

Date	Revision	Changes		
28-Jun-2006	1	Initial release.		
19-Jul-2006	2	Added Table 13: Marking description		
28-Aug-2006	3	Added a footnote concerning package availability in <i>Features on page 1</i> and to <i>Table 12</i> and <i>Table 13</i> ; updated package mechanical data in <i>Table 9</i>		
05-Sep-2006	4	Amended text in the Features on page 1, Section 1: Description, Section 2: Transfer function, Table 6, and Table 12 to elucidate that two packages exist each with specific temperature ranges: SOT323-5 (SC70-5) 5-lead package (–55 to 130 °C) and UDFN 4-lead package (–40 to 85 °C)		
25-Sep-2006	5	Updated operating temperature ranges (now 7 and 9); updated Table 6: DC and AC characteristics		
10-Oct-2006	6	Table 3: Second order parabolic equation ($V_{CC} = 2.7 \text{ V}$) and Table 4: Second order parabolic equation for operation over the full voltage range ($V_{CC} = 2.4 \text{ V}$ to 5.5 V) added. Section 6: Typical operating characteristics added. T _A added to Table 5: Absolute maximum ratings.		
04-Dec-2006	7	Document status updated to full datasheet; updated text in the title of Table 4: Second order parabolic equation for operation over the full voltage range ($V_{CC} = 2.4 \text{ V to } 5.5 \text{ V}$); added footnotes regarding temperature ranges in Table 12: Ordering information scheme.		
08-Feb-2007	8	Added thermal resistance in Table 5: Absolute maximum ratings.		
09-May-2007	9	Modified Equation 1, 2.		
26-Nov-2007	10	Updated footnote 2 in <i>Table 5</i> ; minor text updates.		
07-Nov-2008	11	Updated Figure 6: UDFN-4L – 4-lead (1.00 mm x 1.30 mm) package outline to reflect location of pin 1 with respect to the orientation of the topside marking; updated Figure 5, Table 5, 8, 12.		
10-Sep-2009	12	Updated <i>Table 5</i> , text in <i>Section 7: Package mechanical data</i> ; added tape and reel specifications (<i>Figure 7</i> and <i>Table 10</i>); reformatted document.		
09-Jul-2010	13	Updated Features, Section 1: Description, Section 2: Transfer function, Table 5, 6, 12; added reel information (Figure 8, Table 11); removed references to SC70-5L package; minor textual changes.		

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