

## VS-STPS20L15DPbF, VS-STPS20L15D-N3

Vishay Semiconductors

## Schottky Rectifier, 20 A

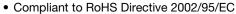
Anode

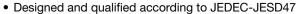


PRODUCT SUMMARY				
Package	TO-220AC			
I <sub>F(AV)</sub>	20 A			
$V_{R}$	15 V			
V <sub>F</sub> at I <sub>F</sub>	See Electrical table			
I <sub>RM</sub> max.	600 mA at 100 °C			
T <sub>J</sub> max.	125 °C			
Diode variation	Single die			
E <sub>AS</sub>	10 mJ			

#### **FEATURES**

- 125 °C T<sub>J</sub> operation (V<sub>R</sub> < 5 V)</li>
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance





 Halogen-free according to IEC 61249-2-21 definition (-N3 only)







FREE

### **DESCRIPTION**

The Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	20	Α			
V <sub>RRM</sub>		15	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	700	Α			
V <sub>F</sub>	19 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (typical)	0.25	V			
T <sub>J</sub>	Range	- 55 to 125	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-STPS20L15DPbF	VS-STPS20L15D-N3	UNITS		
Maximum DC reverse voltage	$V_{R}$	15	15	V		
Maximum working peak reverse voltage	$V_{RWM}$	15	15	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS			
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle, T <sub>C</sub> = 85 °C, rectangular waveform		20	А	
Maximum peak one cycle non-repetitive surge current	l	5 μs sine or 3 μs rect. pulse Following any rated load condition and with rated		700	А	
See fig. 7	Termination of the state of the			330		
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 2 A, L = 6 mH		10	mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2	Α	



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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TYP.	MAX.	UNITS		
		19 A	T <sub>.1</sub> = 25 °C	-	0.41	V	
Forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	40 A	1J=25 C	-	0.52		
See fig. 1	V FM (1)	19 A	T 105 °C	0.25	0.33	]	
		40 A	T <sub>J</sub> = 125 °C	0.37	0.50		
Reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V Data d V	-	10	mA	
See fig. 2	'RM '''	T <sub>J</sub> = 100 °C	V <sub>R</sub> = Rated V <sub>R</sub>	-	600	IIIA	
Threshold voltage	V <sub>F(TO)</sub>	T T mayimum		0.1	82	V	
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum	7	.6	mΩ		
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal ran	-	2000	pF		
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 n	8	-	nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>			000	V/µs	

### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature range	TJ		- 55 to 125	°C		
Maximum storage temperature range	T <sub>Stg</sub>		- 55 to 150	°C		
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation See fig. 4  1.5				
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased (for TO-220)	0.50	°C/W		
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation (for D <sup>2</sup> PAK)	40			
Approximate weight			2	g		
Approximate weight			0.07	OZ.		
Mounting torque	n	Non-lubricated threads	6 (5)	kgf · cm		
Mounting torque ————————————————————————————————————	n	NOTI-TUDITICATED THEADS	12 (10)	(lbf ⋅ in)		
Marking device Case style TO-220AC STPS2		20L15D				

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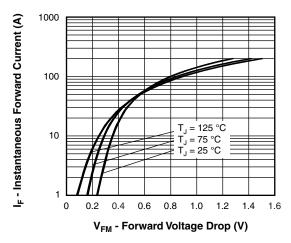


Fig. 1 - Maximum Forward Voltage Drop Characteristics

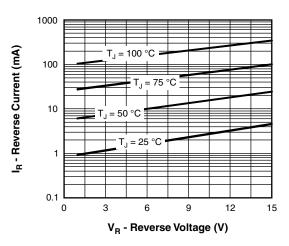


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

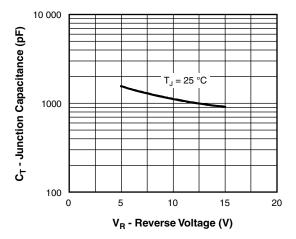


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

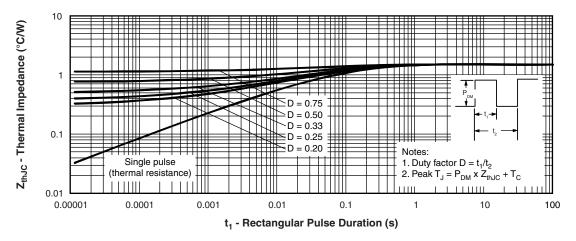


Fig. 4 - Maximum Thermal Impedance  $Z_{\text{thJC}}$  Characteristics

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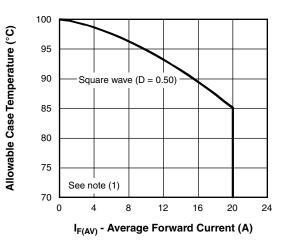


Fig. 5 - Maximum Allowable Case Temperature vs.

Average Forward Current

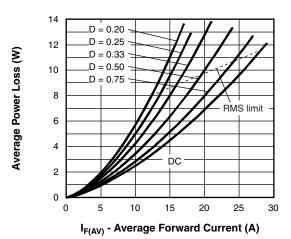


Fig. 6 - Forward Power Loss Characteristics

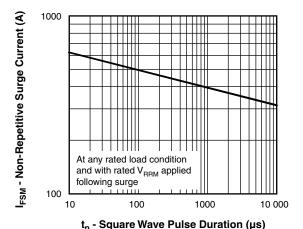


Fig. 7 - Maximum Non-Repetitive Surge Current

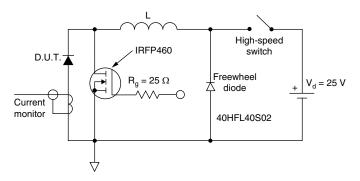


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

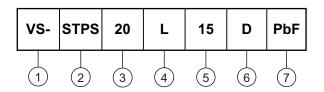
 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{th,JC}}; \\ \text{Pd} & = \text{Forward power loss} = \text{I}_{F(AV)} \times \text{V}_{FM} \text{ at } (\text{I}_{F(AV)}/D) \text{ (see fig. 6)}; \\ \text{Pd}_{REV} & = \text{Inverse power loss} = \text{V}_{R1} \times \text{I}_{R} \text{ (1 - D); I}_{R} \text{ at V}_{R1} = 80 \% \text{ rated V}_{R} \\ \end{array}$ 

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### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Schottky STPS series

Current rating (20 = 20 A)

4 - L = Low voltage drop

**5** - Voltage rating (15 = 15 V)

D = Essential part number

Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-STPS20L15DPbF	50	1000	Antistatic plastic tube			
VS-STPS20L15D-N3	50	1000	Antistatic plastic tube			

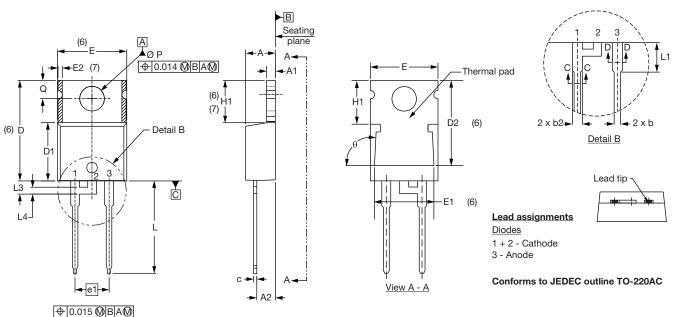
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95221</u>				
Part marking information	TO-220AC PbF	www.vishay.com/doc?95224		
	TO-220AC -N3	www.vishay.com/doc?95068		
SPICE model		www.vishay.com/doc?95305		



### Vishay Semiconductors

### **TO-220AC**

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS INCHES		NOTES		
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIM	IETERS	INCHES		NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° t	o 93°	90° t	o 93°	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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