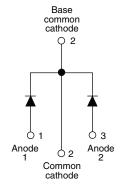


### VS-STPS30L60CWPbF, VS-STPS30L60CW-N3

Vishay Semiconductors

### Schottky Rectifier, 2 x 15 A



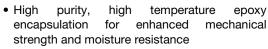


TO-247AC
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PRODUCT SUMMARY					
Package	TO-247AC				
I <sub>F(AV)</sub>	2 x 15 A				
$V_{R}$	60 V				
V <sub>F</sub> at I <sub>F</sub>	0.56 V				
I <sub>RM</sub> max.	100 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	13 mJ				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Very low forward voltage drop
- High frequency operation





- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### **DESCRIPTION**

The VS-STPS30L60CW... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS VALUES						
I <sub>F(AV)</sub>	Rectangular waveform	30	Α				
$V_{RRM}$		60	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 µs sine	1020	Α				
V <sub>F</sub>	15 Apk, T <sub>J</sub> = 125 °C (per leg)	0.56	V				
TJ		- 55 to 150	°C				

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-STPS30L60CWPbF	VS-STPS30L60CW-N3	UNITS			
Maximum DC reverse voltage	$V_{R}$	60	60	V			
Maximum working peak reverse voltage	$V_{RWM}$	00	00	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS		
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 112 °C	duty cycle at T <sub>C</sub> = 112 °C, rectangular waveform				
Maximum peak one cycle non-repetitive surge current per leg	1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1020	Α		
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	265			
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.50 A, L = 11.5 mH		13	mJ		
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5$ x $V_R$ typical		1.50	Α		



# VS-STPS30L60CWPbF, VS-STPS30L60CW-N3

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		15 A	T <sub>.1</sub> = 25 °C	0.60	V	
Maximum forward voltage drop per leg	V (1)	30 A	1J=25 C	0.80		
See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	15 A	T 105 °C	0.56		
		30 A	T <sub>J</sub> = 125 °C	0.70		
	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		0.48		
Maximum reverse leakage current per leg See fig. 2		T 405.00	V <sub>R</sub> = Rated V <sub>R</sub>	50 (typical)	mA	
See lig. 2		T <sub>J</sub> = 125 °C		100		
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		720	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		7.5	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs		

#### Note

 $<sup>^{(1)}</sup>$  Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C		
Maximum thermal resistance, junction to case per leg		В	DC operation See fig. 4	2.20			
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.10	°C/W		
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.24			
Approximate weight				6	g		
Approximate weight				0.21	OZ.		
	minimum		Non-lubricated threads	6 (5)	kgf · cm		
Mounting torque	maximum		Non-lubilicated tilleads	12 (10)	(lbf $\cdot$ in)		
Marking device			Case style TO-247AC (JEDEC)	STPS30	L60CW		

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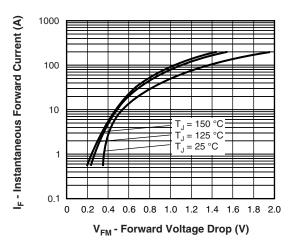


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

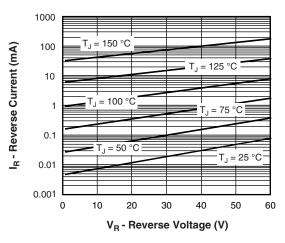


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

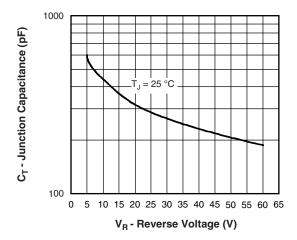


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

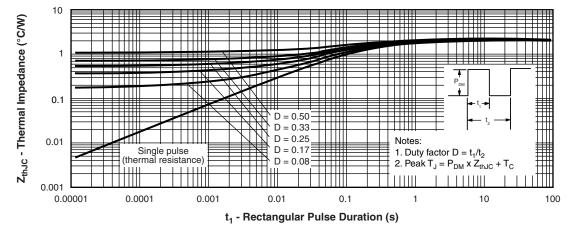


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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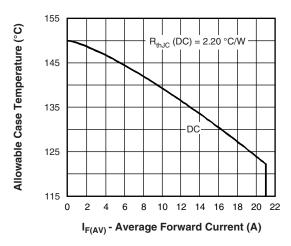


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

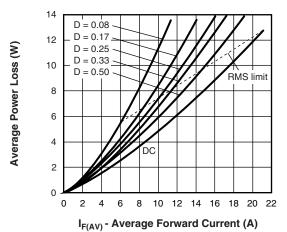


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

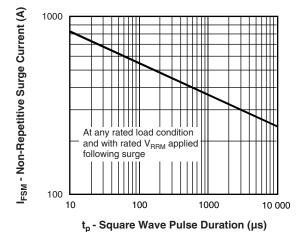


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

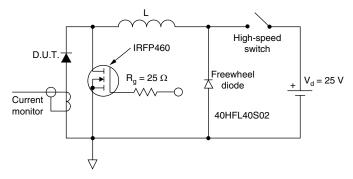


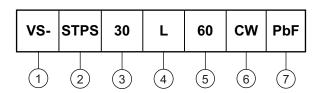
Fig. 8 - Unclamped Inductive Test Circuit

## VS-STPS30L60CWPbF, VS-STPS30L60CW-N3

Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

Schottky STPS series

- Current ratings (30 = 30 A)

- L = Low forward voltage

5 - Voltage code (60 = 60 V)

6 - Package:

CW = TO-247

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

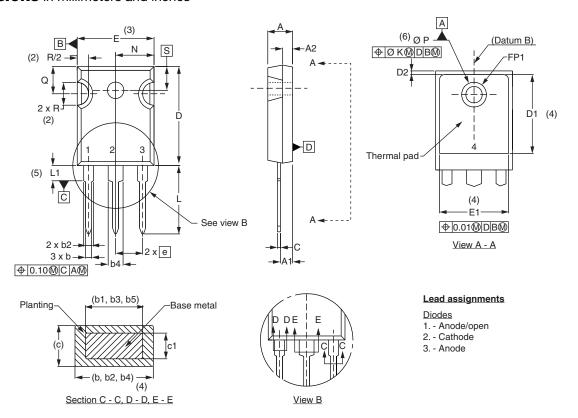
ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-STPS30L60CWPbF	25	500	Antistatic plastic tube				
VS-STPS30L60CW-N3	25	500	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95223</u>					
Dort marking information	TO-247AC PbF	www.vishay.com/doc?95226			
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007			



#### Vishay Semiconductors

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	ITOTES	
D2	0.51	1.30	0.020	0.051		
E	15.29	15.87	0.602	0.625	3	
E1	13.72	-	0.540	-		
е	5.46	BSC	0.215	BSC		
FK	2.	2.54		)10		
L	14.20	16.10	0.559	0.634		
L1	3.71	4.29	0.146	0.169		
N	7.62	7.62 BSC		0.3		
ΦР	3.56	3.66	0.14	0.144		
ФР1	-	6.98	-	0.275		
Q	5.31	5.69	0.209	0.224		
R	4.52	5.49	1.78	0.216		
S	5.51	BSC	0.217	'BSC		

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



### **Legal Disclaimer Notice**

Vishay

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