

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY		
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^b
30	0.0095 at $V_{GS} = 10$ V	63 ^b
	0.014 at $V_{GS} = 4.5$ V	52 ^b

FEATURES

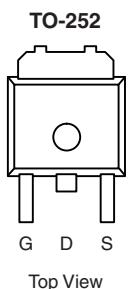
- TrenchFET® Power MOSFET
- Optimized for High- or Low-Side
- 100 % R_g Tested



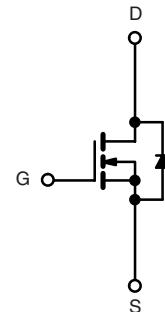
RoHS*
COMPLIANT

APPLICATIONS

- DC/DC Converters
- Synchronous Rectifiers



Drain Connected to Tab



Ordering Information: SUD50N03-09P
SUD50N03-09P-E3 (Lead (Pb)-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V_{DS}	30	V	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current ^a	I_D	63 ^b	A	
		44.5 ^b		
Pulsed Drain Current	I_{DM}	50		
Continuous Source Current (Diode Conduction) ^a	I_S	5		
Avalanche Current	I_{AS}	35	mJ	
Single Pulse Avalanche Energy	E_{AS}	61		
Maximum Power Dissipation	P_D	65.2	W	
		7.5 ^a		
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R_{thJA}	16	20	°C/W	
		40	50		
Maximum Junction-to-Case	R_{thJC}	1.8	2.3		

Notes:

- Surface Mounted on FR4 board, $t \leq 10$ s.
- Based on maximum allowable Junction Temperature, package limitation current is 50 A.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0 \text{ V}$, $I_D = 250 \mu\text{A}$	30			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}$, $I_D = 250 \mu\text{A}$	1.0		3.0	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0 \text{ V}$, $V_{\text{GS}} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 30 \text{ V}$, $V_{\text{GS}} = 0 \text{ V}$		1		μA
		$V_{\text{DS}} = 30 \text{ V}$, $V_{\text{GS}} = 0 \text{ V}$, $T_J = 125^\circ\text{C}$		50		
On-State Drain Current ^b	$I_{\text{D}(\text{on})}$	$V_{\text{DS}} = 5 \text{ V}$, $V_{\text{GS}} = 10 \text{ V}$	50			A
Drain-Source On-State Resistance ^b	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10 \text{ V}$, $I_D = 20 \text{ A}$		0.0076	0.0095	Ω
		$V_{\text{GS}} = 10 \text{ V}$, $I_D = 20 \text{ A}$, $T_J = 125^\circ\text{C}$			0.015	
		$V_{\text{GS}} = 4.5 \text{ V}$, $I_D = 20 \text{ A}$		0.0115	0.014	
Forward Transconductance ^b	g_{fs}	$V_{\text{DS}} = 15 \text{ V}$, $I_D = 20 \text{ A}$	20			S
Dynamic^a						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0 \text{ V}$, $V_{\text{DS}} = 25 \text{ V}$, $f = 1 \text{ MHz}$		2200		pF
Output Capacitance	C_{oss}			410		
Reverse Transfer Capacitance	C_{rss}			180		
Total Gate Charge ^c	Q_g	$V_{\text{DS}} = 15 \text{ V}$, $V_{\text{GS}} = 4.5 \text{ V}$, $I_D = 50 \text{ A}$		11	16	nC
Gate-Source Charge ^c	Q_{gs}			7.5		
Gate-Drain Charge ^c	Q_{gd}			5.0		
Gate Resistance	R_g		0.5	1.5	2.1	Ω
Turn-On Delay Time ^c	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 15 \text{ V}$, $R_L = 0.3 \Omega$ $I_D \cong 50 \text{ A}$, $V_{\text{GEN}} = 10 \text{ V}$, $R_g = 2.5 \Omega$		9	15	ns
Rise Time ^c	t_r			15	25	
Turn-Off Delay Time ^c	$t_{\text{d}(\text{off})}$			22	35	
Fall Time ^c	t_f			8	12	
Source-Drain Diode Ratings and Characteristic $T_C = 25^\circ\text{C}$						
Pulsed Current	I_{SM}				100	A
Diode Forward Voltage ^b	V_{SD}	$I_F = 50 \text{ A}$, $V_{\text{GS}} = 0 \text{ V}$		1.2	1.5	V
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = 50 \text{ A}$, $\text{di}/\text{dt} = 100 \text{ A}/\mu\text{s}$		35	70	ns

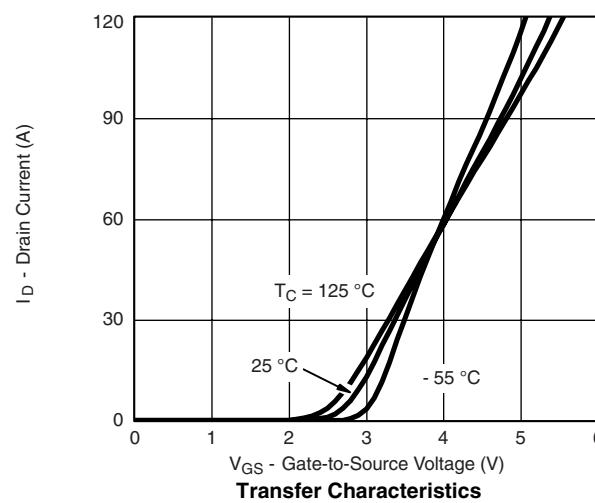
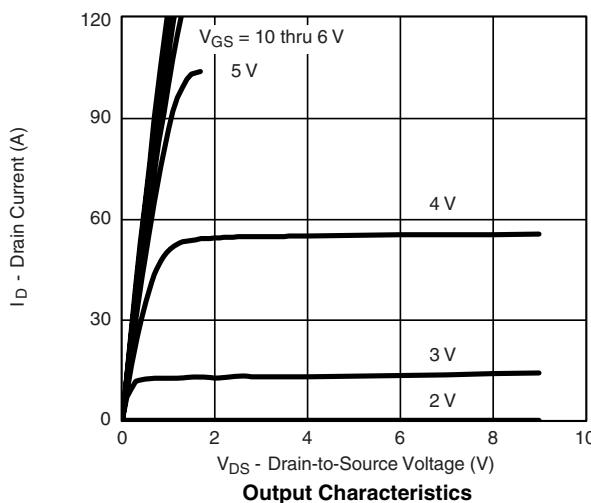
Notes:

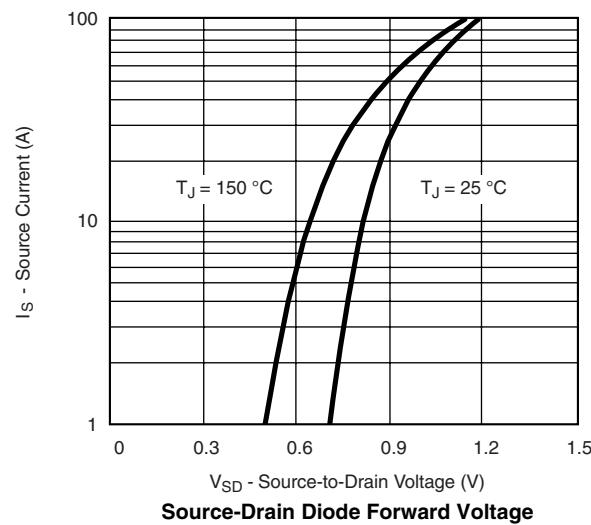
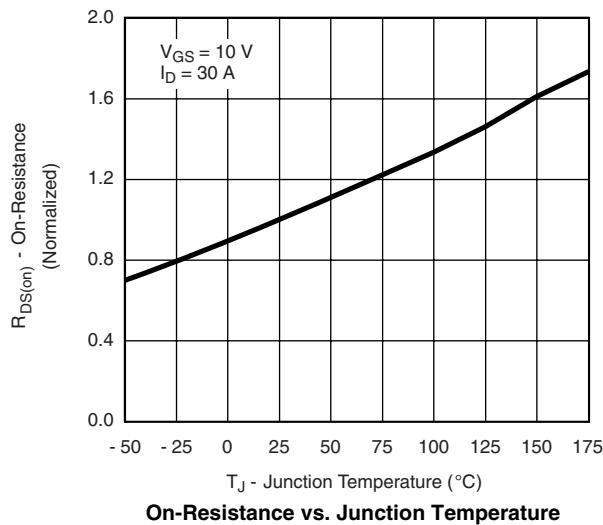
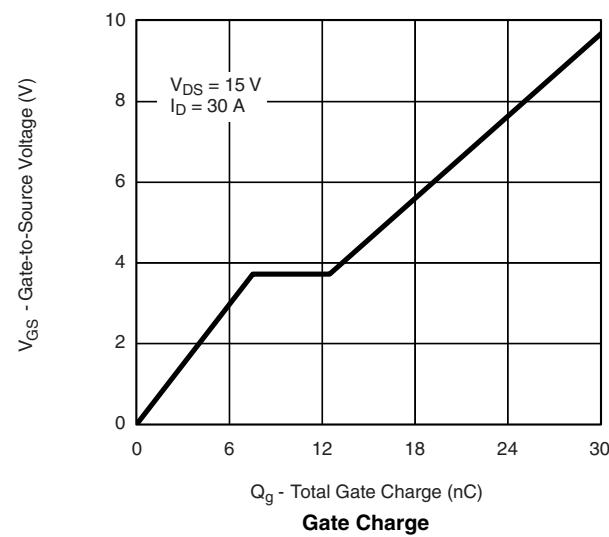
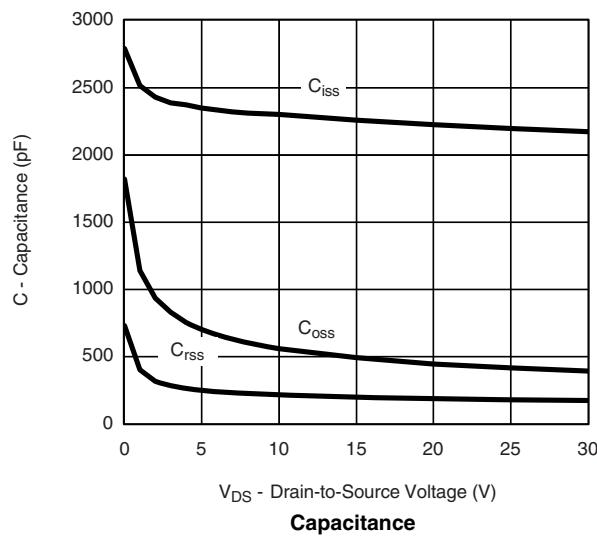
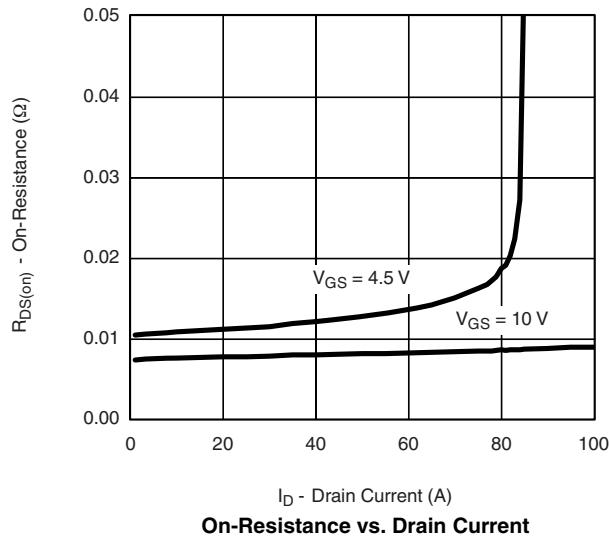
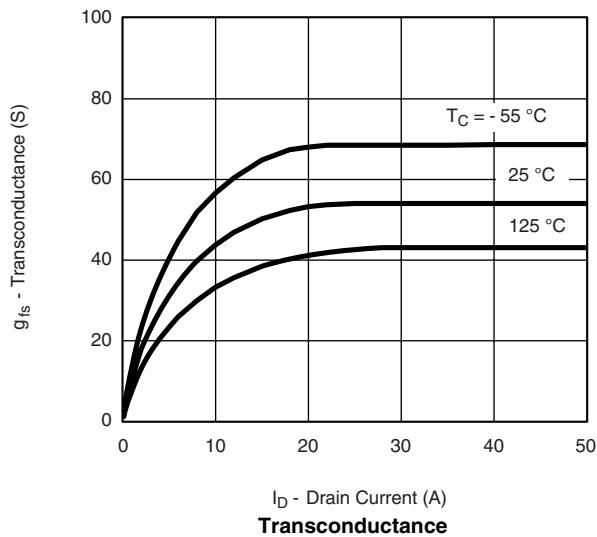
a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.

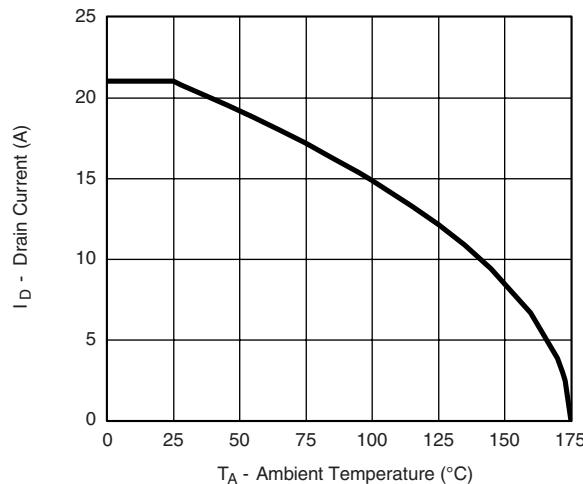
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

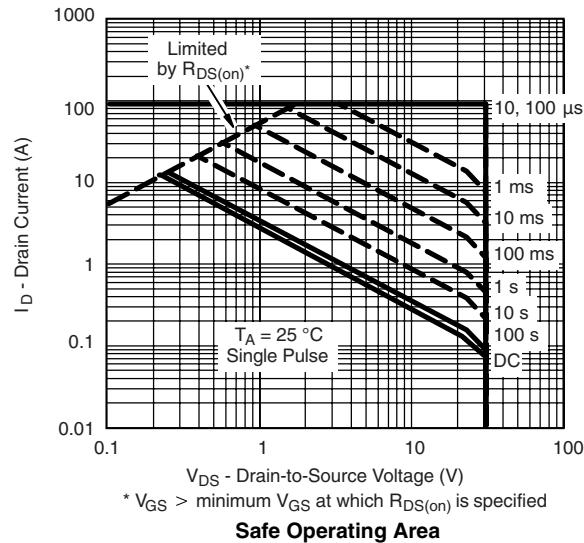
TYPICAL CHARACTERISTICS 25°C , unless otherwise noted


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Thermal Ratings

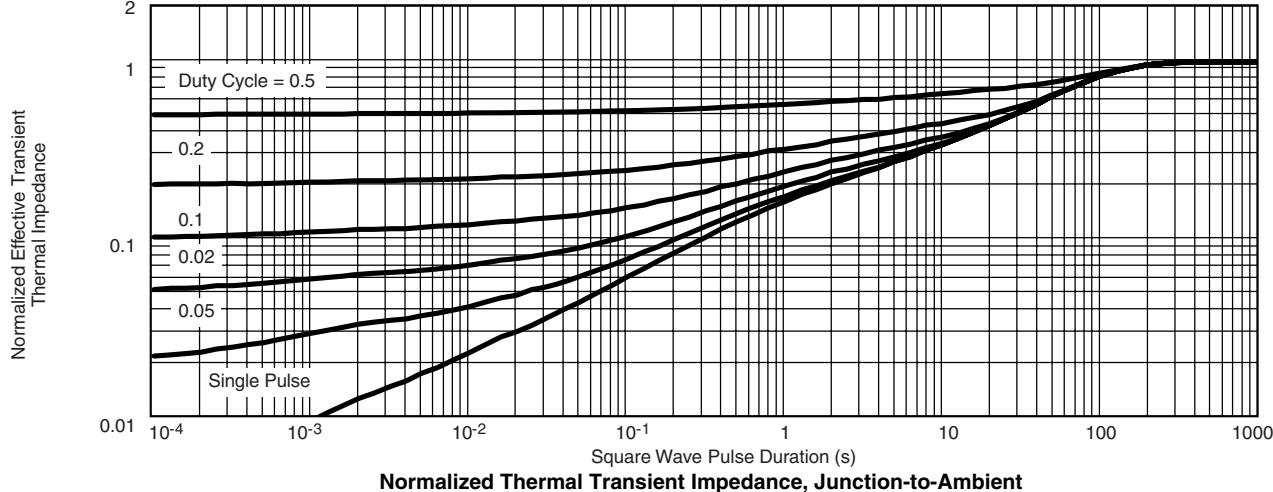


Maximum Drain Current vs. Ambient Temperature

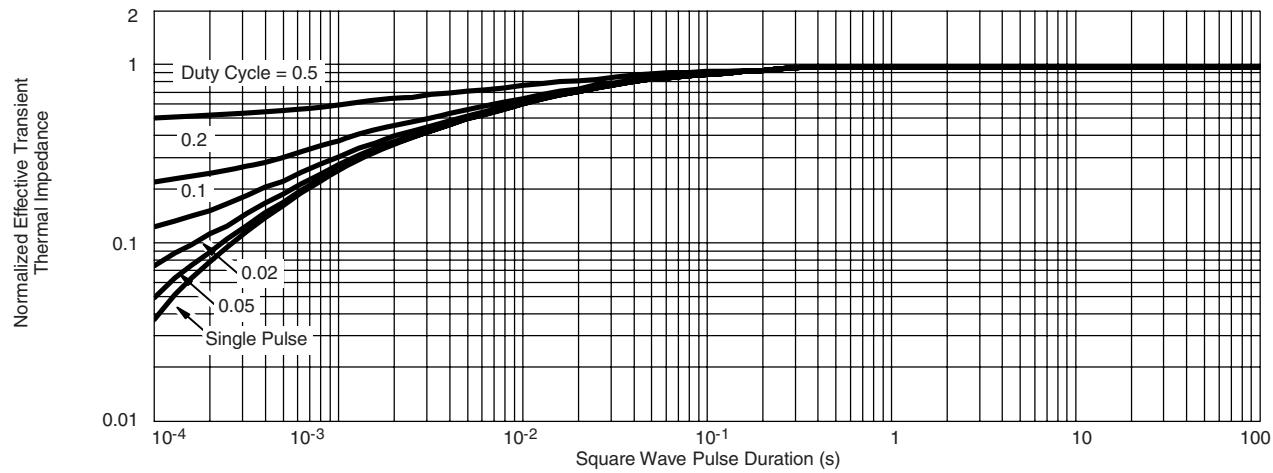


* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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