

N-Channel 30-V (D-S) MOSFET

| PRODUCT SUMMARY | | |
|-----------------|---------------------------|------------------------|
| V_{DS} (V) | $R_{DS(on)}$ (Ω) | I_D (A) ^b |
| 30 | 0.0095 at $V_{GS} = 10$ V | 63 ^b |
| | 0.014 at $V_{GS} = 4.5$ V | 52 ^b |

FEATURES

- TrenchFET® Power MOSFET
- Optimized for High- or Low-Side
- 100 % R_g Tested

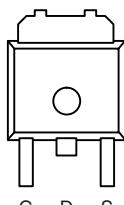


RoHS*
COMPLIANT

APPLICATIONS

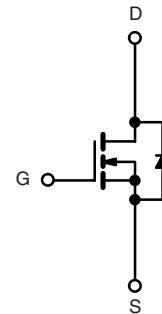
- DC/DC Converters
- Synchronous Rectifiers

TO-252



Drain Connected to Tab

Top View



Ordering Information: SUD50N03-09P
SUD50N03-09P-E3 (Lead (Pb)-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

| Parameter | Symbol | Limit | Unit |
|---|----------------|-------------------|------|
| Drain-Source Voltage | V_{DS} | 30 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | |
| Continuous Drain Current ^a | I_D | 63 ^b | |
| | | 44.5 ^b | |
| Pulsed Drain Current | I_{DM} | 50 | A |
| Continuous Source Current (Diode Conduction) ^a | I_S | 5 | |
| Avalanche Current | I_{AS} | 35 | |
| Single Pulse Avalanche Energy | E_{AS} | 61 | |
| Maximum Power Dissipation | P_D | 65.2 | W |
| | | 7.5 ^a | |
| Operating Junction and Storage Temperature Range | T_J, T_{stg} | - 55 to 175 | °C |

THERMAL RESISTANCE RATINGS

| Parameter | Symbol | Typical | Maximum | Unit |
|--|------------|---------|---------|------|
| Maximum Junction-to-Ambient ^a | R_{thJA} | 16 | 20 | °C/W |
| | | 40 | 50 | |
| Maximum Junction-to-Case | R_{thJC} | 1.8 | 2.3 | |

Notes:

a. Surface Mounted on FR4 board, $t \leq 10$ s.

b. Based on maximum allowable Junction Temperature, package limitation current is 50 A.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

| Parameter | Symbol | Test Conditions | Min. | Typ. ^a | Max. | Unit |
|---|-----------------------------|---|------|-------------------|-----------|---------------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | $V_{(\text{BR})\text{DSS}}$ | $V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$ | 30 | | | V |
| Gate Threshold Voltage | $V_{\text{GS}(\text{th})}$ | $V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$ | 1.0 | | 3.0 | |
| Gate-Body Leakage | I_{GSS} | $V_{\text{DS}} = 0 \text{ V}, V_{\text{GS}} = \pm 20 \text{ V}$ | | | ± 100 | nA |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 0 \text{ V}$ | | 1 | | μA |
| | | $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 0 \text{ V}, T_J = 125^\circ\text{C}$ | | 50 | | |
| On-State Drain Current ^b | $I_{\text{D}(\text{on})}$ | $V_{\text{DS}} = 5 \text{ V}, V_{\text{GS}} = 10 \text{ V}$ | 50 | | | A |
| Drain-Source On-State Resistance ^b | $R_{\text{DS}(\text{on})}$ | $V_{\text{GS}} = 10 \text{ V}, I_D = 20 \text{ A}$ | | 0.0076 | 0.0095 | Ω |
| | | $V_{\text{GS}} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125^\circ\text{C}$ | | | 0.015 | |
| | | $V_{\text{GS}} = 4.5 \text{ V}, I_D = 20 \text{ A}$ | | 0.0115 | 0.014 | |
| Forward Transconductance ^b | g_{fs} | $V_{\text{DS}} = 15 \text{ V}, I_D = 20 \text{ A}$ | 20 | | | S |
| Dynamic^a | | | | | | |
| Input Capacitance | C_{iss} | $V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 25 \text{ V}, f = 1 \text{ MHz}$ | | 2200 | | pF |
| Output Capacitance | C_{oss} | | | 410 | | |
| Reverse Transfer Capacitance | C_{rss} | | | 180 | | |
| Total Gate Charge ^c | Q_g | $V_{\text{DS}} = 15 \text{ V}, V_{\text{GS}} = 4.5 \text{ V}, I_D = 50 \text{ A}$ | | 11 | 16 | nC |
| Gate-Source Charge ^c | Q_{gs} | | | 7.5 | | |
| Gate-Drain Charge ^c | Q_{gd} | | | 5.0 | | |
| Gate Resistance | R_g | | 0.5 | 1.5 | 2.1 | Ω |
| Turn-On Delay Time ^c | $t_{\text{d}(\text{on})}$ | $V_{\text{DD}} = 15 \text{ V}, R_L = 0.3 \Omega$ $I_D \cong 50 \text{ A}, V_{\text{GEN}} = 10 \text{ V}, R_g = 2.5 \Omega$ | | 9 | 15 | ns |
| Rise Time ^c | t_r | | | 15 | 25 | |
| Turn-Off Delay Time ^c | $t_{\text{d}(\text{off})}$ | | | 22 | 35 | |
| Fall Time ^c | t_f | | | 8 | 12 | |
| Source-Drain Diode Ratings and Characteristic $T_C = 25^\circ\text{C}$ | | | | | | |
| Pulsed Current | I_{SM} | | | | 100 | A |
| Diode Forward Voltage ^b | V_{SD} | $I_F = 50 \text{ A}, V_{\text{GS}} = 0 \text{ V}$ | | 1.2 | 1.5 | V |
| Source-Drain Reverse Recovery Time | t_{rr} | $I_F = 50 \text{ A}, \text{di/dt} = 100 \text{ A}/\mu\text{s}$ | | 35 | 70 | ns |

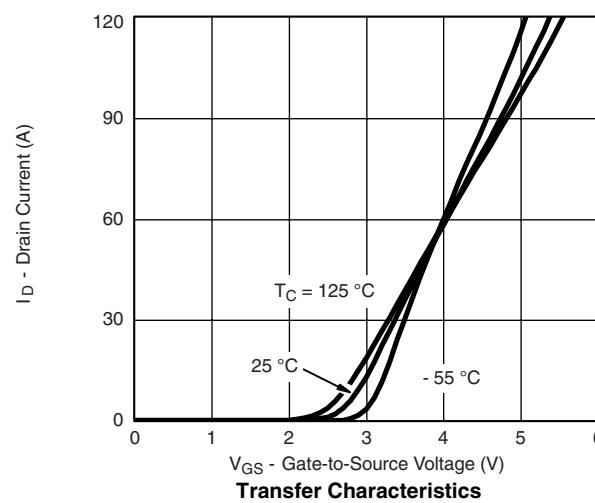
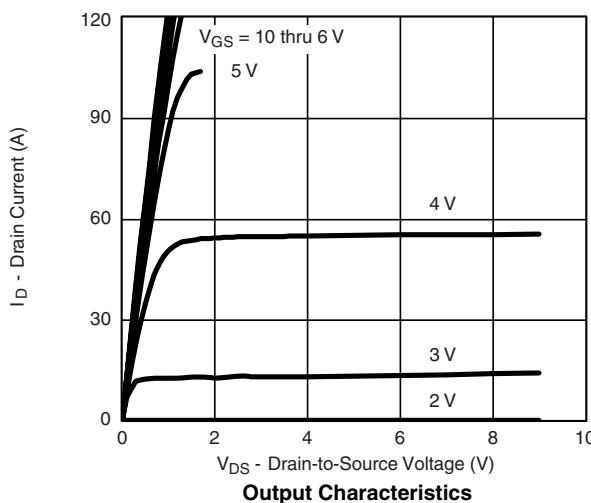
Notes:

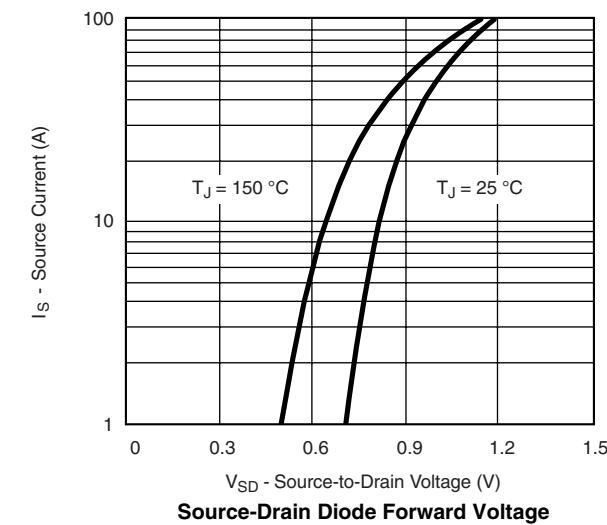
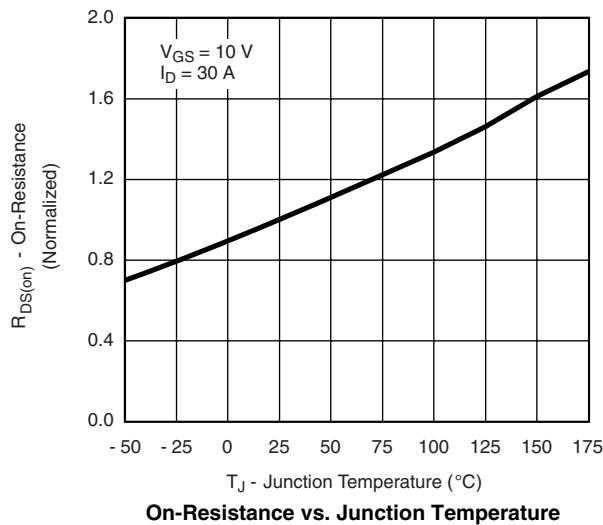
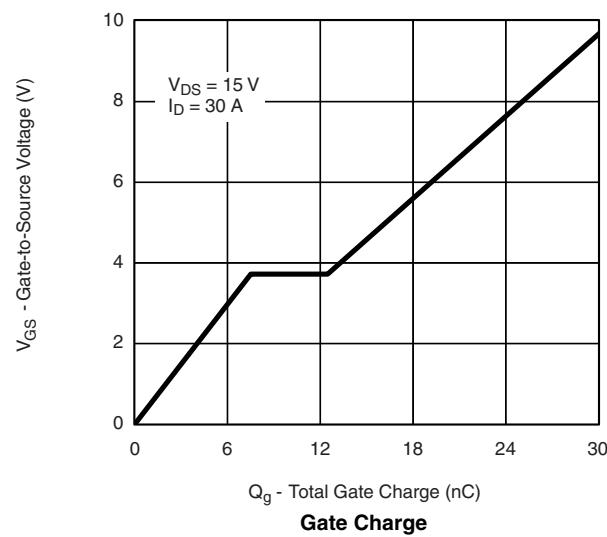
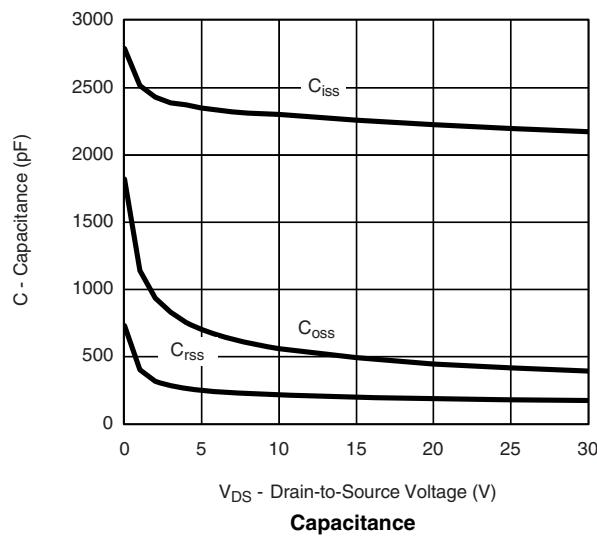
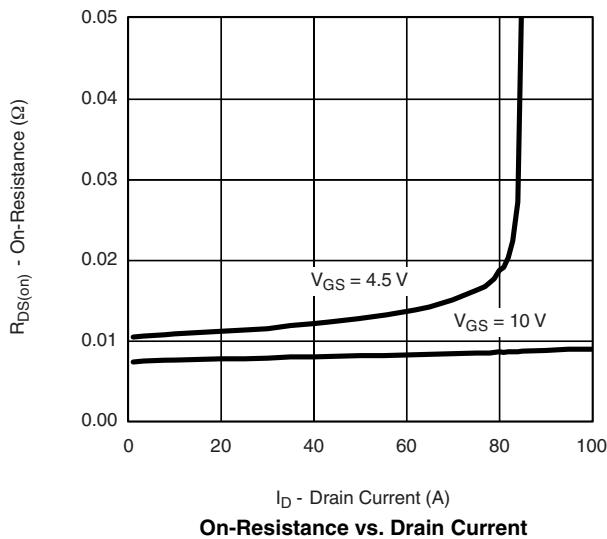
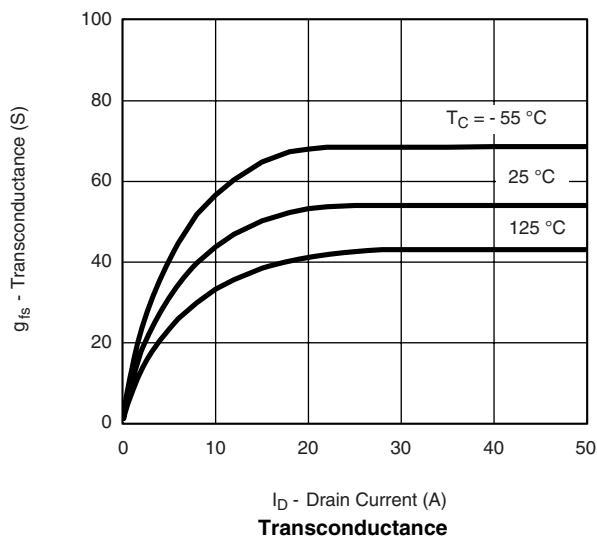
a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.

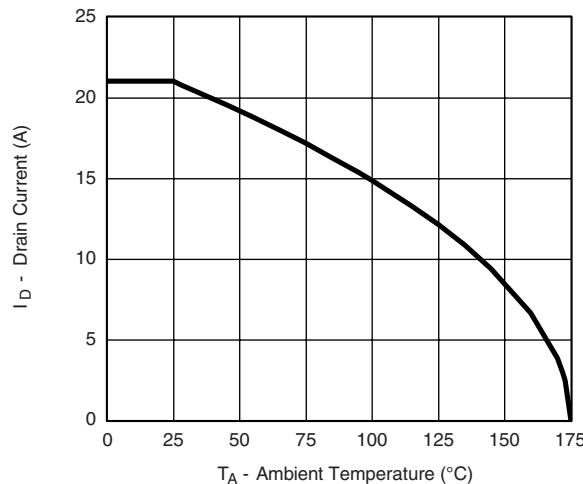
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

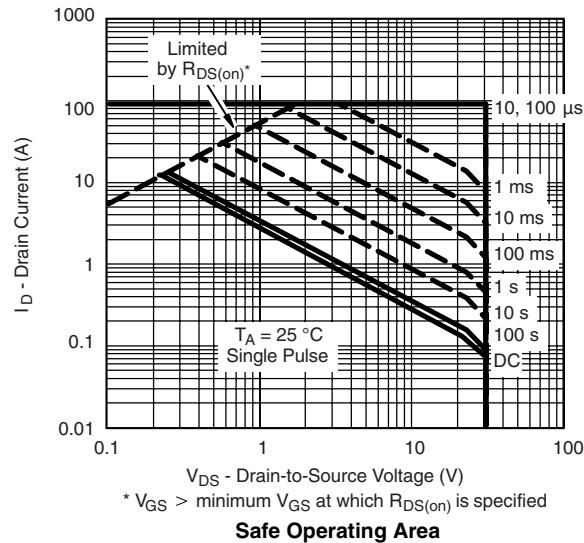
TYPICAL CHARACTERISTICS 25°C , unless otherwise noted


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


Thermal Ratings

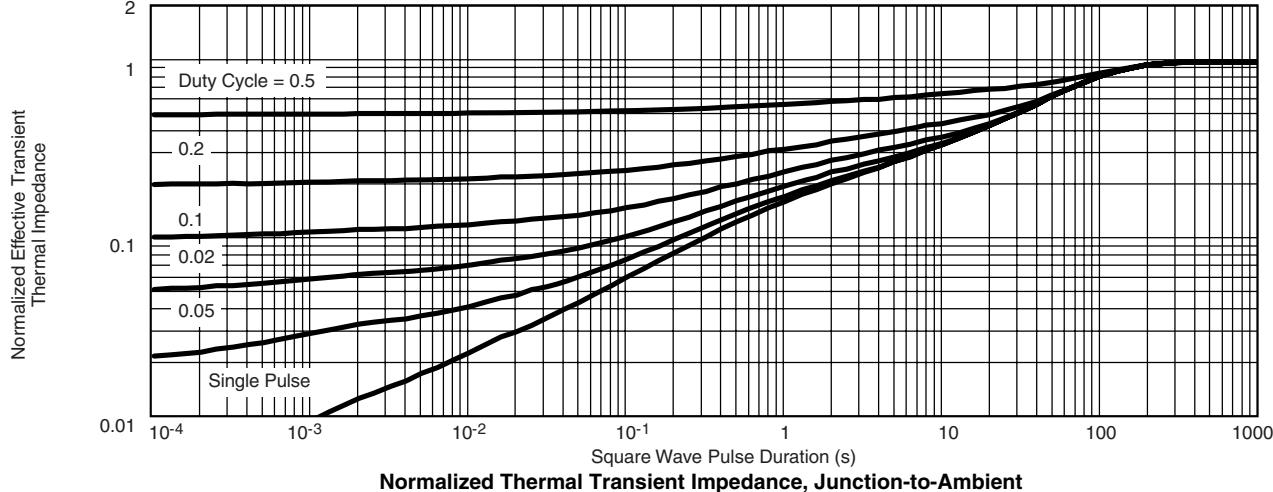


Maximum Drain Current vs. Ambient Temperature

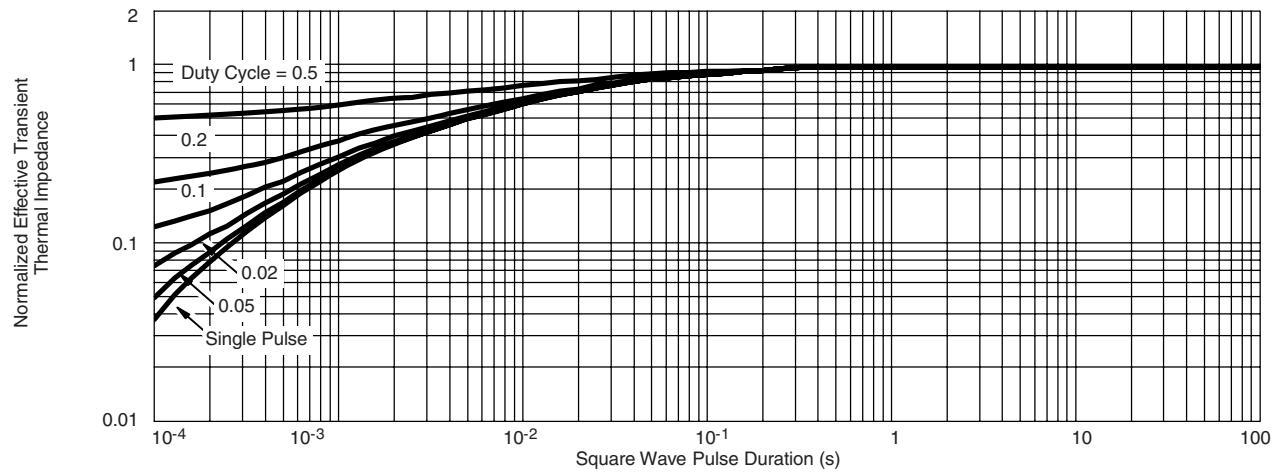


* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Normalized Thermal Transient Impedance, Junction-to-Ambient



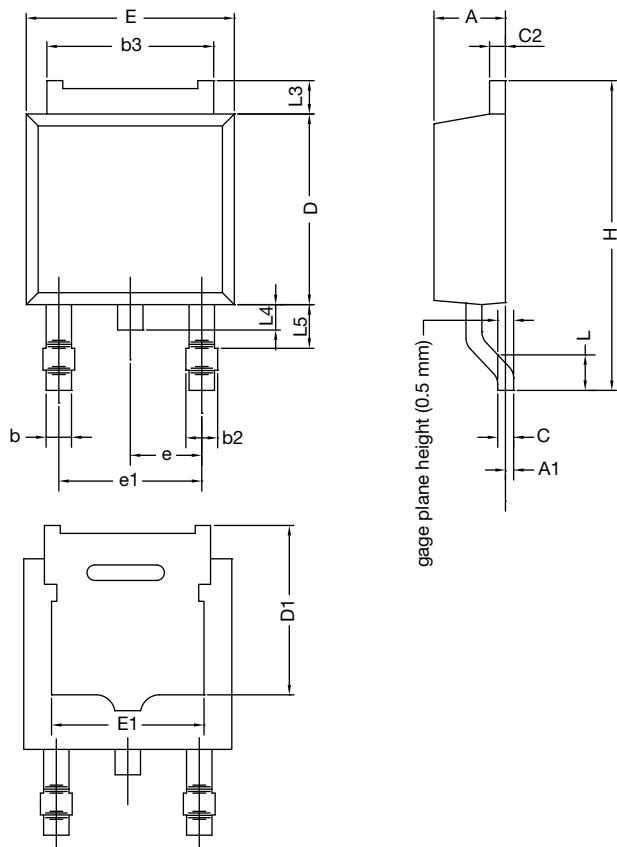
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?71856>.

TO-252AA Case Outline



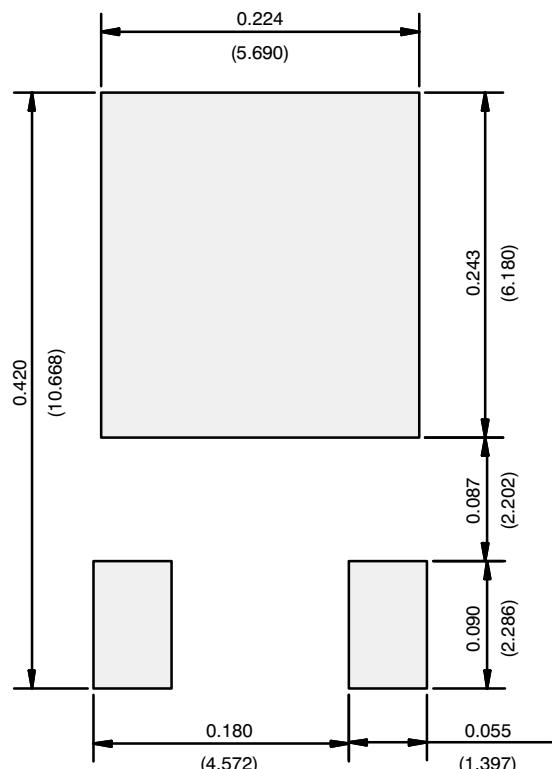
| DIM. | MILLIMETERS | | INCHES | |
|------|-------------|-------|-----------|-------|
| | MIN. | MAX. | MIN. | MAX. |
| A | 2.18 | 2.38 | 0.086 | 0.094 |
| A1 | - | 0.127 | - | 0.005 |
| b | 0.64 | 0.88 | 0.025 | 0.035 |
| b2 | 0.76 | 1.14 | 0.030 | 0.045 |
| b3 | 4.95 | 5.46 | 0.195 | 0.215 |
| C | 0.46 | 0.61 | 0.018 | 0.024 |
| C2 | 0.46 | 0.89 | 0.018 | 0.035 |
| D | 5.97 | 6.22 | 0.235 | 0.245 |
| D1 | 4.10 | - | 0.161 | - |
| E | 6.35 | 6.73 | 0.250 | 0.265 |
| E1 | 4.32 | - | 0.170 | - |
| H | 9.40 | 10.41 | 0.370 | 0.410 |
| e | 2.28 BSC | | 0.090 BSC | |
| e1 | 4.56 BSC | | 0.180 BSC | |
| L | 1.40 | 1.78 | 0.055 | 0.070 |
| L3 | 0.89 | 1.27 | 0.035 | 0.050 |
| L4 | - | 1.02 | - | 0.040 |
| L5 | 1.01 | 1.52 | 0.040 | 0.060 |

ECN: T16-0236-Rev. P, 16-May-16
DWG: 5347

Notes

- Dimension L3 is for reference only.

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



[Return to Index](#)

APPLICATION NOTE

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[Vishay](#):

[SUD50N03-09P-GE3](#) [SUD50N03-09P-E3](#)