

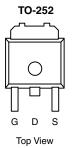
P-Channel 60 V (D-S), 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
- 60	0.015 at $V_{GS} = -10 \text{ V}$	- 50 ^d		
	0.020 at V _{GS} = - 4.5 V	- 50		

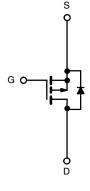
FEATURES

- TrenchFET® Power MOSFET
- 175 °C Junction Temperature
- Compliant to RoHS Directive 2002/95/EC





Drain Connected to Tab



Ordering Information: SUD50P06-15L-E3 (Lead-(Pb)-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $(T_A =$	= 25 °C, unless othe	rwise noted)			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V_{DS}	- 60	V	
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current (T _J = 175 °C)	T _C = 25 °C	- I _D	- 50 ^d	^	
Continuous Diam Current (1) = 175 C)	T _C = 125 °C		- 39		
Pulsed Drain Current		I _{DM}	- 80	А	
Avalanche Current		I _{AR}	- 50		
Repetitive Avalanche Energy ^a	L = 0.1 mH	E _{AR}	125	mJ	
Power Dissipation	T _C = 25 °C	P _D	136 ^c	W	
Power Dissipation	T _A = 25 °C	' D	3 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Lucation to Ambient	t ≤ 10 s	В	15	18	
Junction-to-Ambient ^b	Steady State	R_{thJA}	40	50	°C/W
Junction-to-Case	tion-to-Case		0.82	1.1	

Notes:

- a. Duty cycle \leq 1 %.
- b. When mounted on 1" square PCB (FR-4 material).
- c. See SOA curve for voltage derating.
- d. Package limited.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = - 48 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 48 V, V _{GS} = 0 V, T _J = 125 °C			- 50	μΑ	
		V _{DS} = - 48 V, V _{GS} = 0 V, T _J = 175 °C			- 150		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 50			Α	
Drain-Source On-State Resistance ^a		V _{GS} = - 10 V, I _D = - 17 A		0.012	0.015	- Ω	
	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 50 A, T _J = 125 °C			0.025		
		$V_{GS} = -10 \text{ V}, I_D = -50 \text{ A}, T_J = 175 ^{\circ}\text{C}$			0.030		
		V _{GS} = - 4.5 V, I _D = - 14 A			0.020		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 17 A		61		S	
Dynamic ^b							
Input Capacitance	C _{iss}			4950		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = -25 \text{ V}, f = 1 \text{ MHz}$		480			
Reverse Transfer Capacitance	C _{rss}			405			
Total Gate Charge ^c	Q_g			110	165		
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -50 \text{ A}$		19		nC	
Gate-Drain Charge ^c	Q _{gd}			28			
Turn-On Delay Time ^c	t _{d(on)}			15	23		
Rise Time ^c	t _r	$V_{DD} = -30 \text{ V}, R_L = 0.6 \Omega$ $I_D \cong -50 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 6 \Omega$		70	105	ns	
Turn-Off Delay Time ^c	t _{d(off)}			175	260		
Fall Time ^c	t _f			175	260		
Source-Drain Diode Ratings and Cha	aracteristics (T _C = 25 °C) ^b					
Continuous Current	Is				- 50	^	
Pulsed Current	I _{SM}				- 80	Α	
Forward Voltage ^a	V_{SD}	I _F = - 50 A, V _{GS} = 0 V		1.0	1.6	V	
Reverse Recovery Time	t _{rr}	I _F = - 50 A, dI/dt = 100 A/μs		45	70	ns	

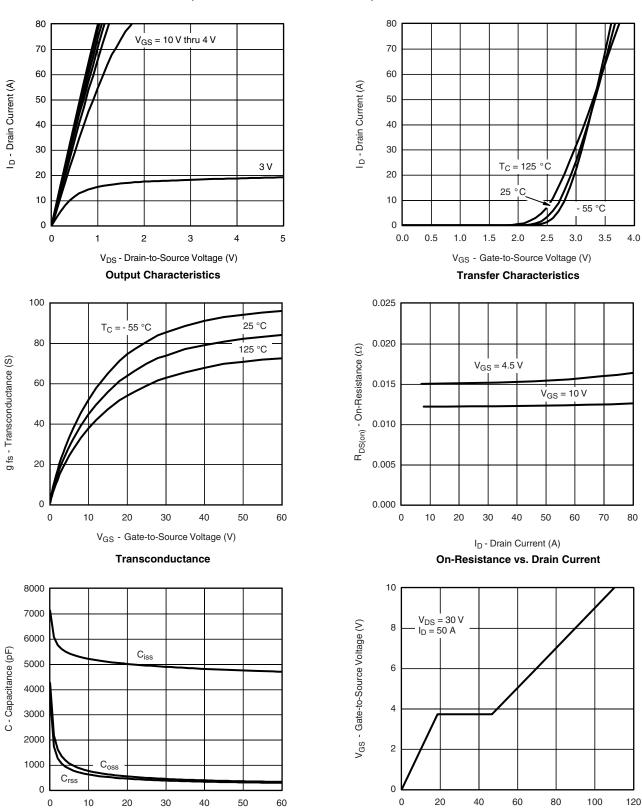
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



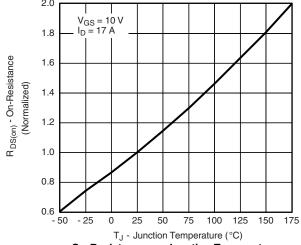
 V_{DS} - Drain-to-Source Voltage (V) $\label{eq:capacitance}$ Q_g - Total Gate Charge (nC)

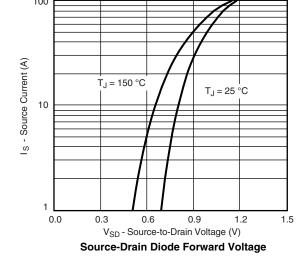
Gate Charge

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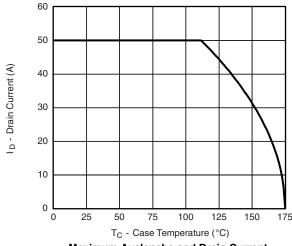
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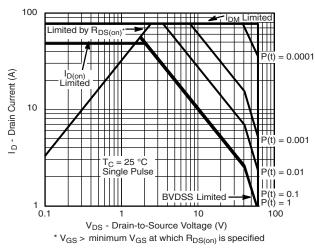




On-Resistance vs. Junction Temperature

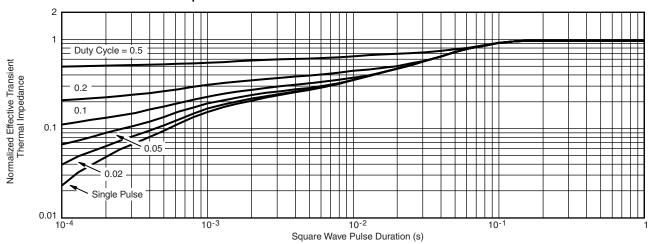
THERMAL RATINGS





Maximum Avalanche and Drain Current vs. Case Temperature

Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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