



P-Channel 60-V (D-S) MOSFET

| PRODUCT SUMMARY | | | | |
|---------------------|-------------------------------------|---------------------------------|-----------------------|--|
| V _{DS} (V) | $R_{DS(on)}(\Omega)$ | I _D (A) ^a | Q _g (Typ.) | |
| - 60 | 0.0195 at V _{GS} = - 10 V | - 53 | 76 nC | |
| - 60 | 0.0250 at V _{GS} = - 4.5 V | - 42 | 70110 | |

FEATURES

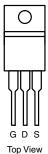
TrenchFET® Power MOSFET

100 % UIS Tested

Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



TO-220AB



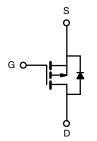
DRAIN connected to TAB

Ordering Information: SUP53P06-20-E3 (Lead (Pb)-free)

SUP53P06-20-GE3 (Lead (Pb)-free and Halogen-free)

APPLICATIONS

· Load Switch



P-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS | (T _A = 25 °C, unle | ess otherwise no | ted) | | |
|--|-----------------------------------|------------------|--------------------|----|--|
| Parameter | Symbol | Limit | Unit | | |
| Drain-Source Voltage | | V _{DS} | - 60 | V | |
| Gate-Source Voltage | | V _{GS} | ± 20 | | |
| | T _C = 25 °C | | - 53 ^a | | |
| Continuous Drain Current (T,I = 150 °C) | T _C = 70 °C | l , [| - 46.8 | | |
| Continuous Diain Current (1 _J = 130 °C) | T _A = 25 °C | I _D | 9.2 ^b | A | |
| | T _A = 70 °C | | - 8.1 ^b | | |
| Pulsed Drain Current | | I _{DM} | - 150 | | |
| Avalanche Current Pulse | L = 0.1 mH | I _{AS} | - 45 | | |
| Single Pulse Avalanche Energy | L = 0.1 IIII1 | E _{AS} | 101 | mJ | |
| Continuous Source-Drain Diode Current | T _C = 25 °C | I. | 69 ^a | A | |
| Continuous Source-Diam Diode Current | T _A = 25 °C | I _S | 2.1 ^b | A | |
| | T _C = 25 °C | | 104.2 ^a | | |
| Maximum Power Dissipation | T _C = 70 °C | P _D | 66.7 ^a | w | |
| | T _A = 25 °C | 'D | 3.1 ^b | VV | |
| | T _A = 70 °C | | 2 ^b | | |
| Operating Junction and Storage Temperature Ra | T _J , T _{stg} | - 55 to 150 | °C | | |

| THERMAL RESISTANCE RATINGS | | | | | | |
|--|--------------|-------------------|---------|------|------|--|
| Parameter | Symbol | Typical | Maximum | Unit | | |
| Maximum Junction-to-Ambient ^b | Steady State | R _{thJA} | 33 | 40 | °C/W | |
| Maximum Junction-to-Case | Steady State | R _{thJC} | 0.98 | 1.2 | C/VV | |

a. Based on T_C = 25 $^{\circ}C.$

b. Surface mounted on 1" x 1" FR4 board.

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| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|-------------------------|---|-------|---------|--------|-------|--|
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | V_{DS} | $V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$ | - 60 | | | V | |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_{J}$ | I _D = - 250 μA | | 68 | | mV/°C | |
| V _{GS(th)} Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | i _D = - 250 μA | | - 5.2 | | | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ | - 1 | | - 3 | V | |
| Gate-Source Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | | | ± 100 | nA | |
| Zoro Coto Voltogo Droin Current | l | V _{DS} = - 60 V, V _{GS} = 0 V | | | - 1 | | |
| Zero Gate Voltage Drain Current | IDSS | $V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$ | | - 10 μA | | μΑ | |
| On-State Drain Current ^a | I _{D(on)} | V _{DS} = - 5 V, V _{GS} = - 10 V | - 120 | | | Α | |
| Dunin Course On Chata Basistanas | B | V _{GS} = - 10 V, I _D = - 30 A | | 0.0160 | 0.0195 | | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | $V_{GS} = -4.5 \text{ V}, I_D = -20 \text{ A}$ | | 0.0200 | 0.0250 | Ω | |
| Forward Transconductance ^a | 9 _{fs} | $V_{DS} = -15 \text{ V}, I_{D} = -50 \text{ A}$ | 20 | | | S | |
| Dynamic ^b | | | | | | | |
| Input Capacitance | C _{iss} | | | 3500 | | pF | |
| Output Capacitance | C _{oss} | $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ | | 390 | | | |
| Reverse Transfer Capacitance | C _{rss} | | | 290 | | | |
| Total Gate Charge | Qg | $V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -55 \text{ A}$ | | 76 | 115 | | |
| Total date charge | | | | 38 | 60 | nC | |
| Gate-Source Charge | Q_{gs} | $V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -55 \text{ A}$ | | 16 | | | |
| Gate-Drain Charge | Q_{gd} | | | 19 | | | |
| Gate Resistance | R_g | f = 1 MHz | | 5.2 | | Ω | |
| Turn-On Delay Time | t _{d(on)} | | | 10 | 15 | | |
| Rise Time | t _r | V_{DD} = - 2 V, R_L = 2 Ω | | 7 | 15 | ns | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong$ - 10 A, V_{GEN} = - 10 V, R_g = 1 Ω | | 70 | 110 | | |
| Fall Time | t _f | | | 40 | 60 | | |
| Drain-Source Body Diode Characteristic | s | | | | | | |
| Continuous Source-Drain Diode Current | I _S | $T_C = 25 ^{\circ}C$ | | | - 69 | | |
| Pulse Diode Forward Current ^a | I _{SM} | | | | - 150 | 50 A | |
| Body Diode Voltage | V_{SD} | I _S = - 30 A | | - 1 | - 1.5 | V | |
| Body Diode Reverse Recovery Time | t _{rr} | | | 45 | 68 | ns | |
| Body Diode Reverse Recovery Charge | Q _{rr} | I _F = - 50 A, di/dt = 100 A/μs, T _{.I} = 25 °C | | 59 | 120 | nC | |
| Reverse Recovery Fall Time | t _a | $I_F = -30 \text{ A}$, $I_J = 25 \text{ °C}$ | | 29 | | 1 | |
| Reverse Recovery Rise Time | t _b | 7 | | 16 | | ns | |

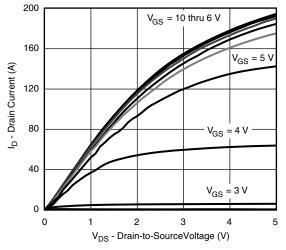
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

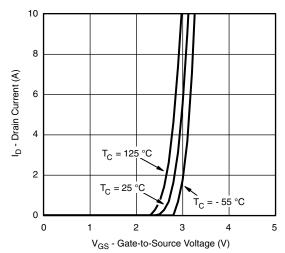
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



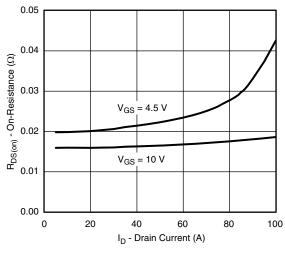
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



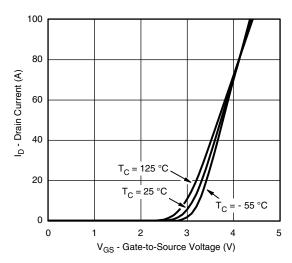
Output Characteristics



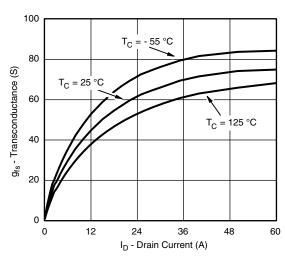
Transfer Characteristics



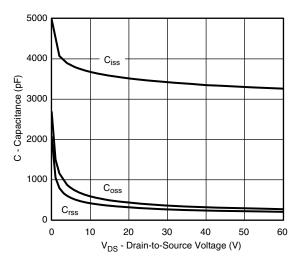
On-Resistance vs. Drain Current



Transfer Characteristics



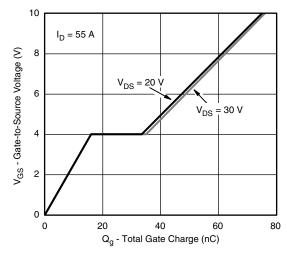
Transconductance



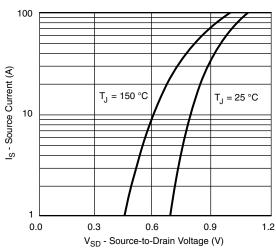
Capacitance

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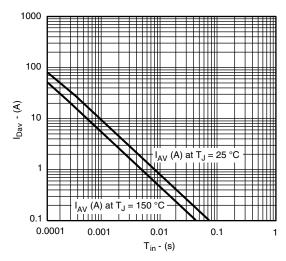
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



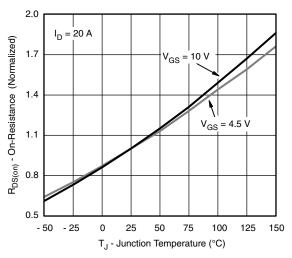




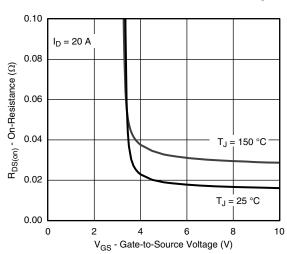
Source-Drain Diode Forward Voltage



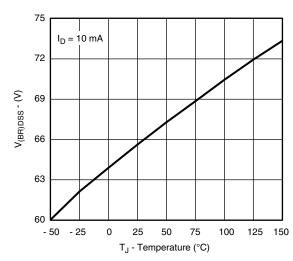
Single Pulse Avalanche Current Capability vs. Time



On-Resistance vs. Gate-to-Source Voltage



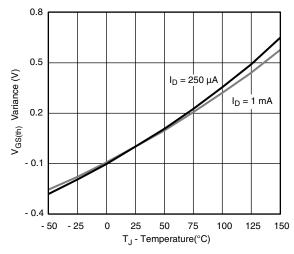
On-Resistance vs. Gate-to-Source Voltage

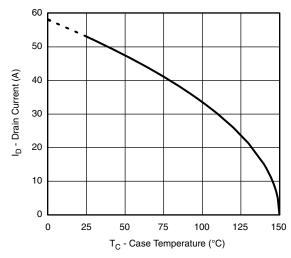


Drain-Source Breakdown Voltage vs. Junction Temperature

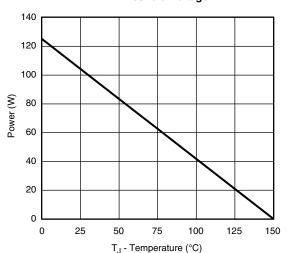


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

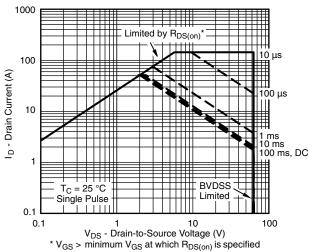




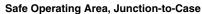
Threshold Voltage

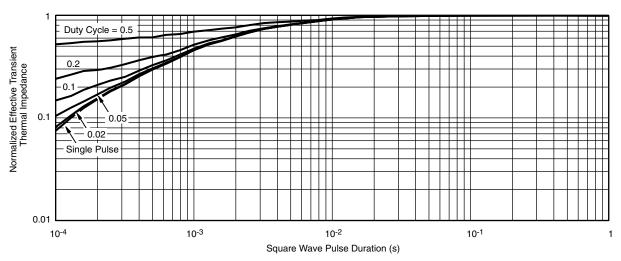


Max. Drain Current vs. Case Temperature



Power Derating, Junction-to-Case





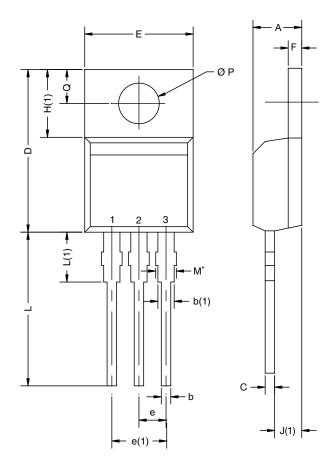
Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?68633.





TO-220AB



| | D2 |
|--|----|
| | |
| | |

| | MILLIMETERS | | INC | HES |
|--|-------------|-------|-------|-------|
| DIM. | MIN. | MAX. | MIN. | MAX. |
| А | 4.25 | 4.65 | 0.167 | 0.183 |
| b | 0.69 | 1.01 | 0.027 | 0.040 |
| b(1) | 1.20 | 1.73 | 0.047 | 0.068 |
| С | 0.36 | 0.61 | 0.014 | 0.024 |
| D | 14.85 | 15.49 | 0.585 | 0.610 |
| D2 | 12.19 | 12.70 | 0.480 | 0.500 |
| Е | 10.04 | 10.51 | 0.395 | 0.414 |
| е | 2.41 | 2.67 | 0.095 | 0.105 |
| e(1) | 4.88 | 5.28 | 0.192 | 0.208 |
| F | 1.14 | 1.40 | 0.045 | 0.055 |
| H(1) | 6.09 | 6.48 | 0.240 | 0.255 |
| J(1) | 2.41 | 2.92 | 0.095 | 0.115 |
| L | 13.35 | 14.02 | 0.526 | 0.552 |
| L(1) | 3.32 | 3.82 | 0.131 | 0.150 |
| ØΡ | 3.54 | 3.94 | 0.139 | 0.155 |
| Q | 2.60 | 3.00 | 0.102 | 0.118 |
| ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471 | | | | |

Note

 $^{^{\}star}$ M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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