

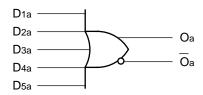
FEATURES

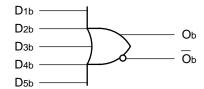
- Max. propagation delay of 750ps
- IEE min. of -25mA
- Industry standard 100K ECL levels
- Extended supply voltage option: VEE = -4.2V to -5.5V
- Voltage and temperature compensation for improved noise immunity
- 20% faster than Fairchild 300K at lower power
- Internal 75k Ω input pull-down resistors
- Function and pinout compatible with Fairchild F100K
- Available in 28-pin PLCC package

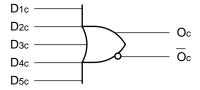
DESCRIPTION

The SY100S301 is an ultra-fast triple 5-input OR/NOR gate designed for use in high-performance ECL systems. The inputs on this device have $75k\Omega$ pull-down resistors.

BLOCK DIAGRAM





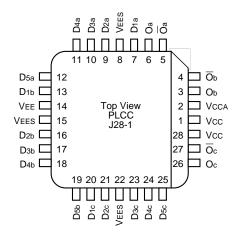


PIN NAMES

Pin	Function
Dna, Dnb, Dnc	Data Inputs (n-15)
\overline{O} a, \overline{O} b, \overline{O} c	Data Outputs
Oa, Ob, Oc	Complementary Data Outputs
VEES	VEE Substrate
VCCA	Vcco for ECL Outputs

Micrel, Inc. SY100S301

PACKAGE/ORDERING INFORMATION



28-Pin PLCC (J28-1)

Ordering Information

Part Number	Package Type	Operating Range	Package Marking	Lead Finish
SY100S301JC	J28-1	Commercial	SY100S301JC	Sn-Pb
SY100S301JCTR ⁽¹⁾	J28-1	Commercial	SY100S301JC	Sn-Pb
SY100S301JZ ⁽²⁾	J28-1	Commercial	SY100S301JZ with Pb-Free bar-line indicator	Matte-Sn
SY100S301JZTR ^(1, 2)	J28-1	Commercial	SY100S301JZ with Pb-Free bar-line indicator	Matte-Sn

Notes:

- 1. Tape and Reel.
- 2. Pb-Free package is recommended for new designs.

LOGIC EQUATION

Oa = D1a + D2a + D3a + D4a + D5a

Ob = D1b + D2b + D3b + D4a + D5b

 $O_c = D_{1c} + D_{2c} + D_{3c} + D_{4c} + D_{5c}$

GUARANTEED OPERATING CONDITIONS

Symbol	Parameter	Min.	Тур.	Max.	Unit
VEE	Input HIGH Current	-5.5	-4.5	-4.2	V
Та	Power Supply Current	0	25	85	°C

ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Rating	Value	Unit
VEE	Power Supply	-0.5 to +7.0	V
VIN	Input Voltage	–0.5 to VEE	V
lout	DC Output Current	- 50	mA
Тс	Temperature Under Bias	-55 to +125	°C
TJ	Junction Temperature	+150	°C
TLEAD	Lead Temperature (soldering, 20 sec.)	+260	°C
Tstore	Storage Temperature	-65 to +150	°C

Note:

 Permanent device damage may occur if absolute maximum ratings are exceeded. This is a stress rating only and functional operation is not implied at conditions other than those detailed in the operational sections of this data book. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

VEE = -4.2V to -5.5V unless otherwise specified, VCC = VCCA = GND

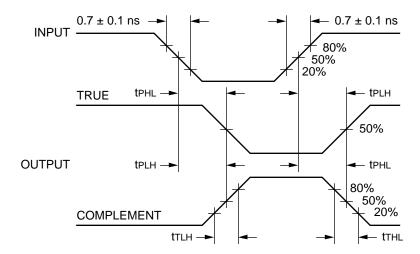
Symbol	Parameter	Min.	Тур.	Max.	Unit	Condition
IIH	Input HIGH Current	_	-	200	μΑ	_
lee	Power Supply Current	-25	-17	-11	mA	Inputs Open

AC ELECTRICAL CHARACTERISTICS

VEE = -4.2V to -5.5V unless otherwise specified, VCC = VCCA = GND

		TA = 0°C		TA = +25°C		TA = +85°C			
Symbol	Parameter	Min.	Max.	Min.	Max.	Min.	Max.	Unit	Condition
tPLH tPHL	Propagation Delay Data to Output	300	750	300	750	300	750	ps	
tTLH tTHL	Transition Time 20% to 80%, 80% to 20%	300	900	300	900	300	900	ps	

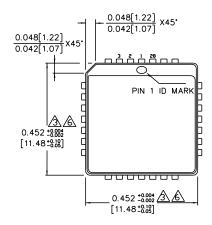
TIMING DIAGRAM



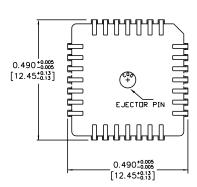
Propagation Delay and Transition Times

SY100S301 Micrel, Inc.

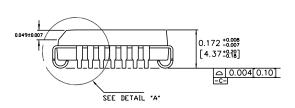
28-PIN PLCC (J28-1)



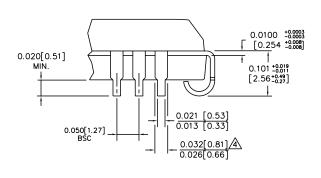
TOP VIEW



BOTTOM VIEW



SIDE VIEW



DETAIL "A"

Rev. A

DIMENSIONS ARE IN INCHES [MM]. CONTROLLING DIMENSION: INCHES.

<u> 4.</u>

CONTROLLING DIMENSION: INCHES.
DIMENSION DOES NOT INCLUDE MOLD FLASH
OR PROTRUSIONS, EITHER OF WHICH SHALL NOT
EXCEED 0.008 [0.203].
LEAD DIMENSION DOES NOT INCLUDE DAMBAR
PROTRUSION.
MAXIMUM AND MINIMUM SPECIFICATIONS ARE
INDICATED AS FOLLOWS: MAX/MIN
PACKAGE TOP DIMENSION MAY BE SLIGHTLY
SMALLER THAN BOTTOM DIMENSION.

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