TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

## TC74AC540P,TC74AC540F,TC74AC540FT TC74AC541P,TC74AC541F,TC74AC541FT

Octal Bus Buffer

TC74AC540P/F/FT Inverting, 3-State

Outputs

TC74AC541P/F/FT Non-Inverting, 3-State

Outputs

The TC74AC540/TC74AC541 are advanced high speed CMOS OCTAL BUS BUFFERs fabricated with silicon gate and double-layer metal wiring C<sup>2</sup>MOS technology.

They achieve the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

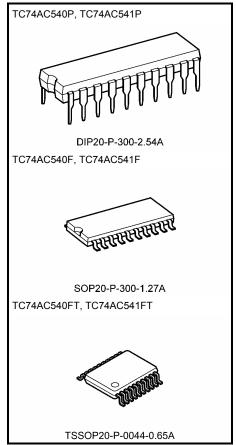
The TC74AC540 is an inverting type, and the TC74AC541 is a non-inverting type.

When either  $\overline{G}1$  or  $\overline{G}2$  are high, the terminal outputs are in the high-impedance state.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

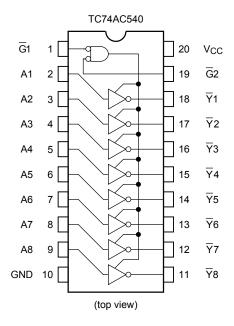
- High speed:  $t_{pd} = 4.0 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $ICC = 8 \mu A \text{ (max)}$  at  $Ta = 25^{\circ}C$
- High noise immunity: V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)
- Symmetrical output impedance:  $|I_{OH}| = I_{OL} = 24$  mA (min) Capability of driving 50  $\Omega$  transmission lines.
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range:  $V_{CC \text{ (opr)}} = 2 \text{ to } 5.5 \text{ V}$
- Pin and function compatible with 74F540/541

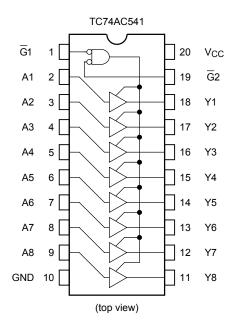


Weight

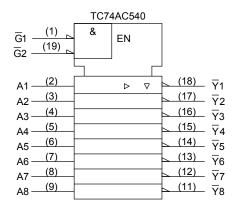
DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.) TSSOP20-P-0044-0.65A : 0.08 g (typ.)

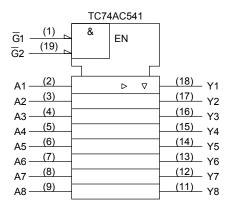
### **Pin Assignment**





### **IEC Logic Symbol**





#### **Truth Table**

	Inputs	Outputs				
G1	G2	A <sub>n</sub>	Yn	$\overline{Y}_{n}$		
Н	Х	Х	Z	Z		
Х	Н	Х	Z	Z		
L	L	Н	Н	L		
L	L	L	L	Н		

X: Don't care

Z: High impedance

Y<sub>n</sub>: AC541

 $\overline{Y}_n$ : AC540



### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	−0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V <sub>CC</sub> /ground current	Icc	±200	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T <sub>stg</sub>	−65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = −40 to 65°C. From Ta = 65 to 85°C a derating factor of −10 mW/°C should be applied up to 300 mW.

## **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2.0 to 5.5	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	−40 to 85	°C
Input rise and fall time	dt/dV	0 to 100 (V <sub>CC</sub> = 3.3 ± 0.3 V)	ns/V
input noc and fail time	αναν	0 to 20 (V <sub>CC</sub> = 5 ± 0.5 V)	115/ V

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.



### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test Condition VCC (V)		Ta = 25°C			Ta = −40 to 85°C		Unit		
Characteristics	Symbol			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic	
		-		2.0	1.50	_	_	1.50	_	V	
High-level input voltage	$V_{IH}$			3.0	2.10	_	_	2.10	_		
				5.5	3.85	_	1	3.85	1		
		_		2.0	_	_	0.50	_	0.50		
Low-level input voltage	$V_{IL}$			3.0	_	_	0.90	_	0.90	V	
1 11 9					5.5	1	_	1.65	_	1.65	
						1.9	2.0	_	1.9	_	
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -50 μA		3.0	2.9	3.0	_	2.9	_	٧
High-level output	Vou				4.5	4.4	4.5	_	4.4	_	
voltage	Vон		I <sub>OH</sub> = -4 mA		3.0	2.58	_	_	2.48	_	
			I <sub>OH</sub> = −24 mA		4.5	3.94	_	_	3.80	_	
			I <sub>OH</sub> = -75 mA	(Note)	5.5	1	_	1	3.85	-	
		VIN = V <sub>IH</sub> or VIL			2.0	_	0.0	0.1	_	0.1	- v
			I <sub>OL</sub> = 50 μA		3.0	_	0.0	0.1	_	0.1	
Low-level output	V <sub>OL</sub>				4.5	_	0.0	0.1	_	0.1	
voltage	VOL		I <sub>OL</sub> = 12 mA		3.0	_	_	0.36	_	0.44	
			I <sub>OL</sub> = 24 mA		4.5	_	_	0.36	_	0.44	
			I <sub>OL</sub> = 75 mA	(Note)	5.5	_	_	_	_	1.65	
3-state output off-state current	I <sub>OZ</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> V <sub>OUT</sub> = V <sub>CC</sub> or GND		5.5	_	_	±0.5	_	±5.0	μA	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	±0.1	_	±1.0	μA	
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5		_	8.0	_	80.0	μA	

Note: This spec indicates the capability of driving 50  $\Omega$  transmission lines.

One output should be tested at a time for a 10 ms maximum duration.



### AC Characteristics ( $C_L = 50 \text{ pF}, R_L = 500 \Omega, \text{ input: } t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
	- <b>,</b>		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
Propagation delay	t <sub>pLH</sub>		3.3 ± 0.3	_	6.8	10.5	1.0	12.0	no
time (Note 2)	$t_{pHL}$	_	5.0 ± 0.5	_	4.7	7.0	1.0	8.0	ns
Propagation delay	t <sub>pLH</sub>		3.3 ± 0.3	_	6.8	11.4	1.0	13.0	ns
time (Note 3)	$t_{pHL}$	_	5.0 ± 0.5	_	4.7	7.5	1.0	8.5	
Output anable time	t <sub>pZL</sub>	_	3.3 ± 0.3	_	9.6	15.8	1.0	18.0	ns
Output enable time	$t_{pZH}$		5.0 ± 0.5	_	6.4	10.0	1.0	11.4	
Output disable time	t <sub>pLZ</sub>		3.3 ± 0.3	_	7.7	12.3	1.0	14.0	ns
	$t_{pHZ}$	_	5.0 ± 0.5	_	6.4	9.2	1.0	10.5	
Input capacitance	C <sub>IN</sub>	_		_	5	10	_	10	pF
Output capacitance	C <sub>OUT</sub>	_		_	10	_	_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	TC74AC540		_	25	_	_	_	~F
	(Note 1)	TC74AC541		_	28	_	_	_	pF

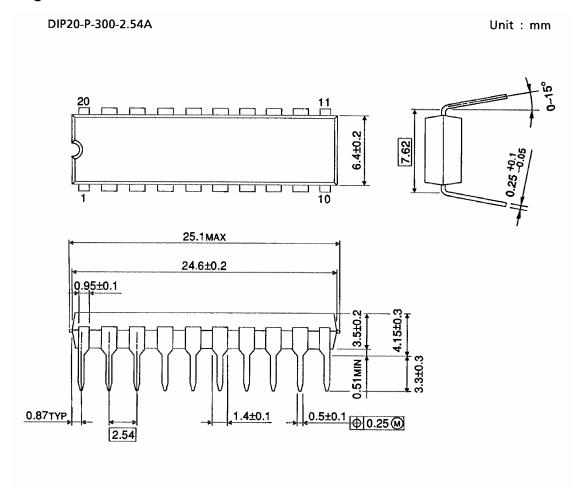
Note1: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$ 

Note 2: For TC74AC540 only Note 3: For TC74AC541 only

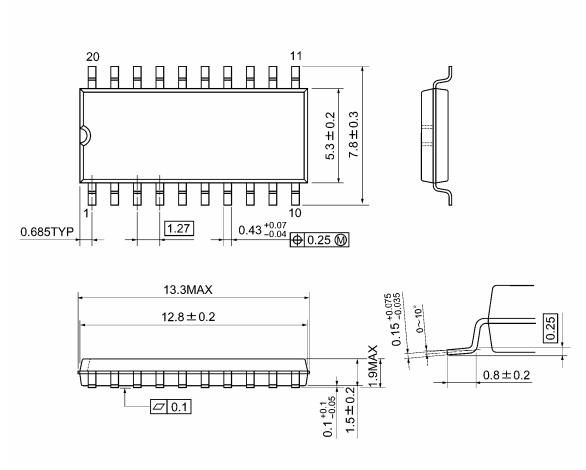
# **Package Dimensions**



Weight: 1.30 g (typ.)

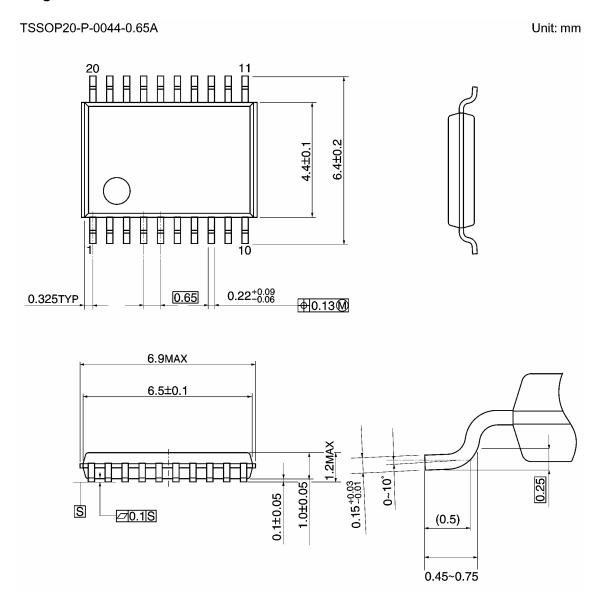
# **Package Dimensions**

SOP20-P-300-1.27A Unit: mm



Weight: 0.22 g (typ.)

## **Package Dimensions**



Weight: 0.08 g (typ.)

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20070701-EN GENERAL

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