

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

**TC74LCX08F,TC74LCX08FT,TC74LCX08FK**

Low-Voltage Quad 2-Input AND Gate with 5-V Tolerant Inputs and Outputs

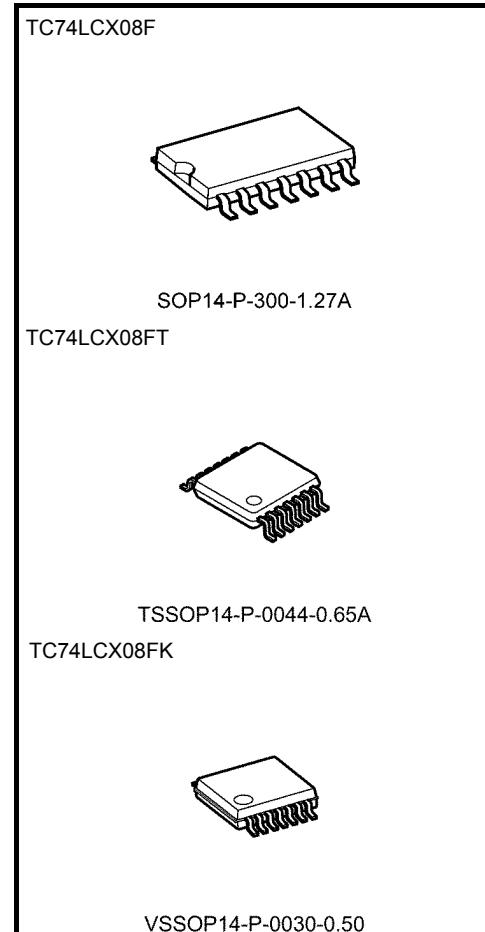
The TC74LCX08 is a high-performance CMOS 2-input AND gate. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

All inputs are equipped with protection circuits against static discharge.

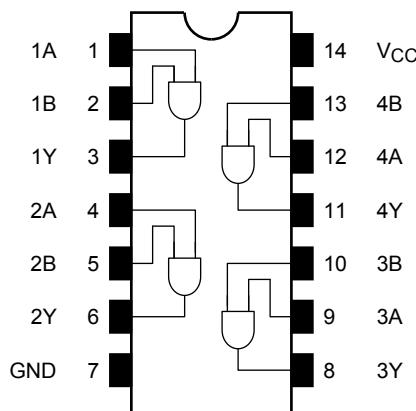
**Features**

- Low-voltage operation:  $V_{CC} = 2.0$  to  $3.6$  V
- High-speed operation:  $t_{pd} = 5.5$  ns (max) ( $V_{CC} = 3.0$  to  $3.6$  V)
- Output current:  $|I_{OH}|/I_{OL} = 24$  mA (min) ( $V_{CC} = 3.0$  V)
- Latch-up performance:  $-500$  mA
- Available in JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series  
(74AC/VHC/HC/F/ALS/LS etc.) 08 type

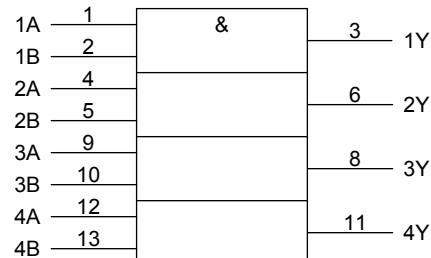
**Weight**

|                      |                 |
|----------------------|-----------------|
| SOP14-P-300-1.27A    | : 0.18 g (typ.) |
| TSSOP14-P-0044-0.65A | : 0.06 g (typ.) |
| VSSOP14-P-0030-0.50  | : 0.02 g (typ.) |

## Pin Assignment (top view)



## IEC Logic Symbol



## Truth Table

| Inputs |   | Outputs |
|--------|---|---------|
| A      | B | Y       |
| L      | L | L       |
| L      | H | L       |
| H      | L | L       |
| H      | H | H       |

## Absolute Maximum Ratings (Note 1)

| Characteristics             | Symbol           | Rating                          | Unit |
|-----------------------------|------------------|---------------------------------|------|
| Power supply voltage        | $V_{CC}$         | -0.5 to 7.0                     | V    |
| DC input voltage            | $V_{IN}$         | -0.5 to 7.0                     | V    |
| DC output voltage           | $V_{OUT}$        | -0.5 to 7.0 (Note 2)            | V    |
|                             |                  | -0.5 to $V_{CC} + 0.5$ (Note 3) |      |
| Input diode current         | $I_{IK}$         | -50                             | mA   |
| Output diode current        | $I_{OK}$         | $\pm 50$ (Note 4)               | mA   |
| DC output current           | $I_{OUT}$        | $\pm 50$                        | mA   |
| Power dissipation           | $P_D$            | 180                             | mW   |
| DC $V_{CC}$ /ground current | $I_{CC}/I_{GND}$ | $\pm 100$                       | mA   |
| Storage temperature         | $T_{stg}$        | -65 to 150                      | °C   |

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 2:  $V_{CC} = 0$  V

Note 3: High or low state.  $I_{OUT}$  absolute maximum rating must be observed.

Note 4:  $V_{OUT} < GND$ ,  $V_{OUT} > V_{CC}$

## Operating Ranges (Note 1)

| Characteristics          | Symbol                           | Rating                        | Unit |
|--------------------------|----------------------------------|-------------------------------|------|
| Power Supply voltage     | V <sub>CC</sub>                  | 2.0 to 3.6                    | V    |
|                          |                                  | 1.5 to 3.6 (Note 2)           |      |
| Input voltage            | V <sub>IN</sub>                  | 0 to 5.5                      | V    |
| Output voltage           | V <sub>OUT</sub>                 | 0 to 5.5 (Note 3)             | V    |
|                          |                                  | 0 to V <sub>CC</sub> (Note 4) |      |
| Output current           | I <sub>OH</sub> /I <sub>OL</sub> | ±24 (Note 5)                  | mA   |
|                          |                                  | ±12 (Note 6)                  |      |
| Operating temperature    | T <sub>opr</sub>                 | −40 to 85                     | °C   |
| Input rise and fall time | dt/dv                            | 0 to 10 (Note 7)              | ns/V |

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

Note 3:  $V_{CC} = 0$  V

Note 4: High or low state

Note 5:  $V_{CC} = 3.0$  to  $3.6$  V

Note 6:  $V_{CC} = 2.7$  to  $3.0$  V

Note 7:  $V_{IN} \equiv 0.8$  to  $2.0$  V,  $V_{CC} \equiv 3.0$  V

## Electrical Characteristics

### DC Characteristics (Ta = -40 to 85°C)

| Characteristics                       |         | Symbol           | Test Condition                                       |                           |   | V <sub>CC</sub> (V) | Min                   | Max   | Unit |  |
|---------------------------------------|---------|------------------|--|---------------------------|---|---------------------|-----------------------|-------|------|--|
| Input voltage                         | H-level | V <sub>IH</sub>  | —  |                           |   |                     |                       |       |      |  |
|                                       | L-level | V <sub>IL</sub>  | —  |                           |   | 2.7 to 3.6          | —                     | 0.8   | V    |  |
| Output voltage                        | H-level | V <sub>OH</sub>  | V <sub>IN</sub> = V <sub>IH</sub>                    | I <sub>OH</sub> = -100 µA | — | 2.7 to 3.6          | V <sub>CC</sub> - 0.2 | —     |      |  |
|                                       |         |                  |  | I <sub>OH</sub> = -12 mA  | — | 2.7                 | 2.2                   | —     |      |  |
|                                       |         |                  |  | I <sub>OH</sub> = -18 mA  | — | 3.0                 | 2.4                   | —     |      |  |
|                                       |         |                  |  | I <sub>OH</sub> = -24 mA  | — | 3.0                 | 2.2                   | —     |      |  |
|                                       | L-level | V <sub>OL</sub>  | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> | I <sub>OL</sub> = 100 µA  | — | 2.7 to 3.6          | —                     | 0.2   |      |  |
|                                       |         |                  |  | I <sub>OL</sub> = 12 mA   | — | 2.7                 | —                     | 0.4   |      |  |
|                                       |         |                  |  | I <sub>OL</sub> = 16 mA   | — | 3.0                 | —                     | 0.4   |      |  |
|                                       |         |                  |  | I <sub>OL</sub> = 24 mA   | — | 3.0                 | —                     | 0.55  |      |  |
| Input leakage current                 |         | I <sub>IN</sub>  | V <sub>IN</sub> = 0 to 5.5 V                         |                           |   | 2.7 to 3.6          | —                     | ±5.0  | µA   |  |
| Power-off leakage current             |         | I <sub>OFF</sub> | V <sub>IN</sub> /V <sub>OUT</sub> = 5.5 V            |                           |   | 0                   | —                     | 10.0  | µA   |  |
| Quiescent supply current              |         | I <sub>CC</sub>  | V <sub>IN</sub> = V <sub>CC</sub> or GND             |                           |   | 2.7 to 3.6          | —                     | 10.0  | µA   |  |
|                                       |         |                  | V <sub>IN</sub> = 3.6 to 5.5 V                       |                           |   | 2.7 to 3.6          | —                     | ±10.0 |      |  |
| Increase in I <sub>CC</sub> per input |         | ΔI <sub>CC</sub> | V <sub>IH</sub> = V <sub>CC</sub> - 0.6 V            |                           |   | 2.7 to 3.6          | —                     | 500   | —    |  |

### AC Characteristics ( $T_a = -40$ to $85^\circ\text{C}$ )

| Characteristics        | Symbol     | Test Condition     | V <sub>CC</sub> (V) | Min | Max | Unit |
|------------------------|------------|--------------------|---------------------|-----|-----|------|
|                        |            |                    | 2.7                 |     |     |      |
| Propagation delay time | $t_{pLH}$  | Figure 1, Figure 2 | 2.7                 | —   | 6.2 | ns   |
|                        | $t_{pHL}$  |                    | $3.3 \pm 0.3$       | 1.5 | 5.5 |      |
| Output to output skew  | $t_{osLH}$ | (Note)             | 2.7                 | —   | —   | ns   |
|                        | $t_{osHL}$ |                    | $3.3 \pm 0.3$       | —   | 1.0 |      |

Note: Parameter guaranteed by design.

$$(t_{\text{osI}} \text{ H} = |t_{\text{pI}} \text{ Hm} - t_{\text{pI}} \text{ Hn}|, t_{\text{osHI}} = |t_{\text{pHI}} \text{ m} - t_{\text{pHI}} \text{ n}|)$$

**Dynamic Switching Characteristics (Ta = 25°C, input:  $t_r = t_f = 2.5$  ns,  $C_L = 50$  pF,  $R_L = 500$  Ω)**

| Characteristics                              | Symbol           | Test Condition                                 | V <sub>CC</sub> (V) | Typ. | Unit |
|--|------------------|--|---------------------|------|------|
|  |                  |  |                     |      |      |
| Quiet output maximum dynamic V <sub>OL</sub> | V <sub>OLP</sub> | V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V | 3.3                 | 0.8  | V    |
| Quiet output minimum dynamic V <sub>OL</sub> | V <sub>OLV</sub> | V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V | 3.3                 | 0.8  | V    |

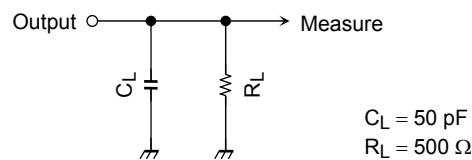
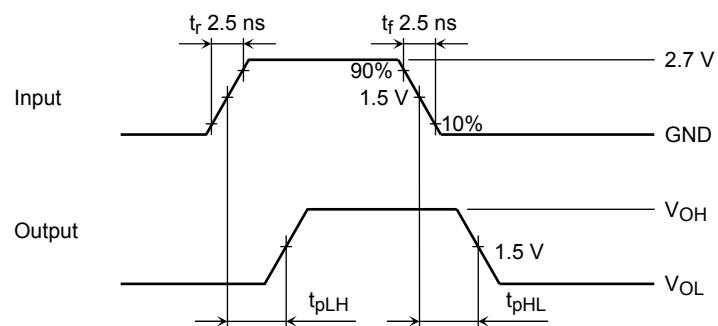
### Capacitive Characteristics ( $T_a = 25^\circ\text{C}$ )

| Characteristics               | Symbol           | Test Condition           | V <sub>CC</sub> (V) | Typ. | Unit |    |
|-------------------------------|------------------|--------------------------|---------------------|------|------|----|
|                               |                  |                          |                     |      |      |    |
| Input capacitance             | C <sub>IN</sub>  | —                        | 3.3                 | 7    | pF   |    |
| Output capacitance            | C <sub>OUT</sub> | —                        | 0                   | 8    | pF   |    |
| Power dissipation capacitance | C <sub>PD</sub>  | f <sub>IN</sub> = 10 MHz | (Note)              | 3.3  | 25   | pF |

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

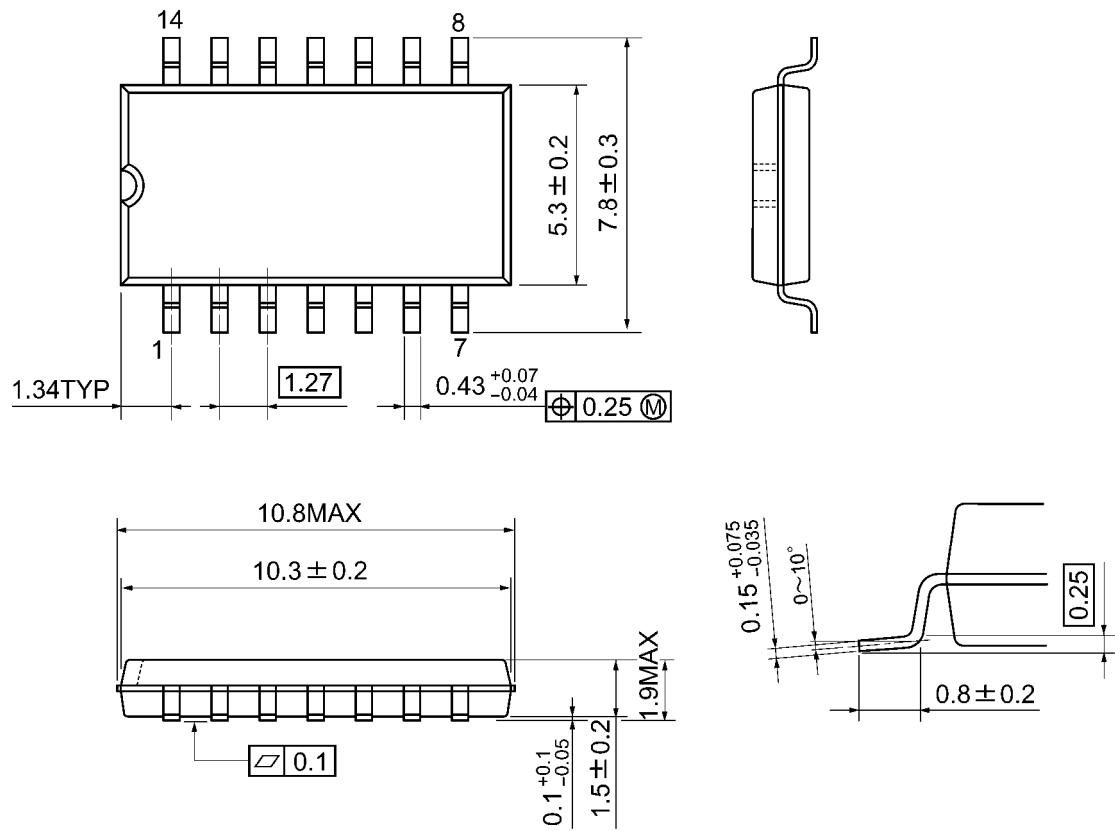
$$I_{CC(\text{opr})} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$$

**AC Test Circuit****Figure 1****AC Waveform****Figure 2  $t_{pLH}$ ,  $t_{pHL}$**

**Package Dimensions**

SOP14-P-300-1.27A

Unit: mm

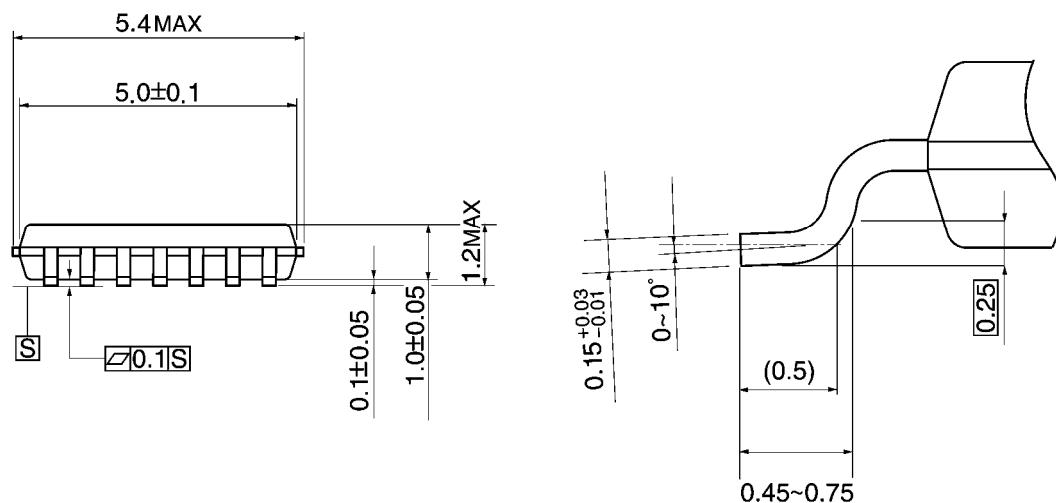
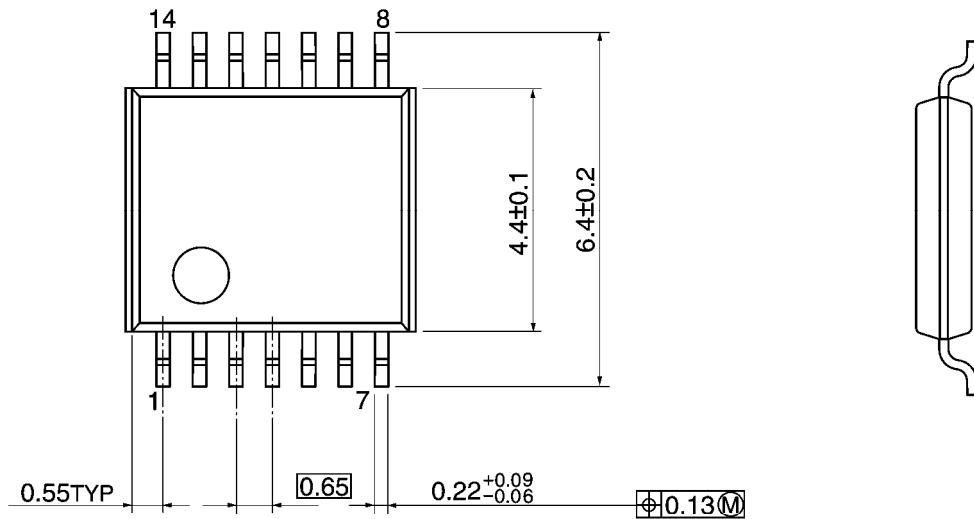


Weight: 0.18 g (typ.)

## Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm

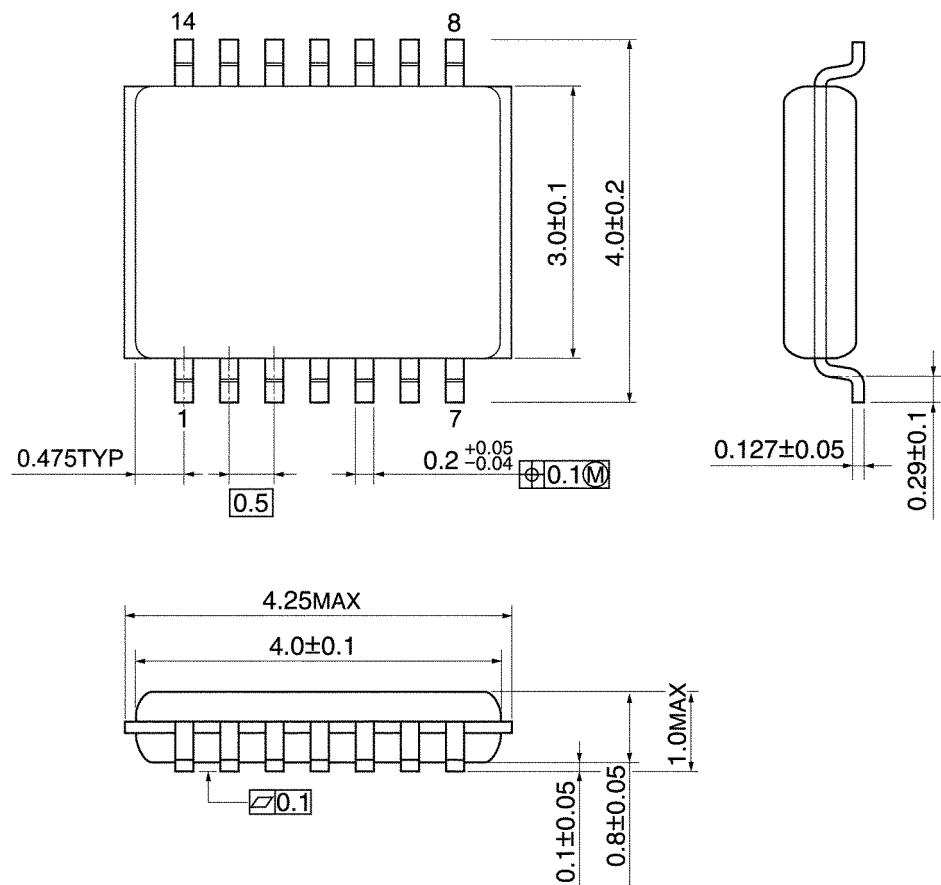


Weight: 0.06 g (typ.)

**Package Dimensions**

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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